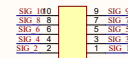
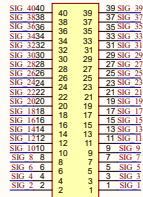
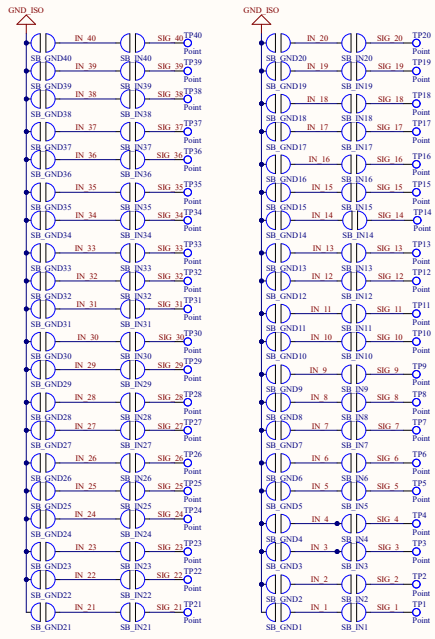


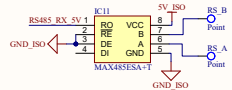
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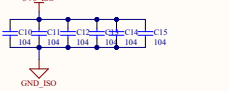
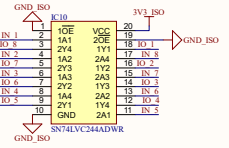
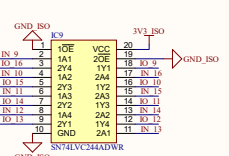
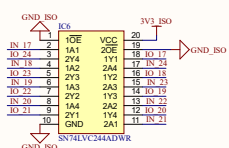
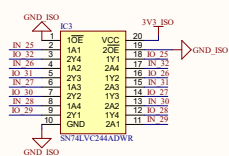
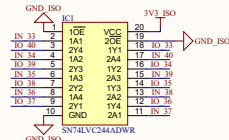
Solder Bridge



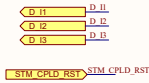
Đầu vào RS485



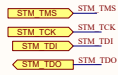
Buffer +Voltage Level Shifter



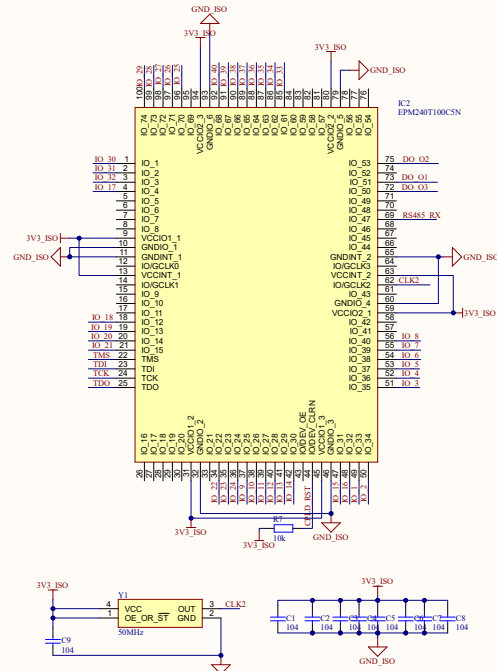
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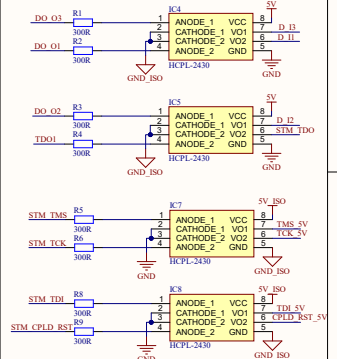
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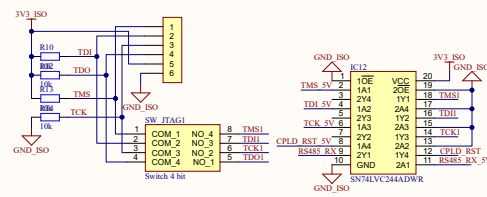
CPLD



OPTO



JTAG



Size	Number	Revision
A2	2/2025	
Date:	2/2025	Sheet of
File:	D:\workspace\UnipolSignal\BchDoc	Drawn By: