

12345678

A

B

C

D

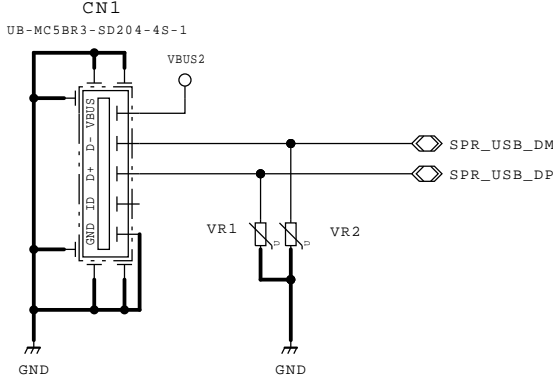
E

F

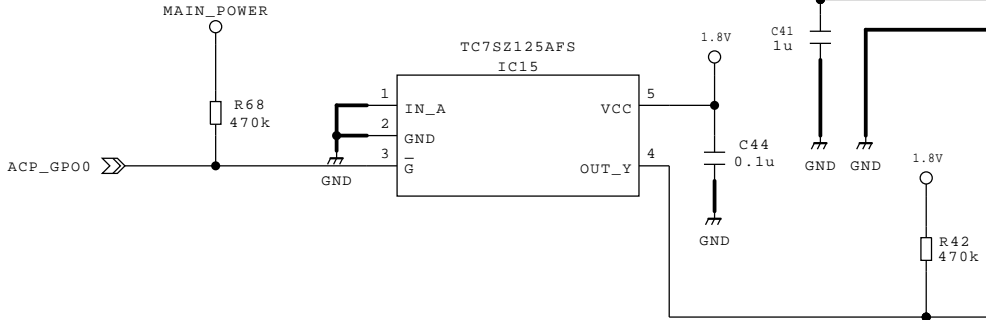
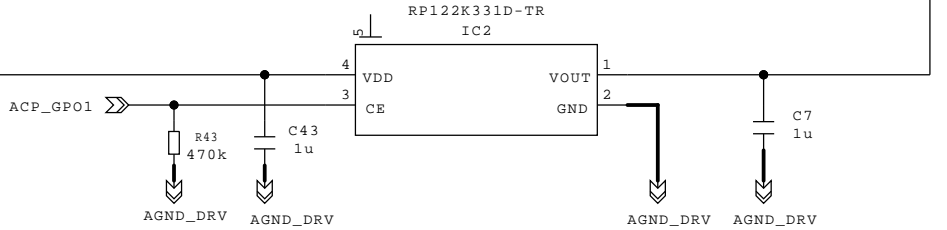
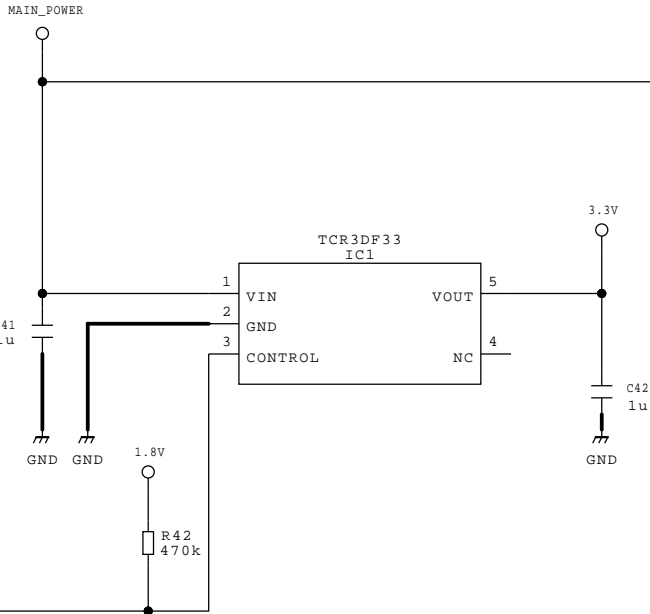
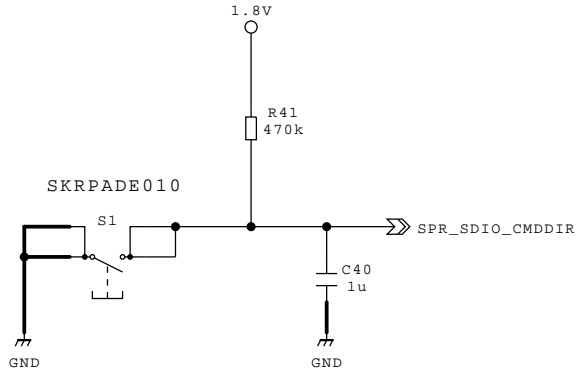
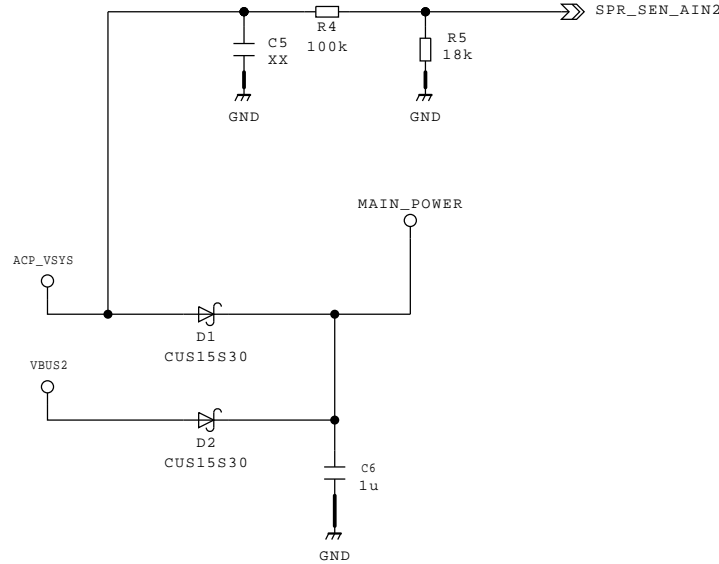
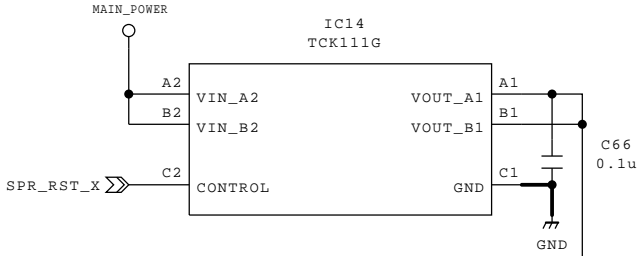
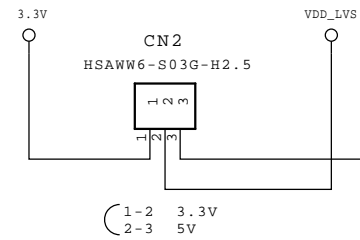
A3
'15.12

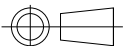
SONY

MICRO USB CN TYPE-B

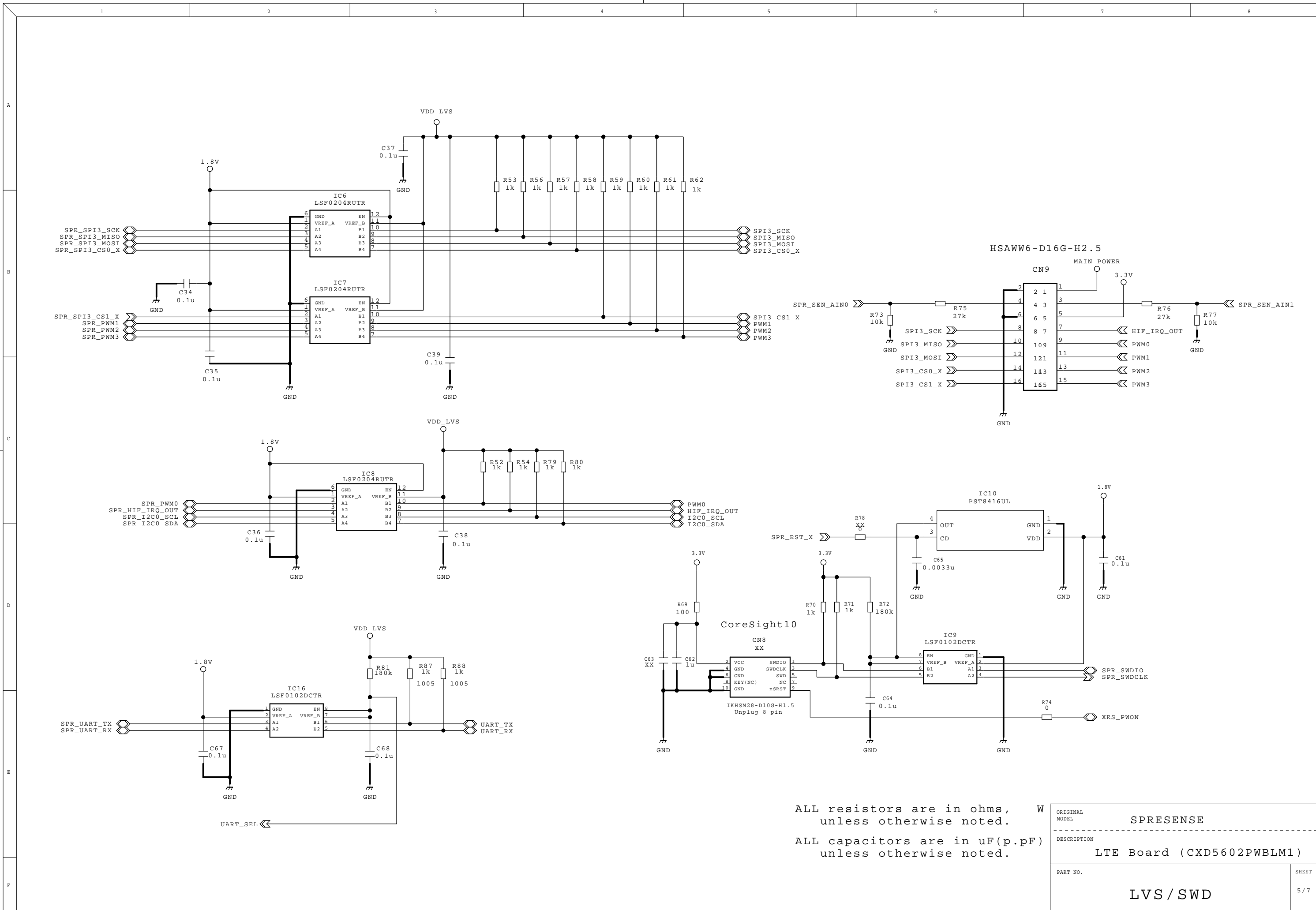


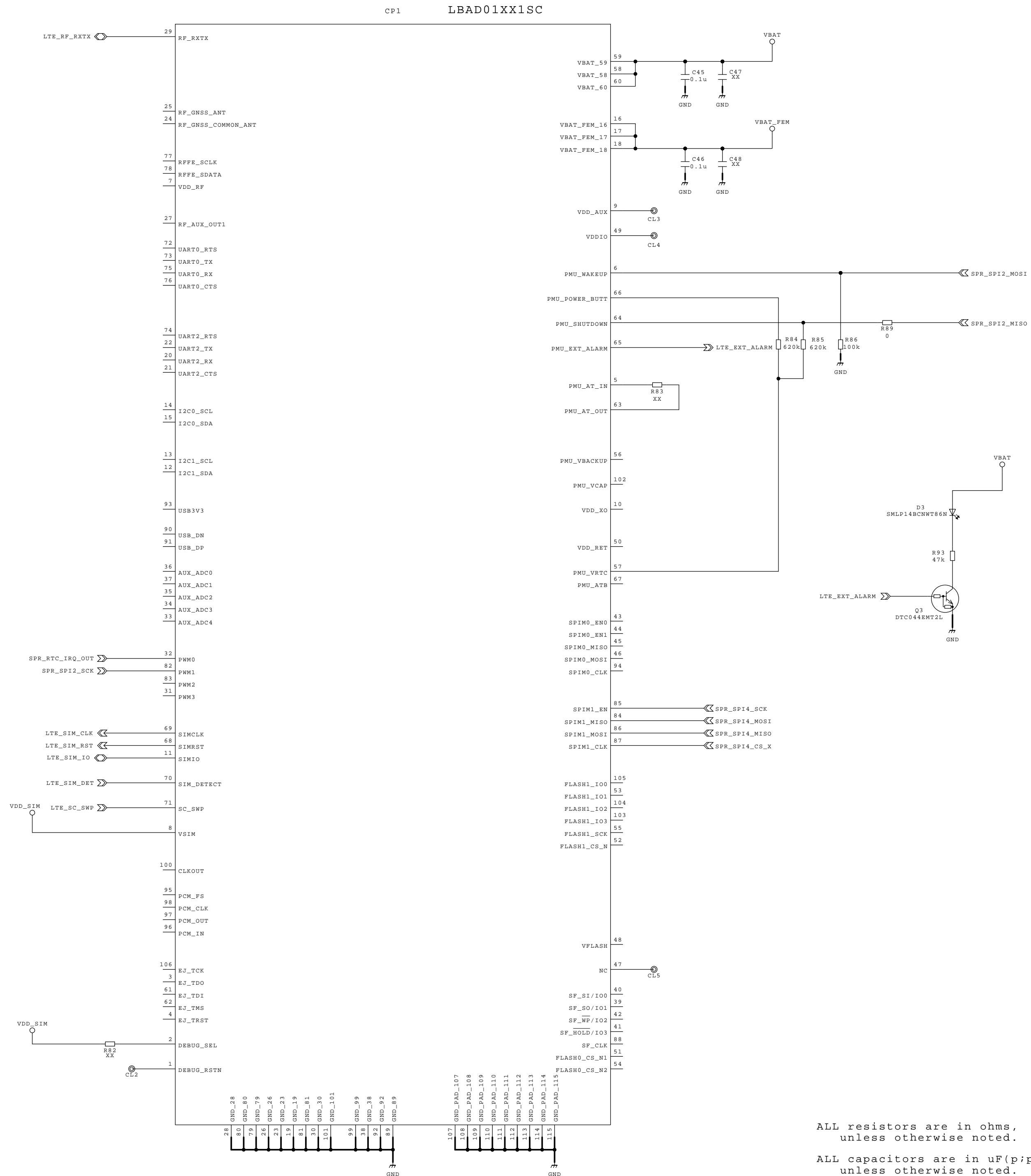
I/O Power SW JP



<div>3 X</div>							GENERAL TOLERANCE		DATE OF ISSUE						
<div>2 X</div>															
<div>1 X</div>															
HISTORY X COUNT		SUFFIX /REPL.		RELEASE NO.		REVISION		SIGN.		MATERIAL(COLOR)		FINISH(COLOR)			
		DATE													
LEGAL OWNER							2024.4.8								
RESPONSIBLE DEPARTMENT						SIZE	UNIT	ORIGINAL MODEL		DESCRIPTION (E)					
						A3	mm	SPRESENSE		LTE Board (CXD5602PWBLM1)					
PLANNED BY			CHECKED BY		APPROVED BY	SCALE		TENTATIVE MODEL		DESCRIPTION (J)					
						:									
DRAWN BY						PRJ.METHOD		TENTATIVE PART NO.		PART NO.		SHEET/PAGES			
										POWER		1 / 7			

THIS DOCUMENT IS THE PROPRIETARY PROPERTY OF SONY. ITS USE IS AUTHORIZED ONLY FOR RESPONDING TO A REQUEST FOR QUOTATION, OR FOR THE PERFORMANCE OF WORK FOR SONY. ALL QUESTIONS MUST BE REFERRED TO THE LOCAL PURCHASING DEPARTMENT OF SONY. THIS PART SHOULD NOT CONTAIN ANY SUBSTANCES WHICH ARE SPECIFIED IN SS-00259-01. THE REPRODUCTION, DISTRIBUTION AND UTILIZATION OF THIS DOCUMENT AS WELL AS THE COMMUNICATION OF ITS CONTENTS TO OTHERS WITHOUT EXPRESS AUTHORIZATION IS PROHIBITED. OFFENDERS WILL BE HELD LIABLE FOR THE PAYMENT OF DAMAGES. ALL RIGHTS RESERVED IN THE EVENT OF THE GRANT OF A PATENT, UTILITY MODEL OR DESIGN. COPYRIGHT RESERVED.

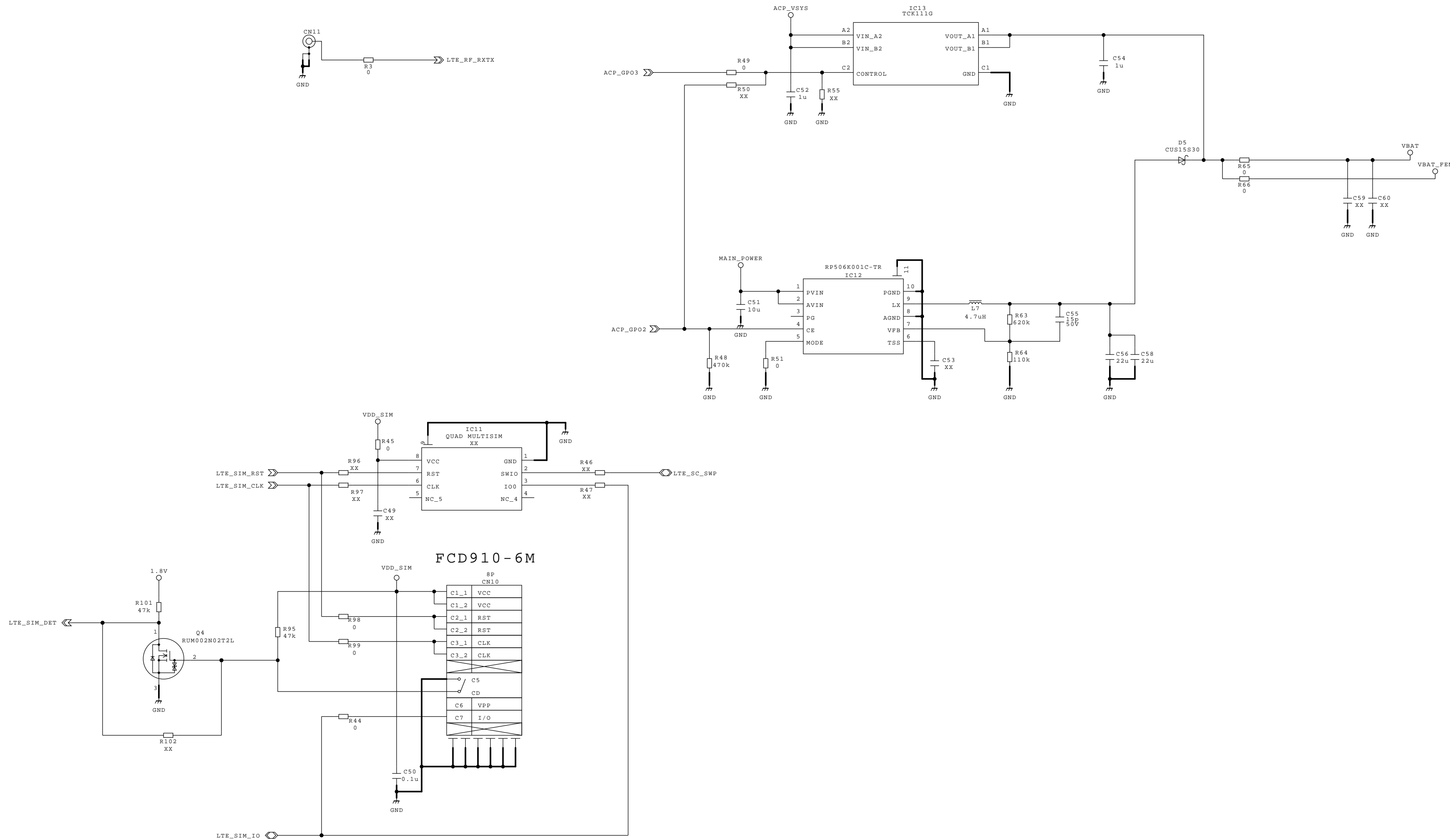




ALL resistors are in ohms, W
unless otherwise noted.

ALL capacitors are in uF(p;pF)
unless otherwise noted.

ORIGINAL MODEL		SPRESENSE	
DESCRIPTION		LTE Board (CXD5602PWBLM1)	
PART NO.	LTE Module		SHEET 6 / 7



ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p;pF)
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE
DESCRIPTION	LTE Board (CXD5602PWBLM1)
PART NO.	LTE Others
SHEET	7 / 7