Main Ext	Cam	LTE(LM1) LTE(LM2	)	ArduinoIDE		SDK上の			電池	原投入後	Arduin	10起動後 5	DK起動後		ピングル-	-プ		CXD56020	GG ピン機能		最大絶対定格		
Ref ピン Ref ピン		Ref ピン Ref	ピン 回路図上の名前	上の名称	SDK上の名称	ピン番号	タイプ	dir	電圧 dir	初期値	dir	初期値 dir	初期値	接続先	モード名	仕様書	モード0	<b>モー</b> ⊦°1	±− 1°2	モード3	電圧(V)	拡張ボード上の機能	説明
JP1 1			GND	-	-	_	Power	-	-   -	_	-		_	-	-	-	-	-	-	-			
JP1 2			UART2_TX	D01	PIN_UART2_TXD	67	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z -	· Hi-Z		UART2	P1n	GPIO	UART2_TXD	-	GPIO	2.5		
JP1 3 IP1 4			UART2_RX	D00	PIN_UART2_RXD	68	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z -	Hi-Z		UART2	P1n	GPIO GPIO	UART2_RXD	-	GPIO GPIO	2.5		
JP1 4 JP1 5	+ +		UART2_RTS UART2_CTS	D28 D27	PIN_UART2_RTS PIN_UART2_CTS	70 69	Digital Digital	1/0	1.8 – 1.8 –	Hi-Z Hi-Z	_	Hi-Z -	Hi-Z	CXD5602GG CXD5602GG	UART2 UART2	P1n P1n	GPIO GPIO	UART2_RTS UART2_CTS	-	GPIO GPIO	2.5 2.5		
JP1 6			I2SO_BCK	D26	PIN_I2S0_BCK	93	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z -	· Hi-Z	CXD5602GG	1280	P1v	GPI0	I2S0_BCK	-	GPIO	2.5		
JP1 7			I2S0_LRCK	D25	PIN_I2S0_LRCK	94	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z -	Hi-Z	CXD5602GG	1280	P1v	GPIO	I2S0_LRCK	-	GPIO	2.5		
JP1 8			SPI5_CS_X	D24	PIN_SPI5_CS_X	76	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z -	Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_CMD	SPI5_CS_X	GPIO	2.5		
JP1 9			SPI5_SCK	D23	PIN_SPI5_SCK	75	Digital		1.8 -	Hi-Z	-	Hi-Z -	Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_CLK	SPI5_SCK	GPIO	2.5		
JP1 10			3.3V	-	=	-	Power		3.3 0	-	0	- 0	_	-	-	-	-	-	-	_			
JP1 11 JP1 12	-		1.8V SEN_IRQ	- D22	PIN_SEN_IRQ_IN	37	Power Digital	0	1.8 O	Hi-Z	0	- C	) – Hi-Z	CXD5602GG	SEN_IRQ_IN	P1e	- GPIO	SEN_IRQ_IN	-		2.5		
JP1 13	+ +		SEN_INQ SEN AIN4	A2	LPADC2	-	Analog	1/O	~0.7 I	-		- I	- 111-2	CXD5602GG	3LIV_IIVQ_IIV		- -	JEN_INQ_IN	_		1.05		
JP2 1			XRST	-	(SPR_RST_X)	-	Digital	0	1.8 -	Low	0	High C	) High		i –	-	-	_	_	_			
JP2 2			1.8V	-	=	-	Power	0	1.8 0	-	0	- (	) –	-	-		-	-	-	-			
JP2 3			3.7V(4.0V)	-	=	-	Power	1/0 3	3.6-4.4 0	-	0	- (	) –	-	-	-	-	-	-	-	7		
JP2 4			GPIO	D21	PIN_EMMC_DATA3	80	Digital	1/0	1.8 –	Hi-Z	-	Hi-Z -	· Hi-Z		EMMCB	P1q	GPIO	EMMC_DATA3	-	GPIO	2.5		
JP2 5	1		GPIO	D20	PIN_EMMC_DATA2	79	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z -	· Hi-Z	CXD5602GG	EMMCB	P1q	GPI0	EMMC_DATA2	-	GPIO OPIO	2.5		
JP2 6 JP2 7	+ +		I2SO_DATA_IN I2SO DATA OUT	D19 D18	PIN_I2SO_DATA_IN PIN I2SO DATA OUT	95 96	Digital Digital	1/0	1.8 – 1.8 –	Hi-Z Hi-Z	_	Hi-Z -	Hi-Z	CXD5602GG CXD5602GG	i 12S0 i 12S0	P1v P1v	GPIO GPIO	I2SO_DATA_IN I2SO DATA OUT	-	GPIO GPIO	2.5 2.5		
JP2 8	+ +		SPR_SPI5_MISO	D18	PIN_SPI5_MISO	78	Digital	1/0	1.8 -	Hi-Z	- 1	Hi-Z -	· Hi-7	CXD5602GG	EMMCA	P1p	GPIO GPIO	EMMC DATA1	SPI5 MISO	GPIO GPIO	2.5		
JP2 9	1		SPR_SPI5_MOSI	D16	PIN_SPI5_MOSI	77	Digital	1/0	1.8 -	Hi-Z	- 1	Hi-Z -	· Hi-Z		EMMCA	P1p	GPI0	EMMC_DATA0	SPI5_MOSI	GPIO	2.5		
JP2 10			GND	-			Power		_   -	-	1	_   -	-	-	-		-	-	-	-			
JP2 11			I2C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0	1.8 -	High	Τ	High I	High	_	i 12C0	P1j	GPIO	I2C0_BCK	ı	-	2.5		
JP2 12			I2C0_SDA	D14	PIN_I2C0_BDT	45	Digital	1/0	1.8 -	High		High I	High		12C0	P1j	GPI0	I2C0_BDT	-	_	2.5		
JP2 13	CN1 1		SEN_AIN5	A3	LPADC3	-	Analog		~0.7 I	-		- I		CXD5602GG	-	-	-	-	-	_	1.05		
CN5 1 CN5 2	CN1 1 CN1 2		MCLK GND	-	_ _	-	Digital Power	0	1.8 –	_	0	Hi-Z C	) Hi-Z	26MHz TCX0	) – –	-	_	_	-		2.5	+	
CN5 2 CN5 3	CN1 2 CN1 3		I2C_SDA	_	PIN_SPI0_MISO	20	Digital	1/0	1.8 -	- High	+	High I	High		i 12C2	P17	GPIO	I2C2_BDT	SPI0_MISO	GPIO	2.5		
CN5 4	CN1 4		I2C_SCL	_	PIN_SPI0_MOSI	19	Digital	1/0	1.8 -	High	-	High I	High		12C2	P17	GPI0	I2C2_BCK	SPI0_MOSI	GPIO	2.5		
CN5 5	CN1 5		XRS	D35	PIN_SDIO_DIR1_3	91	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z -	Hi-Z	CXD5602GG	SDIOC	P1t	GPIO	SDIO_DIR1_3	GPIO	GPIO	2.5		
CN5 6	CN1 6		PWDN	D34	PIN_SDIO_DIR0	90	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z -	· Hi-Z	CXD5602GG	SDIOC	P1t	GPI0	SDIO_DIR0	GPIO	GPIO	2.5		
CN5 7	CN1 7		LDO_EN	-	(ACP_GPO4)	_	Digital		3.6-4.4 0	Hi-Z	0	Low C		_	_	-	-	-	-	_			
CN5 8	CN1 8		VDD_3.7V	-	- DIN 10 DATA4	-	Power		3.6-4.4 0	-	0	- (	) –	-	-	- D1	-	- 10 DATA4	-	-	0.5		
CN5 9 CN5 10	CN1 9 CN1 10		IS_DATA4 IS_DATA6	-	PIN_IS_DATA4 PIN_IS_DATA6	63 65	Digital Digital	I/O I/O	1.8 – 1.8 –	Hi-Z Hi-Z		Hi-Z I		CXD5602GG CXD5602GG	i IS	P1m P1m	GPIO GPIO	IS_DATA4 IS_DATA6	GPIO GPIO	GPIO GPIO	2.5 2.5		
CN5 11	CN1 11		IS DATA0	-	PIN_IS_DATA0	59	Digital	1/0	1.8 -	Hi-Z	- 1	Hi-Z I	_	CXD5602GG	i IS	P1m	GPIO GPIO	IS_DATA0	GPIO GPIO	GPIO GPIO	2.5		
CN5 12	CN1 12		IS DATA7	-	PIN_IS_DATA7	66	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z I	_	CXD5602GG	i IS	P1m	GPIO	IS DATA7	GPIO	GPIO	2.5		
CN5 13	CN1 13		IS_DATA5	-	PIN_IS_DATA5	64	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z I	_	CXD5602GG	i IS	P1m	GPIO	IS_DATA5	GPIO	GPIO	2.5		
CN5 14	CN1 14		IS_DATA2	-	PIN_IS_DATA2	61	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z I	-	CXD5602GG	i IS	P1m	GPI0	IS_DATA2	GPIO	GPIO	2.5		
CN5 15	CN1 15		IS_HSYNC	-	PIN_IS_HSYNC	58	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z I	-	CXD5602GG	i IS	P1m	GPI0	IS_HSYNC	GPIO	GPIO	2.5		
CN5 16	CN1 16		IS_DATA3	-	PIN_IS_DATA3	62	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z I	-	CXD5602GG	i IS	P1m	GPIO	IS_DATA3	GPIO	GPIO	2.5		
CN5 17	CN1 17		IS_VSYNC	-	PIN_IS_VSYNC	57	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z I		CXD5602GG	i IS	P1m	GPIO GPIO	IS_VSYNC	GPIO CPIO	GPIO GPIO	2.5		
CN5 18 CN5 19	CN1 18 CN1 19		IS_DATA1 GND	-	PIN_IS_DATA1	60	Digital Power	I/O _	1.8 -	Hi-Z	_	Hi-Z I		CXD5602GG	i IS	P1m –	GPIO –	IS_DATA1	GPIO –	GPIO _	2.5		
CN5 19	CN1 19		IS CLK	-	PIN_IS_CLK	56	Digital	1	1.8 I	-		- 1	_	CXD5602GG	i IS	P1m	GPIO	IS_CLK	GPIO	GPIO	2.5		
CN4[L] 1 CN4[L] 1		CN4[L] 1 CN4[L]	1 3.3V_AU	-	-	-	Power	0	3.3 I	-	1	- I	-	-	-	-	-	-	-				
CN4[L] 3 CN4[L] 3		CN4[L] 3 CN4[L]	3 3.3V_AU	-	=	-	Power	0	3.3 I	-	I	- 1	-	-	-	-	-	-	-	-			
CN4[L] 5 CN4[L] 5		CN4[L] 5 CN4[L]	5 ACP_MICA	-	-	-	Analog	_	-	-		- 1	-	CXD5247GF	-	-	-	-	-	-			
CN4[L] 7 CN4[L] 7		CN4[L] 7 CN4[L]	7 ACP_MICB	-	-	-	Analog	1	- 1	-	- 1	- 1	-	CXD5247GF	-	-	-	-	-				
CN4[L] 9 CN4[L] 9 CN4[L] 11 CN4[L] 11		CN4[L] 9 CN4[L]	9 ACP_MICC	-	_	-	Analog	-	- 1	-		- 1	-	CXD5247GF		_	-	_	_	_			
CN4[L] 11 CN4[L] 11 CN4[L] 13 CN4[L] 13		CN4[L] 11 CN4[L] CN4[L] 13 CN4[L]	11 ACP_MICD  13 ACP_MICBIASA	-	<u> </u>	-	Analog Analog	0	- I	-	0	- I	_	CXD5247GF CXD5247GF		_	-	-	-			1	
CN4[L] 15 CN4[L] 15		CN4[L] 15 CN4[L]	15 ACP_MICBIASA	-	-	-	Analog	0	2 0	-	0	- 0		CXD5247GF		-	-	_	-	_			
CN4[L] 17 CN4[L] 17		CN4[L] 17 CN4[L]	17 AGND_MIC	-	-	-	Power	-		-	- 1		-	-	-	-	-	-	-	-			
CN4[L] 19 CN4[L] 19		CN4[L] 19 CN4[L]	19 SPR_I2C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0	1.8 -	High	- 1	High I	High	_		P1j	GPI0	I2C0_BCK	-	-	2.5		
CN4[L] 21 CN4[L] 21		CN4[L] 21 CN4[L]	21 SPR_I2CO_SDA	D14	PIN_I2C0_BDT	45			1.8 -	High		High I	High	_		P1j	GPIO	I2C0_BDT	-		2.5		
CN4[L] 23 CN4[L] 23		CN4[L] 23 CN4[L]	23 SPR_SPI4_SCK	D13	PIN_SPI4_SCK	72	Digital		1.8 -	Hi-Z	-	Hi-Z -	Hi-Z		SPI4	P1o	GPIO GPIO	SPI4_SCK	-	GPIO CRIO	2.5	-	
CN4[L] 25 CN4[L] 25 CN4[L] 27 CN4[L] 27		CN4[L] 25 CN4[L] CN4[L] 27 CN4[L]	<ul> <li>25 SPR_SPI4_MISO</li> <li>27 SPR_SPI4_MOSI</li> </ul>	D12 D11	PIN_SPI4_MISO PIN_SPI4_MOSI	74 73			1.8 – 1.8 –	Hi-Z Hi-Z	+ - 1	Hi-Z -	Hi-Z	_		P1o P1o	GPIO GPIO	SPI4_MISO SPI4_MOSI	=	GPIO GPIO	2.5 2.5	-	
CN4[L] 29 CN4[L] 29		CN4[L] 29 CN4[L]	29 SPR_SPI4_CS_X	D11	PIN_SPI4_MOSI	71			1.8 -	Hi-Z	_	Hi-Z -	· Hi-Z			P10	GPIO GPIO	SPI4_INIOSI SPI4_CS_X	_	GPIO	2.5		
CN4[L] 31 CN4[L] 31		CN4[L] 31 CN4[L]	31 SPR_PWM2	D09	PIN_PWM2	48			1.8 -	Hi-Z	-	Hi-Z -	Hi-Z	_		P1I	GPIO	PWM2	I2C1_BCK	-	2.5		
CN4[L] 33 CN4[L] 33		CN4[L] 33 CN4[L]	33 SPR_I2S0_LRCK	D25	PIN_I2S0_LRCK	94			1.8 -	Hi-Z	_	Hi-Z -	· Hi-Z			P1v	GPI0	I2S0_LRCK	=	GPIO	2.5		
CN4[L] 35 CN4[L] 35		CN4[L] 35 CN4[L]	35 GND	-	-	-	Power	-		-	_		-	-	-	-	-	-	П	-			
CN4[L] 37 CN4[L] 37		CN4[L] 37 CN4[L]	37 SPR_I2SO_DATA_OUT	D18	PIN_I2SO_DATA_OUT	96			1.8 -	Hi-Z	-	Hi-Z -	Hi-Z			P1v	GPI0	I2S0_DATA_OUT	-	GPIO	2.5		
CN4[L] 39 CN4[L] 39		CN4[L] 39 CN4[L]	39 SPR_PWM0	D06	PIN_PWM0	46			1.8 -	Hi-Z	-	Hi-Z -	Hi-Z			P1k	GPIO GPIO	PWM0	- CDIO	-	2.5	-	
CN4[L] 41 CN4[L] 41 CN4[L] 43 CN4[L] 43		CN4[L] 41 CN4[L] CN4[L] 43 CN4[L]	41 SPR_PWM1 43 SPR_I2S0_DATA_IN	D05 D19	PIN_PWM1 PIN_I2S0_DATA_IN	47 95	Digital Digital		1.8 – 1.8 –	Hi-Z Hi-Z	_	Hi-Z -	Hi-Z	CXD5602GG CXD5602GG		P1k P1v	GPIO GPIO	PWM1 I2S0 DATA IN	GPIO –	- GPIO	2.5 2.5	<del>                                     </del>	
CN4[L] 43 CN4[L] 43 CN4[L] 45 CN4[L] 45		CN4[L] 43 CN4[L]  CN4[L] 45 CN4[L]	43 SPR_IZSU_DATA_IN 45 SPR_PWM3	D19	PIN_IZSU_DATA_IN PIN_PWM3	49	Digital		1.8 -	Hi-Z	<del>                                     </del>	Hi-Z -	· Hi-Z	-	PWMB	P1v P1l	GPIO GPIO	PWM3	I2C1_BDT	GPI0 -	2.5		
CN4[L] 47 CN4[L] 47		CN4[L] 47 CN4[L]	47 SPR_I2S0_BCK	D26	PIN_I2S0_BCK	93			1.8 -	Hi-Z	-	Hi-Z -	· Hi-Z	CXD5602GG		P1v	GPIO	I2S0_BCK	-	GPIO	2.5		
CN4[L] 49 CN4[L] 49		CN4[L] 49 CN4[L]	49 SPR_UART2_TX	D01	PIN_UART2_TX	67	Digital		1.8 -	Hi-Z	-	Hi-Z -	Hi-Z			P1n	GPIO	UART2_TX	-	GPIO	2.5		
CN4[L] 51 CN4[L] 51		CN4[L] 51 CN4[L]	51 SPR_UART2_RX	D00	PIN_UART2_RX	68		1/0	1.8 -	Hi-Z		Hi-Z -	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RX	=	GPIO	2.5		
CN4[L] 53 CN4[L] 53		CN4[L] 53 CN4[L]	53 SPR_UART2_RTS	D28	PIN_UART2_RTS	70			1.8 -	Hi-Z	_	Hi-Z -	Hi-Z			P1n	GPIO	UART2_RTS	ı	GPIO	2.5		
CN4[L] 55 CN4[L] 55		CN4[L] 55 CN4[L]	55 SPR_UART2_CTS	D27	PIN_UART2_CTS	69			1.8 -	Hi-Z	-	Hi-Z -	Hi-Z			P1n	GPI0	UART2_CTS	-	GPIO	2.5		
CN4[L] 57 CN4[L] 57		CN4[L] 57 CN4[L]	57 SPR_EMMC_CLK	D23	PIN_EMMC_CLK	75			1.8 -	Hi-Z	-	Hi-Z -	Hi-Z			P1p	GPIO GPIO	EMMC_CLK	SPI5_SCK	GPIO GPIO	2.5	-	
CN4[L] 59 CN4[L] 59 CN4[L] 61 CN4[L] 61		CN4[L] 59 CN4[L] CN4[L] 61 CN4[L]	59 SPR_EMMC_CMD 61 SPR_EMMC_DATA0	D24 D16	PIN_EMMC_CMD PIN_EMMC_DATA0	76 77	Digital	I/O I/O	1.8 – 1.8 –	Hi-Z Hi-Z	-	Hi-Z -	Hi-Z	_		P1p P1p	GPIO GPIO	EMMC_CMD EMMC_DATA0	SPI5_CS_X SPI5_MOSI	GPIO GPIO	2.5 2.5	-	
CN4[L] 61 CN4[L] 61 CN4[L] 63 CN4[L] 63		CN4[L] 61 CN4[L] CN4[L] 63 CN4[L]	63 SPR_EMMC_DATA1	D16	PIN_EMMC_DATAU PIN_EMMC_DATA1	78	Digital Digital	1/0	1.8 -	Hi-Z	-	Hi-Z -	· Hi-Z		EMMCA	P1p P1p	GPIO GPIO	EMMC_DATA0  EMMC_DATA1	SPI5_MUSI SPI5_MISO	GPIO GPIO	2.5	+	
CN4[L] 65 CN4[L] 65		CN4[L] 65 CN4[L]	65 SPR EMMC DATA2		PIN_EMMC_DATA2			1/0	1.8 -	Hi-Z	_	Hi-Z -	· Hi-Z		_	P1q	GPIO GPIO	EMMC_DATA1	- -	GPIO	2.5		
CN4[L] 67 CN4[L] 67		CN4[L] 67 CN4[L]	67 SPR_EMMC_DATA3		PIN_EMMC_DATA3				1.8 -	Hi-Z	-	Hi-Z -	Hi-Z	-		P1q	GPIO	EMMC_DATA3	-	GPIO	2.5		
CN4[L] 69 CN4[L] 69		CN4[L] 69 CN4[L]	69 GND	_	_	-	Power			_			-	-	-	_	-		-	_			

CNIAFET																									
CN4[L] 71 CI	CN4[L] 71		CN4[L]	71 CN4[L]	'1 SPR_SPI2_SCK	D43	PIN_SPI2_SCK	28	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z –	Hi-Z	CXD5602GG	SPI2A	P00	GPIO	SPI2_SCK	UART0_RXD	I2C3_BDT	2.5		
CN4[L] 73 CI	CN4[L] 73		CN4[L]	73 CN4[L]	3 SPR SPI2 MISO	D08	PIN SPI2 MISO	30	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z –	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2 MISO	UARTO RTS	GPIO	2.5		
CN4[L] 75 CI			CN4[L]	75 CN4[L]	'5 SPR_SPI2_MOSI	D04	PIN_SPI2_MOSI	29	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z -	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MOSI	UARTO_CTS	GPIO	2.5		
-	CN4[L] 77		CN4[L]	77 CN4[L]	77 SPR_SPI2_CS_X	D42	PIN_SPI2_CS_X	27	Digital	1/0	1.8 -	Hi-Z	_	Hi-Z -	Hi-Z	CXD5602GG	SPI2A	P00	GPIO	SPI2_CS_X	UART0_TXD	I2C3_BCK	2.5		
	CN4[L] 79		CN4[L]	79 CN4[L]	'9 1.8V	-	-	-	Power	., 0	1.8 0		0	- 0	_	-	-	-	-	-	-	1200_2011	2.10		
										1/0		11: 7	U		_	CVDECOSCO		D1+				CDIO	2.5		
	CN4[L] 81		CN4[L]		SPR_SDIO_CMDDIR	D33	PIN_SDIO_CMDDIR	89	Digital	1/0	1.8 -	Hi-Z	_	Hi-Z O	_	CXD5602GG	SDIOC	P1t	GPI0	SDIO_CMDDIR	GPIO	GPI0	2.5		
CN4[L] 83 CI			CN4[L]		33 SPR_SDIO_CLK	D38	PIN_SDIO_CLK	81	Digital	1/0	1.8 -	Hi-Z	_	Hi-Z 0	_	CXD5602GG	SDIOA	P1u	GPIO	SDIO_CLK	SPI5_SCK	GPIO	2.5		
	CN4[L] 85		CN4[L]	85 CN4[L]	SPR_SDIO_CMD	-	PIN_SDIO_CMD	82	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z O		CXD5602GG	SDIOA	P1r	GPIO	SDIO_CMD	SPI5_CS_X	GPIO	2.5		
CN4[L] 87 CI	CN4[L] 87		CN4[L]	87 CN4[L]	37 SPR_SDIO_DATA0	-	PIN_SDIO_DATA0	83	Digital	1/0	1.8 -	Hi-Z	_	Hi-Z O	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA0	SPI5_MOSI	GPIO	2.5		
CN4[L] 89 CI	CN4[L] 89		CN4[L]	89 CN4[L]	SPR_SDIO_DATA1	_	PIN_SDIO_DATA1	84	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z 0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA1	SPI5_MISO	GPIO	2.5		
CN4[L] 91 CI	CN4[L] 91		CN4[L]	91 CN4[L]	1 SPR_SDIO_DATA2	-	PIN_SDIO_DATA2	85	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z 0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA2	GPIO	GPI0	2.5		
CN4[L] 93 CI	CN4[L] 93		CN4[L]	93 CN4[L]	3 SPR SDIO DATA3	-	PIN SDIO DATA3	86	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z O	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO DATA3	GPIO	GPIO	2.5		
	CN4[L] 95		CN4[L]	95 CN4[L]	95 GND	-		-	Power	_	_ 1 _	-	-		_	_	_	-	_	_	_	_			
	CN4[L] 97		CN4[L]		7 SPR_SDIO_WP	D37	PIN_SDIO_WP	88	Digital	1/0	1.8 -	Hi-Z	_	Hi-Z –	Hi-Z	CXD5602GG	SDIOB	P1s	GPIO	SDIO_WP	GPIO	GPIO	2.5		
-	CN4[L] 99		CN4[L]		9 GND	-	1 IIV_3DIO_WI	-	Power	-		- 111-2	+		111-2	- -	-	1 13	-	3DIO_WI	-	-	2.5		
										- 1/0		_	_		_					_					
	CN4[R] 2		CN4[R]		2 5V	-	-	-	Power	1/0	5 0	_	0	- 0		-	-	_	-	-	-	-	6		
	CN4[R] 4		CN4[R]	. 0111[11]	4 5V	-	-	-	Power	1/0	5 O	_	0	- 0	_	-	-	-	-	-	-	=.	6		
	CN4[R] 6		CN4[R]	6 CN4[R]	6 ACP_SPAP	-	-	-	Analog	0	3.3 –	Hi-Z	-	Hi-Z –	Hi-Z	CXD5247GF	-	-	-	-	-	-			
CN4[R] 8 CI	CN4[R] 8		CN4[R]	8 CN4[R]	8 ACP_SPAN	-	=	-	Analog	0	3.3 –	Hi-Z	-	Hi-Z –	Hi-Z	CXD5247GF	-	-	-	=	_	=			
CN4[R] 10 CI	CN4[R] 10		CN4[R]	10 CN4[R]	.0 ACP_SPBN	-	-	-	Analog	0	3.3 -	Hi-Z	-	Hi-Z –	Hi-Z	CXD5247GF	-	-	-	-	-	-			
CN4[R] 12 CI	CN4[R] 12		CN4[R]	12 CN4[R]	.2 ACP_SPBP	-	-	-	Analog	0	3.3 -	Hi-Z	-	Hi-Z –	Hi-Z	CXD5247GF	-	-	-	-	-	-			
CN4[R] 14 CI			CN4[R]		.4 AGND_DRV	-	-	-	Power	-		_	-		-	-	-	-	_	-	-	-			
CN4[R] 16 CI		<del>-    </del>	CN4[R]	16 CN4[R]	.6 SPR SWDIO	-	-	-	Digital	1/0	1.8 -	Hi-Z	_	Hi-Z –	Hi-Z	CXD5602GG	-	-	_	-	-	-	2.5	1	
CN4[R] 18 CI			CN4[R]		.8 SPR SWDCLK	_	_	-	Digital	., 0	1.8 I	Hi-Z		Hi-Z I	Hi-7	CXD5602GG	_	<b>+</b> -	_	_	-	_	2.5	<del> </del>	
	N4[R] 20		CN4[R]	20 CN4[R]	20 ACP CLK DMIC	_	_	<del>-</del>	Digital	_	1.8 0	Hi-Z	0	Hi-Z O		CXD5002GG CXD5247GF	_	<u> </u>	_	_	_	_	2.3	+	
							-			1/0		_						+-	1	_		_	7	1	
CN4[R] 22 CI	CN4[R] 22		CN4[R]	22 CN4[R]	22 XRS_PWON	-	=	-	Power	1/0	3.3 1/0	Low	1/0	Low I/C	Low	CXD5247GF	-	+	-	=	-	- CDIL WDT	7	1	
CN4[R] 24 CI	CN4[R] 24		CN4[R]	24 CN4[R]	SPR_GNSS_1PPS_OUT	T D44	PIN_GNSS_1PPS_OUT	6	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z –	Hi-Z	CXD5602GG	GNSS_1PPS_OUT	P14	GPIO	GNSS_1PPS_OUT	CPU_WDT	CPU_WDT	2.5		
													1						<u> </u>			(Open Drain)		ļ	
CN4[R] 26 CI	CN4[R] 26		CN4[R]	26 CN4[R]	26 SPR_SEN_IRQ_IN	D22	PIN_SEN_IRQ_IN	37	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z –	Hi-Z	CXD5602GG	SEN_IRQ_IN	P1e	SEN_IRQ_IN	SEN_IRQ_IN	SEN_IRQ_IN	SEN_IRQ_IN	2.5		
CN4[R] 28 CI	CN4[R] 28		CN4[R]	28 CN4[R]	SPR_HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0	1.8 -	Hi-Z	_	Hi-Z –	Hi-Z	CXD5602GG	HIFIRO	P02	GPIO	HIF_IRQ_OUT	HIF_IRQ_OUT	GNSS_1PPS_OUT	2.5	1	
514-[11] 20 CI	2[11]		ONTEN	25 5144[11]	or n_nn _nvq_oor	202	1 114_1111 _111Q_001	31	DiPirai	1, 0	1.0	111-2		111 2	111-2	5/123002dd	Till IIIQ	1 02	G. 10	1111 _111Q_001	(Open Drain)	G.400_11 1 0_001	2.3	<u> </u>	
CN4[R] 30 CI	CN4[R] 30		CN4[R]	30 CN4[R]	30 GND	-	-	-	Power			-				-	_	-	-	-	-	-			
CN4[R] 32 CI	CN4[R] 32		CN4[R]	32 CN4[R]	32 SPR_RST_X	-	-	-	Digital	0	1.8 -	Low	0	High 0	High	CXD5602GG	-	-	-	-	-	-			
								_														PMU_WDT			
CN4[R] 34 CI	CN4[R] 34		CN4[R]	34 CN4[R]	SPR_AP_CLK	D40	PIN_AP_CLK	5	Digital	1/0	1.8	Hi-Z	-	Hi-Z –	Hi-Z	CXD5602GG	AP_CLK	P13	GPIO	AP_CLK	PMU_WDT	(Open Drain)	2.5	1	
CN4[R] 36 CI	N4[R] 36		CN4[R]	36 CN4[R]	6 SPR_SEN_AIN2	A0	LPADC0	-	Analog	_	0.7 I	_	1	- 1	_	CXD5602GG	_	<del>                                     </del>	_	_	-	-	1.05		
CN4[R] 38 CI			CN4[R]	38 CN4[R]	SPR SEN AIN3	A1	LPADC1	-	Analog	-	0.7		+ ; -	<u> </u>		CXD5602GG	_		_	_	-	_	1.05		
						+		<del>-</del>	, i	-			-	- !			_		_					+	
	CN4[R] 40		CN4[R]	40 CN4[R]	O SPR_SEN_AIN4	A2	LPADC2		Analog		0.7 I			- !	_	CXD5602GG		_		-	-	=	1.05		
	CN4[R] 42		CN4[R]	42 CN4[R]	2 SPR_SEN_AIN5	A3	LPADC3	-	Analog	- 1	0.7 I	_	' '	- 1	_	CXD5602GG	-	_	-	-	-	-	1.05		
	CN4[R] 44		CN4[R]		4 SPR_SEN_AIN0	A4	HPADC0	-	Analog	ı	1.4 I	_		- I	_	CXD5602GG	-	-	_	-	-	-	2.5		
-	CN4[R] 46		CN4[R]	46 CN4[R]	6 SPR_SEN_AIN1	A5	HPADC1	-	Analog	- 1	1.4 I	-	1	- 1	_	CXD5602GG	-	-	-	-	-	-	2.5		
CN4[R] 48 CI	N4[R] 48		CN4[R]	48 CN4[R]	8 GND	-	=		Power				<u> </u>			-	-		_	-	-	-			
CN4[R] 50 CI	CN4[R] 50		CN4[R]	50 CN4[R]	60 SPR_SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z -	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	-	=	2.5		
CN4[R] 52 CI	CN4[R] 52		CN4[R]	52 CN4[R]	SPR_SPI3_MOSI	D31	PIN_SPI3_MOSI	42	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z –	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_MOSI	-	-	2.5		
																		541				i	-	1	i
CN4[R] 54 CI	CN4[R] 54		CN4[R]	54 CN4[R]	SPR_SPI3_SCK	D29	PIN_SPI3_SCK	41	Digital	1/0	1.8	Hi-Z	-	Hi-Z –	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_SCK	-	-	2.5		
CN4[R] 54 CI CN4[R] 56 CI	CN4[R] 54		CN4[R] CN4[R]		54 SPR_SPI3_SCK 56 SPR_SPI3_MISO	D29 D30	PIN_SPI3_SCK PIN_SPI3_MISO	41	Digital Digital	1/0	1.8 -	Hi-Z Hi-Z	-	Hi-Z –	Hi-Z Hi-Z	CXD5602GG CXD5602GG	SPI3 SPI3	P1i P1i	GPIO GPIO	SPI3_SCK SPI3_MISO	-	-	2.5 2.5		
CN4[R] 56 CI	CN4[R] 54 CN4[R] 56		CN4[R]	56 CN4[R]	66 SPR_SPI3_MISO	D30	PIN_SPI3_MISO	43	Digital		1.8 –	Hi-Z	-	Hi-Z –	_	CXD5602GG	SPI3	P1i	GPI0	SPI3_MISO			2.5		
CN4[R] 56 CI CN4[R] 58 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58		CN4[R]	56 CN4[R] 58 CN4[R]	SPR_SPI3_MISO SPR_SPI3_CSO_X	D30 D32	PIN_SPI3_MISO PIN_SPI3_CS0_X	43 38	Digital Digital	1/0	1.8 – 1.8 –	Hi-Z Hi-Z	_ 	Hi-Z –	Hi-Z Hi-Z	CXD5602GG CXD5602GG	SPI3_CS0_X	P1i P1f	GPIO GPIO	SPI3_MISO SPI3_CS0_X	-	-	2.5 2.5		
CN4[R] 56 CI CN4[R] 58 CI	CN4[R] 54 CN4[R] 56		CN4[R]	56 CN4[R] 58 CN4[R]	66 SPR_SPI3_MISO	D30	PIN_SPI3_MISO	43	Digital		1.8 –	Hi-Z	- - -	Hi-Z –	Hi-Z	CXD5602GG	SPI3	P1i	GPI0	SPI3_MISO	- RTC_IRQ_OUT	-	2.5		
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60		CN4[R] CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT	D30 D32	PIN_SPI3_MISO PIN_SPI3_CS0_X	43 38	Digital Digital Digital	1/0	1.8 – 1.8 – 1.8 –	Hi-Z Hi-Z	- - -	Hi-Z –	Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG	SPI3_CS0_X	P1i P1f	GPIO GPIO	SPI3_MISO SPI3_CS0_X	-	-	2.5 2.5 2.5		
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62		CN4[R] CN4[R] CN4[R] CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT -	43 38	Digital Digital Digital Analog	I/0 I/0	1.8 - 1.8 - 1.8 - 3.3 -	Hi-Z Hi-Z Hi-Z	- - -	Hi-Z – Hi-Z – Hi-Z –	Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SPI3 SPI3_CS0_X RTC_IRQ_OUT -	P1i P1f	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT -	- RTC_IRQ_OUT (Open Drain)	- - GPIO	2.5 2.5 2.5 5.25		
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64		CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CS0_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP	D30 D32 D41 -	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	43 38 4 - -	Digital Digital Digital Analog Analog	I/0 I/0 I/0 I/0	1.8 - 1.8 - 1.8 - 3.3 - 3.3 -	Hi-Z Hi-Z Hi-Z	- - - -	Hi-Z - Hi-Z - Hi-Z - 	Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SPI3 SPI3_CS0_X RTC_IRQ_OUT	P1i P1f P12 -	GPIO GPIO GPIO —	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT	- RTC_IRQ_OUT (Open Drain) - -	- - GPIO - -	2.5 2.5 2.5 5.25 5.25		
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66		CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD	D30 D32 D41 D39	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT -	43 38 4 - - 32	Digital Digital Digital Analog Analog Digital	I/0 I/0	1.8 - 1.8 - 1.8 - 3.3 - 3.3 - 1.8 -	Hi-Z Hi-Z Hi-Z - - Hi-Z		Hi-Z - Hi-Z - Hi-Z - Hi-Z - Hi-Z -	Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SPI3 SPI3_CS0_X RTC_IRQ_OUT -	P1i P1f	GPIO GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT -	- RTC_IRQ_OUT (Open Drain) - GPIO	- GPIO - - GPS_EXTLD	2.5 2.5 2.5 5.25		
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 66 CN4[R] 68		CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND	D30 D32 D41 -	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	43 38 4 - -	Digital Digital Digital Analog Analog	I/0 I/0 I/0 I/0 I/0 I/0 -	1.8	Hi-Z Hi-Z Hi-Z - - Hi-Z	-	Hi-Z - Hi-Z - Hi-Z - 	Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SPI3 SPI3_CS0_X RTC_IRQ_OUT	P1i P1f P12 -	GPIO GPIO GPIO —	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT	- RTC_IRQ_OUT (Open Drain) - -	- - GPIO - -	2.5 2.5 2.5 5.25 5.25		
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 68 CI CN4[R] 70 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 66 CN4[R] 68 CN4[R] 70		CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD	D30 D32 D41 D39	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT PIN_HIF_GPIOO	43 38 4 - - 32	Digital Digital Digital Analog Analog Digital	I/0 I/0 I/0 I/0 I/0 I/0 O	1.8 - 1.8 - 1.8 - 3.3 - 3.3 - 1.8 - - 3.6-4.4 0	Hi-Z Hi-Z Hi-Z - - Hi-Z - Hi-Z	-	Hi-Z - Hi-Z - Hi-Z - Hi-Z - Hi-Z -	Hi-Z Hi-Z Hi-Z - - Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SPI3 SPI3_CS0_X RTC_IRQ_OUT - HIF_GPI00	P1i P1f P12 -	GPIO GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPI0	- RTC_IRQ_OUT (Open Drain) - GPIO	- GPIO - - GPS_EXTLD	2.5 2.5 2.5 5.25 5.25		
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 66 CN4[R] 68 CN4[R] 70		CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND	D30 D32 D41 D39 -	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT PIN_HIF_GPI00	43 38 4 - - 32 -	Digital Digital Digital Analog Analog Digital Power	I/0 I/0 I/0 I/0 I/0 I/0 O	1.8	Hi-Z Hi-Z Hi-Z - - Hi-Z - Hi-Z	- - 0	Hi-Z - Hi-Z - Hi-Z Hi-Z Hi-Z -	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SPI3 SPI3_CS0_X RTC_IRQ_OUT - HIF_GPI00	P1i P1f P12 -	GPIO GPIO GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPI0 -	- TC_IRQ_OUT (Open Drain) - GPIO -	GPIO  GPS_EXTLD	2.5 2.5 2.5 5.25 5.25	ヘッドフォンミュート	Lowでミュート
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 68 CI CN4[R] 70 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 66 CN4[R] 70 CN4[R] 72		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 72 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 70 ACP_GP05	D30 D32 D41 D39	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT PIN_HIF_GPI00 -	43 38 4 - - 32 -	Digital Digital Digital Analog Analog Digital Power Digital	I/O I/O I/O I/O I/O I/O O O	1.8 - 1.8 - 1.8 - 3.3 - 3.3 - 1.8 - - 3.6-4.4 0	Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z Hi-Z Hi-Z Hi-	- - 0	Hi-Z - Hi-Z - Hi-Z Hi-Z Low 0	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - Low Low	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SPI3 SPI3_CS0_X RTC_IRQ_OUT - HIF_GPI00	P1i P1f P12 -	GPIO GPIO GPIO  - GPIO GPIO - GPIO - GPIO GPIO -	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPI0 -	RTC_IRQ_OUT (Open Drain) GPIO	- GPIO - - - GPS_EXTLD -	2.5 2.5 2.5 5.25 5.25	ヘッドフォンミュート	Lowでミュート
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 72 CI CN4[R] 74 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 66 CN4[R] 70 CN4[R] 72		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 70 ACP_GPO5 72 ACP_GPO6	D30 D32 D41 D39	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT  PIN_HIF_GPI00	43 38 4 - - 32 - -	Digital Digital Digital Analog Analog Digital Power Digital Digital	I/0 I/0 I/0 I/0 I/0 O O	1.8	Hi-Z Hi-Z Hi-Z - Hi-Z Hi-Z Hi-Z Hi-Z Hi-	- - 0	Hi-Z - Hi-Z - Hi-Z Hi-Z Low 0 Low 0	Hi-Z Hi-Z Hi-Z - Hi-Z - Low Low	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF	SPI3 SPI3_CS0_X RTC_IRQ_OUT HIF_GPI00	P1i P1f P12 -	GPIO GPIO GPIO - GPIO - GPIO - GPIO GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPI0	- RTC_IRQ_OUT (Open Drain) - GPIO	- GPIO - - - GPS_EXTLD - -	2.5 2.5 2.5 5.25 5.25	ヘッドフォンミュート	Lowでミュート
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 72 CI CN4[R] 74 CI CN4[R] 76 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 76		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 76 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4  32  	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Digital Power	I/O I/O I/O I/O I/O O O I/O I/O O I/O	1.8 - 1.8 - 1.8 - 1.8 - 3.3 - 3.3 - 1.8 3.6-4.4 0 3.6-4.4 0 3.6-4.4 0 3.6-4.4 1/0	Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z - Hi-Z - Hi-Z - Hi-Z Hi-Z	- 0 0 0 1/0	Hi-Z - Hi-Z - Hi-Z Hi-Z Low 0 Low 0 - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - Low Low Low	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF	SPI3 SPI3_CS0_X RTC_IRQ_OUT HIF_GPI00	P1i P1f P12 P03	GPIO GPIO GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPI0	- RTC_IRQ_OUT (Open Drain) - GPIO	- GPIO - GPS_EXTLD	2.5 2.5 2.5 5.25 5.25 5.25 2.5	ヘッドフォンミュート	Lowでミュート
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 72 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 60 CN4[R] 60 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 76 CN4[R] 77		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 75 CN4[R] 76 CN4[R] 77 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 69 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS 68 ACP_VSYS	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT  PIN_HIF_GPIOO	43 38 4 	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Power Power	I/O I/O I/O I/O I/O O O I/O I/O O I/O	1.8 - 1.8 - 1.8 - 3.3 - 3.3 - 1.8 - - 3.6-4.4 O 3.6-4.4 O 3.6-4.4 O	Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z - Hi-Z - Hi-Z - Hi-Z Hi-Z	- 0 0	Hi-Z - Hi-Z - Hi-Z Hi-Z Low 0 Low 0 - Hi-Z Low 10	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - Low Low Low	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5624TGF CXD524TGF CXD524TGF	SPI3 SPI3_CS0_X RTC_IRQ_OUT HIF_GPI00	P1i P1f P12	GPIO GPIO GPIO GPIO - GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0	RTC_IRQ_OUT (Open Drain)	- GPIO - - GPS_EXTLD - - - -	2.5 2.5 2.5 5.25 5.25 2.5	ヘッドフォンミュート	Lowでミュート
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 72 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 80 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 76 CN4[R] 76 CN4[R] 78 CN4[R] 80		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 76 CN4[R] 778 CN4[R] 80 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 70 ACP_GPO5 72 ACP_GPO6 74 ACP_GPO7 76 ACP_VSYS 78 ACC_VSYS 78 GND	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	43 38 4 - - 32 - - - - - -	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Power Power Power	1/0 1/0 1/0 1/0 1/0 1/0 0 0 0 1/0 1/0 1/	1.8	Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z Hi-Z Hi-Z Hi-	- 0 0 0 1/0	Hi-Z - Hi-Z - Hi-Z -    Low 0 Low 0 - 1/C	Hi-Z Hi-Z Hi-Z Hi-Z  — Hi-Z  — Low Low — — — —	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF	SPI3 SPI3_CS0_X RTC_IRQ_OUT HIF_GPI00	P1i P1f P12	GPIO GPIO  GPIO  GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPIO	RTC_IRQ_OUT (Open Drain)	- GPIO - GPS_EXTLD	2.5 2.5 2.5 5.25 5.25 2.5 7	ヘッドフォンミュート	Lowでミュート
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 66 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 72 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 82 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 74 CN4[R] 78 CN4[R] 80 CN4[R] 82		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 71 CN4[R] 72 CN4[R] 74 CN4[R] 75 CN4[R] 76 CN4[R] 778 CN4[R] 80 CN4[R] 82 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GP05 64 ACP_GP06 64 ACP_GP07 66 ACP_VSYS 68 ACP_VSYS 60 GND 62 SPR_SDIO_CD	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	43 38 4 - - 32 - - - - - - - - - - - - -	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Power Power Power Power Power Digital	1/0 1/0 1/0 1/0 1/0 1/0 0 0 1/0 1/0 1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z Hi-Z - Hi-Z - Hi-Z Hi-Z	- 0 0 0 1/0	Hi-Z - Hi-Z - Hi-Z Low 0 Low 0 Low 0	Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z - Low Low Low Hi-B Hi-B Hi-B Hi-B Hi-B Hi-B Hi-B Hi-B	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  - HIF_GPI00 SDIOB	P1i P1f P12 - - - P03 - - - - - - - - - - - - - - - - - - -	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT  - GPI0 SDI0_CD	- RTC_IRQ_OUT (Open Drain) - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	- GPIO - GPS_EXTLD GPIO	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7	ヘッドフォンミュート	Lowでミュート
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 82 CI CN4[R] 84 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 66 CN4[R] 66 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 74 CN4[R] 74 CN4[R] 78 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 71 CN4[R] 72 CN4[R] 74 CN4[R] 75 CN4[R] 76 CN4[R] 80 CN4[R] 81 CN4[R] 82 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 70 ACP_GPO5 72 ACP_GPO6 74 ACP_GPO7 76 ACP_VSYS 76 ACP_VSYS 76 GND 77 GND 78 SPR_SDIO_CD 79 SPR_IZC2_SDA	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 - - 32 - - - - - - - - - - - - -	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Power Power Power Digital Digital	I/O I/O I/O I/O I/O I/O O O I/O I/O I/O	1.8	Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z Hi-Z - Hi-Z Hi-Z	- 0 0 0 0 1/0 1/0	Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low Hi-Z Hi-B Hi-B High	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  - HIF_GPI00 SDIOB SPI0B	P1i P1f P12 	GPIO GPIO GPIO	SPI3_MISO SPI3_CSO_X RTC_IRQ_OUT - GPI0 SDIO_CD I2C2_BDT	- CONTINUE OF THE PROPERTY OF	- GPIO -	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 2.5 2.5	ヘッドフォンミュート	Lowでミュート
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 72 CI CN4[R] 74 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 82 CI CN4[R] 84 CI CN4[R] 84 CI CN4[R] 86 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 70 CN4[R] 77 CN4[R] 74 CN4[R] 76 CN4[R] 78 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 84 CN4[R] 84		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 71 CN4[R] 72 CN4[R] 74 CN4[R] 75 CN4[R] 76 CN4[R] 78 CN4[R] 80 CN4[R] 81 CN4[R] 82 CN4[R] 84 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO7 66 ACP_VSYS 68 ACP_VSYS 69 GND 60 GND 612 SPR_SDIO_CD 634 SPR_IZC2_SDA 64 SPR_IZC2_SCL	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 - - 32 - - - - - - - - - - - - -	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Power Power Power Power Digital Digital Power Power Digital Digital	I/O I/O I/O I/O I/O I/O O O I/O I/O I/O	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- 0 0 0 0 1/0 1/0 1/0	Hi-Z - Hi-Z - Hi-Z Low 0 Low 0 Low 0	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z  Hi-Z - Low Low Low Low High High	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5602GG CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  - HIF_GPI00 SDI0B SPI0B	P1i P1f P12 	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0 SDI0_CD I2C2_BDT I2C2_BCK	- CONTINUE OF THE PROPERTY OF	- GPIO - GPS_EXTLD GPIO	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7	ヘッドフォンミュート	Lowでミュート
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 82 CI CN4[R] 84 CI CN4[R] 86 CI CN4[R] 86 CI CN4[R] 86 CI CN4[R] 87 CI CN4[R] 88 CI CN4[R] 88 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 78 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 88		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 75 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 86 CN4[R] 88 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 65 SPR_GPS_EXTLD 66 SPR_GPS_EXTLD 67 ACP_GPO5 68 ACP_GPO6 69 ACP_GPO7 60 ACP_VSYS 60 GND 60 GND 60 SPR_SDIO_CD 60 SPR_JC2_SDA 60 SPR_JC2_SDA 60 SPR_JC2_SCL 60 SPR_JC2_SCL 60 SPR_JC2_SCL	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Digital Power Power Power Power Digital Digital Digital	I/O I/O I/O I/O I/O O O O I/O I/O I/O I/	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z Hi-Z	- 0 0 0 0 1/0 1/0 1/0 1/0 0	Hi-Z - Hi-Z - Hi-Z Low 0 Low 0 Low 0	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  - HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P12 	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0 SDI0_CD 12C2_BDT 12C2_BCK -	- CONTROL CONT	- GPIO - GPS_EXTLD GPIO	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 2.5 2.5		
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 72 CI CN4[R] 74 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 82 CI CN4[R] 84 CI CN4[R] 84 CI CN4[R] 86 CI	CN4[R] 54 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 78 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 88		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 71 CN4[R] 72 CN4[R] 73 CN4[R] 74 CN4[R] 75 CN4[R] 76 CN4[R] 77 CN4[R] 78 CN4[R] 80 CN4[R] 80 CN4[R] 81 CN4[R] 82 CN4[R] 84 CN4[R] 85 CN4[R] 86 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO7 66 ACP_VSYS 68 ACP_VSYS 69 GND 60 GND 612 SPR_SDIO_CD 634 SPR_IZC2_SDA 64 SPR_IZC2_SCL	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 - - 32 - - - - - - - - - - - - -	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Power Power Power Power Digital Digital Power Power Digital Digital	I/O I/O I/O I/O I/O O O I/O I/O I/O I/O	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z Hi-Z	- 0 0 0 0 1/0 1/0 1/0	Hi-Z - Hi-Z - Hi-Z Low 0 Low 0 Low 0	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5602GG CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  - HIF_GPI00 SDI0B SPI0B	P1i P1f P12 	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0 SDI0_CD I2C2_BDT I2C2_BCK	- CONTINUE OF THE PROPERTY OF	- GPIO - GPS_EXTLD GPIO	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 2.5 2.5	オーディオ3.3V出力	Lowでミュート
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 82 CI CN4[R] 84 CI CN4[R] 86 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 89 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 66 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 74 CN4[R] 74 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 84 CN4[R] 88 CN4[R] 88 CN4[R] 89 CN4[R] 89		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 71 CN4[R] 72 CN4[R] 74 CN4[R] 75 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 85 CN4[R] 86 CN4[R] 87 CN4[R] 88 CN4[R] 89 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS 66 GND 67 GND 68 SPR_SDIO_CD 69 SPR_SDIO_CD 60 SPR_IZC2_SDA 60 SPR_IZC2_SCL 60 ACP_GPO1	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Power Power Power Power Digital	I/O I/O I/O I/O I/O I/O O O O I/O I/O I/	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - 0 0 1/0 1/0 1/0 - - 1/0 0	Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5247GF	SPI3 SPI3_CSO_X RTC_IRQ_OUT  - HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P12 	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0 SDI0_CD 12C2_BDT 12C2_BCK -	- CONTROL CONT	- GPIO - GPS_EXTLD GPIO	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 2.5 2.5	オーディオ3.3V出力 LTE用電源出力	Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 82 CI CN4[R] 84 CI CN4[R] 86 CI CN4[R] 86 CI CN4[R] 86 CI CN4[R] 87 CI CN4[R] 88 CI CN4[R] 88 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 66 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 74 CN4[R] 74 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 84 CN4[R] 88 CN4[R] 88 CN4[R] 89 CN4[R] 89		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 71 CN4[R] 72 CN4[R] 74 CN4[R] 75 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 85 CN4[R] 86 CN4[R] 87 CN4[R] 88 CN4[R] 89 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 65 SPR_GPS_EXTLD 66 SPR_GPS_EXTLD 67 ACP_GPO5 68 ACP_GPO6 69 ACP_GPO7 60 ACP_VSYS 60 GND 60 GND 60 SPR_SDIO_CD 60 SPR_JC2_SDA 60 SPR_JC2_SDA 60 SPR_JC2_SCL 60 SPR_JC2_SCL 60 SPR_JC2_SCL	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Digital Power Power Power Power Digital Digital Digital	I/O I/O I/O I/O I/O I/O O O O I/O I/O I/	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- 0 0 0 0 1/0 1/0 1/0 1/0 0	Hi-Z - Hi-Z - Hi-Z Low 0 Low 0 Low 0	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  - HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P12	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0 SDI0_CD I2C2_BDT I2C2_BCK	RTC_IRQ_OUT (Open Drain)	- GPIO GPIO	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ポードのみ)	
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 82 CI CN4[R] 84 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 92 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 74 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 88 CN4[R] 89 CN4[R] 99 CN4[R] 92		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 70 CN4[R] 71 CN4[R] 72 CN4[R] 74 CN4[R] 75 CN4[R] 80 CN4[R] 80 CN4[R] 81 CN4[R] 82 CN4[R] 84 CN4[R] 85 CN4[R] 86 CN4[R] 87 CN4[R] 88 CN4[R] 89 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS 67 ACP_VSYS 68 ACP_VSYS 69 GND 612 SPR_SDIO_CD 64 SPR_IZC2_SDA 65 SPR_IZC2_SCL 66 ACP_GPO0 66 ACP_GPO1 67 ACP_GPO1 68 ACP_GPO1 69 ACP_GPO1 69 ACP_GPO1 60 ACP_GPO1	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Power Power Power Power Digital	1/0 1/0 1/0 1/0 1/0 1/0 1/0 0 0 0 1/0 1/	1.8	Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z - Hi-Z Hi-Z H	 0 0 1/0 1/0   1/0 0	Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5247GF	SPI3 SPI3_CSO_X RTC_IRQ_OUT  - HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P12	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0 SDI0_CD I2C2_BDT I2C2_BCK	RTC_IRQ_OUT (Open Drain)	- GPIO GPIO	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの	Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 82 CI CN4[R] 84 CI CN4[R] 86 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 89 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 74 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 88 CN4[R] 89 CN4[R] 99 CN4[R] 92		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 70 CN4[R] 71 CN4[R] 72 CN4[R] 74 CN4[R] 75 CN4[R] 80 CN4[R] 80 CN4[R] 81 CN4[R] 82 CN4[R] 84 CN4[R] 85 CN4[R] 86 CN4[R] 87 CN4[R] 88 CN4[R] 89 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS 66 GND 67 GND 68 SPR_SDIO_CD 69 SPR_SDIO_CD 60 SPR_IZC2_SDA 60 SPR_IZC2_SCL 60 ACP_GPO1	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Power Power Power Power Digital	1/0 1/0 1/0 1/0 1/0 1/0 1/0 0 0 0 1/0 1/	1.8	Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z - Hi-Z Hi-Z H	 0 0 1/0 1/0   1/0 0	Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5247GF	SPI3 SPI3_CSO_X RTC_IRQ_OUT  - HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P12	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0 SDI0_CD I2C2_BDT I2C2_BCK	RTC_IRQ_OUT (Open Drain)	- GPIO GPIO	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ポードのみ)	Highで出力
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CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 82 CI CN4[R] 84 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 92 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 66 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 74 CN4[R] 74 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 84 CN4[R] 88 CN4[R] 88 CN4[R] 89 CN4[R] 99 CN4[R] 99		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 71 CN4[R] 72 CN4[R] 74 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 84 CN4[R] 85 CN4[R] 86 CN4[R] 87 CN4[R] 88 CN4[R] 89 CN4[R] 89 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS 67 ACP_VSYS 68 ACP_VSYS 69 GND 612 SPR_SDIO_CD 64 SPR_IZC2_SDA 65 SPR_IZC2_SCL 66 ACP_GPO0 66 ACP_GPO1 67 ACP_GPO1 68 ACP_GPO1 69 ACP_GPO1 69 ACP_GPO1 60 ACP_GPO1	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X PIN_SPI3_CSO_X PIN_RTC_IRQ_OUT	43 38 4 	Digital Digital Digital Analog Analog Digital Power Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z - Hi-Z Hi-Z H	 0 0 1/0 1/0   1/0 0	Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z  Hi-Z  Low Low Low High High High High Low Low Low	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5247GF	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P12 	GPIO GPIO GPIO  GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0 SDI0_CD I2C2_BDT I2C2_BCK	- RTC_IRQ_OUT (Open Drain) - GPIO GPIO SPIO_MISO SPIO_MOSI	- GPIO - GPS_EXTLD	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 82 CI CN4[R] 84 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 90 CI CN4[R] 92 CI CN4[R] 94 CI CN4[R] 94 CI CN4[R] 96 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 66 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 74 CN4[R] 74 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 84 CN4[R] 88 CN4[R] 88 CN4[R] 89 CN4[R] 99 CN4[R] 99		CN4[R]	56 CN4[R] 58 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 70 CN4[R] 71 CN4[R] 72 CN4[R] 73 CN4[R] 74 CN4[R] 75 CN4[R] 76 CN4[R] 78 CN4[R] 80 CN4[R] 80 CN4[R] 81 CN4[R] 82 CN4[R] 84 CN4[R] 85 CN4[R] 86 CN4[R] 87 CN4[R] 88 CN4[R] 89 CN4[R] 89 CN4[R] 90 CN4[R]	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 68 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 65 ACP_VSYS 66 GND 67 GND 68 SPR_IZC2_SDA 69 SPR_IZC2_SCL 69 ACP_GPO0 60 ACP_GPO0 61 ACP_GPO0 62 ACP_GPO0 63 ACP_GPO0 64 ACP_GPO0 65 ACP_GPO0 66 ACP_GPO0 67 ACP_GPO0 68 ACP_GPO0 69 ACP_GPO0	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Digital Digital Digital Power Power Power Digital	1/0 1/0 1/0 1/0 1/0 1/0 0 0 0 1/0 1/0 1/	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - 0 0 0 1/0 1/0 - - - 1/0 0 0	Hi-Z - Hi-Z - Hi-Z Low 0 Low 0 Low 0	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P12 	GPIO GPIO GPIO  GPIO GPIO GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0 SDI0_CD I2C2_BDT I2C2_BCK		- GPIO - GPS_EXTLD	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 2.5 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 70 CI CN4[R] 72 CI CN4[R] 74 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 82 CI CN4[R] 84 CI CN4[R] 85 CI CN4[R] 86 CI CN4[R] 87 CI CN4[R] 87 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 89 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 91 CI CN4[R] 92 CI CN4[R] 94 CI CN4[R] 96 CI CN4[R] 98 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 74 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 80 CN4[R] 84 CN4[R] 80 CN4[R] 84 CN4[R] 89 CN4[R] 84 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 99 CN4[R] 99 CN4[R] 99		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 62 ACP_GPO5 64 ACP_GPO7 66 ACP_VSYS 68 ACP_VSYS 69 GND 60 SPR_IZC2_SDA 60 SPR_IZC2_SDA 61 ACP_GPO1 61 ACP_GPO1 62 SPR_IZC2_SDA 63 ACP_GPO1 64 ACP_GPO1 65 SPR_IZC2_SDA 66 SPR_IZC2_SCL 67 ACP_GPO1 68 ACP_GPO1 69 ACP_GPO1 60 ACP_GPO1 60 ACP_GPO1 61 ACP_GPO3 61 SWOCLK 61 SWOCLK 61 SPR_IZCS SWOCLK 61 SWOCLK 61 SWOCLK	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 32 87 20 19	Digital Digital Digital Analog Analog Digital Power Digital	1/0 1/0 1/0 1/0 1/0 1/0 0 0 0 1/0 1/0 1/	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - 0 0 0 1/0 1/0 - - - 1/0 0 0	Hi-Z - Hi-Z - Hi-Z Low 0 Low 0 Low 0 Hi-Z 1 High 1 - Hi-Z 0 Low 0 Low 0 Hi-Z 0	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5247GF CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5247GF CXD5247GF CXD5247GF	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P12	GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0 SDI0_CD I2C2_BDT I2C2_BCK	RTC_IRQ_OUT (Open Drain)	- GPIO - GPS_EXTLD	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 2.5 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
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CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 70 CI CN4[R] 77 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 89 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 94 CI CN4[R] 95 CI CN4[R] 96 CI CN4[R] 98 CI CN4[R] 99 CI CN4[R] 99 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 74 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 80 CN4[R] 84 CN4[R] 80 CN4[R] 84 CN4[R] 89 CN4[R] 84 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 99 CN4[R] 99 CN4[R] 99		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 69 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO6 65 ACP_VSYS 66 GND 62 SPR_SDIO_CD 63 SPR_JZC2_SDA 64 SPR_JZC2_SDA 65 SPR_JZC2_SCL 66 ACP_GPO0 67 ACP_GPO0 68 ACP_GPO0 69 ACP_GPO0 60 ACP_GPO1 60 ACP_GPO1 61 ACP_GPO1 62 ACP_GPO2 64 SPR_JZC3_SCL 65 SPR_JZC3_SCL 66 SPR_JZC3_SCL 67 ACP_GPO0 68 SWOCLK 68 SWO 69 GND 69 SPR_JZS1_BCK	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X PIN_SPI3_CSO_X PIN_RTC_IRQ_OUT	43 38 4 32 87 20 19	Digital Digital Digital Analog Analog Digital Power Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - 0 0 0 1/0 1/0 - - - 1/0 0 0	Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P1g P12	GPIO GPIO GPIO  GPIO GPIO GPIO GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT	- CONTINUE OF THE PROPERTY OF	- GPIO - GPS_EXTLD - GPS_EXTLD	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 7 2.5 2.5 2.5 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 70 CI CN4[R] 77 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 80 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 89 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 92 CI CN4[R] 94 CI CN4[R] 95 CI CN4[R] 96 CI CN4[R] 98 CI CN4[R] 99 CI CN4[R] 98 CI CN4[R] 99 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 74 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 80 CN4[R] 84 CN4[R] 80 CN4[R] 84 CN4[R] 89 CN4[R] 84 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 99 CN4[R] 99 CN4[R] 99		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 69 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS 69 GND 60 SPR_IZC2_SDA 60 SPR_IZC2_SDA 61 SPR_IZC2_SDA 62 SPR_SDIO_CD 63 ACP_GPO1 64 ACP_GPO1 65 SPR_IZC2_SDA 66 SPR_IZC2_SDA 66 SPR_IZC2_SDA 67 ACP_GPO1 68 ACP_GPO1 69 ACP_GPO1 60 ACP_GPO1 60 ACP_GPO1 61 SWOCLK 61 SWO 61 SPR_IZSI_BCK 61 SPR_IZSI_BCK 61 SPR_IZSI_BCK	D30 D32 D41 D39	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 32 87 20 19 97 98	Digital Digital Digital Analog Analog Digital Power Digital Digital Power Power Power Power Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - 0 0 0 1/0 1/0 - - - 1/0 0 0	Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P1f P12	GPIO GPIO GPIO  GPIO  - GPIO - GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT GPI0 SDI0_CD I2C2_BDT I2C2_BCK		- GPIO - GPS_EXTLD	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 7 2.5 2.5 2.5 2.5 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 74 CI CN4[R] 74 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 86 CI CN4[R] 87 CI CN4[R] 87 CI CN4[R] 87 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 92 CI CN4[R] 94 CI CN4[R] 95 CI CN4[R] 96 CI CN4[R] 97 CI CN4[R] 98 CI CN4[R] 98 CI CN4[R] 99 CI CN4[R] 99 CI CN4[R] 99 CI CN4[R] 90 CI CN4[R] 91 CI CN4[R] 92 CI	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 74 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 80 CN4[R] 84 CN4[R] 80 CN4[R] 84 CN4[R] 89 CN4[R] 84 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 99 CN4[R] 99 CN4[R] 99		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 69 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 69 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS 67 GND 67 GND 68 SPR_IZC2_SDA 69 SPR_IZC2_SDA 69 SPR_IZC2_SCL 60 ACP_GPO0 61 ACP_GPO0 62 SPR_SDIO_CD 63 SPR_IZC2_SCL 64 ACP_GPO0 65 SPR_IZC3_SCL 66 SPR_IZC3_SCL 67 GPO0 68 SPR_IZC3_SCL 69 ACP_GPO0 69 ACP_GPO1 69 SWOCLK 69 SWOCLK 69 SPR_IZS1_BCK 60 SPR_IZS1_DATA_IN	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CS0_X  PIN_RTC_IRQ_OUT	43 38 4 32 87 20 19	Digital Digital Digital Analog Analog Digital Power Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - 0 0 0 1/0 1/0 - - - 1/0 0 0	Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P1g	GPIO GPIO GPIO GPIO  GPIO	SPI3_MISO SPI3_CSO_X  RTC_IRQ_OUT  - GPI0 SDI0_CD I2C2_BDT I2C2_BCK I2S1_BCK I2S1_LRCK I2S1_DATA_IN		- GPIO - GPS_EXTLD	2.5 2.5 2.5 2.5 5.25 2.5 2.5 2.5 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 76 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 94 CI CN4[R] 98 CI CN4[R] 100 CI D7 D6 D5 D4	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 60 CN4[R] 60 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 70 CN4[R] 72 CN4[R] 76 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 89 CN4[R] 90 CN4[R] 92 CN4[R] 92 CN4[R] 94 CN4[R] 96 CN4[R] 98 CN4[R] 98 CN4[R] 98 CN4[R] 98 CN4[R] 98 CN4[R] 98		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 68 SPR_SPI3_CSO_X 69 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 69 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 65 ACP_VSYS 66 ACP_VSYS 67 GND 68 SPR_IZC2_SDA 69 SPR_IZC2_SCL 69 ACP_GPO0 60 ACP_GPO0 60 ACP_GPO0 61 SPR_IZC2_SCL 63 ACP_GPO0 64 SPR_IZC3_SCL 65 SPR_IZC2_SCL 66 SPR_IZC3_SCL 67 GPO0 68 SPR_IZC3_SCL 69 SPR_IZC3_SCL 60 ACP_GPO0 60 ACP_GPO0 60 ACP_GPO1 61 SWOCLK 61 SWOCLK 61 SPR_IZS1_BCK 62 SPR_IZS1_DATA_IN 63 SPR_IZS1_DATA_IN 64 SPR_IZS1_DATA_IN 65 SPR_IZS1_DATA_IN	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X  PIN_SPI3_CSO_X  PIN_RTC_IRQ_OUT	43 38 4 32 87 20 19 97 98 99 100	Digital Digital Digital Analog Analog Digital Power Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - 0 0 0 1/0 1/0 - - - 1/0 0 0	Hi-Z - Hi-Z - Hi-Z Hi-Z Hi-Z Low 0 Low 0 Low 0 - I/C Hi-Z I High I - Hi-Z 0 Low 0 Low 0  Low 0  Hi-Z -	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B 1 - 1 - 1 - 1 - 1 - 1 - 1 -	P1i P1f P1f P12	GPIO GPIO GPIO GPIO GPIO  GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT  - GPI0 SDI0_CD I2C2_BDT I2C2_BCK I2S1_BCK I2S1_LRCK I2S1_DATA_IN I2S1_DATA_OUT		- GPIO - GPS_EXTLD	2.5 2.5 2.5 5.25 5.25 2.5 7 7 7 7 2.5 2.5 2.5 2.5 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 76 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 80 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 89 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 92 CI CN4[R] 94 CI CN4[R] 95 CI CN4[R] 98 CI CN4[R] 96 CI CN4[R] 97 CI CN4[R] 98 CI CN4[R] 96 CI CN4[R] 97 CI CN4[R] 98 CI CN4[R	CN4[R] 54 CN4[R] 56 CN4[R] 56 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 72 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 80 CN4[R] 84 CN4[R] 86 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 99 CN4[R]		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 68 SPR_SPI3_CSO_X 69 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 69 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS 69 GND 60 SPR_IZC2_SDA 60 SPR_IZC2_SDA 61 SPR_IZC2_SDA 62 SPR_SDIO_CD 63 ACP_GPO0 64 ACP_GPO0 65 SPR_IZC2_SCL 66 SPR_IZC2_SCL 67 ACP_GPO0 68 SPR_IZC2_SCL 68 ACP_GPO0 69 ACP_GPO1 69 SPR_IZC2_SCL 60 SPR_IZC2_SCL 61 SPR_IZC2_SCL 62 SPR_IZC2_SCL 63 SPR_IZC2_SCL 64 SPR_IZC2_SCL 65 SPR_IZC2_SCL 66 SPR_IZC2_SCL 67 SPR_IZC2_SCL 68 SPR_IZC2_SCL 69 SPR_IZC2_SCL 69 SPR_IZC2_SCL 60 SPR_IZC2_SCL 60 SPR_IZC2_SCL 61 SPR_IZC2_SCL 61 SPR_IZC2_SCL 62 SPR_IZC2_SCL 63 SPR_IZC2_SCL 64 SPR_IZC2_SCL 65 SPR_IZC2_SCL 66 SPR_IZC2_SCL 66 SPR_IZC2_SCL 67 SPR_IZC2_SCL 68 SPR_IZC2_SCL	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X PIN_SPI3_CSO_X PIN_RTC_IRQ_OUT	43 38 4 32 87 20 19	Digital Digital Digital Analog Analog Digital Power Digital Digital Digital Digital Digital Power Power Digital Power Digital Digital Digital Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low Low High High High Hir-Z Low	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5247GF CXD5247GF CXD5247GF CXD5247GF CXD5602GG	SPI3 SPI3_CS0_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B SPI0B 12S1 12S1 12S1	P1i P1f P12	GPIO GPIO GPIO GPIO  GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT  - GPI0 GPI0 SDI0_CD I2C2_BDT I2C2_BCK I2S1_BCK I2S1_LRCK I2S1_LRCK I2S1_DATA_IN I2S1_DATA_OUT			2.5 2.5 2.5 2.5 5.25 2.5 2.5 2.5 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 70 CI CN4[R] 72 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 88 CI CN4[R] 84 CI CN4[R] 84 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 89 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 91 CI CN4[R] 92 CI CN4[R] 92 CI CN4[R] 93 CI CN4[R] 94 CI CN4[R] 96 CI CN4[R] 98 CI CN4[R] 90 CI CN4[R] 91 CI CN4[R] 92 CI CN4[R] 92 CI CN4[R] 93 CI CN4[R] 94 CI CN4[R] 95 CI CN4[R] 96 CI CN4[R] 97 CI CN4[R] 98 CI CN4[R] 98 CI CN4[R] 98 CI CN4[R] 98 CI CN4[R] 99 CI CN4[R] 98 CI CN4[R] 98 CI CN4[R] 99 CI CN4[R] 99 CI	CN4[R] 54 CN4[R] 55 CN4[R] 56 CN4[R] 56 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 68 CN4[R] 70 CN4[R] 72 CN4[R] 78 CN4[R] 78 CN4[R] 80 CN4[R] 80 CN4[R] 84 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 89 CN4[R] 99 CN4[R]		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 60 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO5 65 ACP_SYS 66 ACP_VSYS 67 GND 67 ACP_USSS 68 ACP_VSYS 69 GND 60 SPR_IZC2_SDA 60 SPR_IZC2_SDA 61 ACP_GPO0 61 ACP_GPO0 62 ACP_GPO0 63 ACP_GPO0 64 SPR_IZC2_SDA 65 SPR_IZC3_SDA 66 SPR_IZC3_SDA 67 GND 68 SPR_IZC3_SDA 69 SPR_IZC3_SDA 60 SPR_IZC3_SDA 60 SPR_IZC3_SDA 61 SPR_IZC3_SDA 62 SPR_IZC3_SDA 63 SPR_IZC3_SDA 64 SPR_IZC3_SDA 65 SWOCLK 68 SWO 69 GND 69 SPR_IZS1_BCK 69 SPR_IZS1_DATA_IN 60 SPR_IZS1_DATA_OUT 60 NC 60 VDD_LVS	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	43 38 4	Digital Digital Analog Analog Digital Power Digital Power Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z - Hi-Z - Hi-Z Low 0 Low 0 Low 0 - Hi-Z 1 High 1 - Hi-Z 0 Low 0 Low 0 - Hi-Z - Hi-Z 1 High 2 - Hi-Z 1 - Hi-Z 0 Low 0 - Low 0 - Low 0 - Hi-Z - Hi-Z - Hi-Z - Hi-Z - Hi-Z - Hi-Z Hi-Z O 0	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT	P1i P1f P1g	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT		- GPIO GPIO GPIO GPS_EXTLD	2.5 2.5 2.5 2.5 5.25 5.25 2.5 2.5 2.5 2.	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 70 CI CN4[R] 72 CI CN4[R] 74 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 89 CI CN4[R] 90 CI CN4[R] 92 CI CN4[R] 94 CI CN4[R] 95 CI CN4[R] 96 CI CN4[R] 98 CI CN4[R] 99 CI CN4[R] 90 CI CN4[R] 91 CI CN4[R] 91 CI CN4[R] 92 CI CN4[R] 95 CI CN4[R] 96 CI CN4[R] 97 CI CN4[R] 98 CI	SN4   S4		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 69 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS 69 GND 62 SPR_SDIO_CD 63 SPR_IZC2_SDA 64 SPR_IZC2_SDA 65 SPR_IZC2_SCL 66 ACP_GPO0 67 ACP_GPO0 68 ACP_GPO0 69 ACP_GPO1 60 ACP_GPO1 60 ACP_GPO1 60 ACP_GPO1 61 ACP_GPO1 62 SPR_IZC3_SDA 63 SPR_IZC3_SDA 64 SPR_IZC3_SDA 65 SPR_IZC3_SDA 66 SPR_IZC3_SCL 67 SPR_IZC3_SDA 68 SPR_IZC3_SCL 68 SPR_IZC3_SCL 69 SPR_IZC3_SCL 60 SPR_IZC3_	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X PIN_SPI3_CSO_X PIN_RTC_IRQ_OUT	43 38 4	Digital Digital Analog Analog Digital Power Digital Digital Digital Power Power Power Power Poigital Digital Power Digital Digital Power Digital Digital Digital Digital Power Digital Digital Digital Digital Digital Digital Digital Digital Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5247GF CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B 12S1 12S1 12S1	P1i P1f P12	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT  GPI0 SDI0_CD I2C2_BDT I2C2_BCK I2S1_BCK I2S1_LRCK I2S1_LRCK I2S1_DATA_OUT		- GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	2.5 2.5 2.5 2.5 5.25 2.5 2.5 2.5 2.5 2.5	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 77 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 89 CI CN4[R] 89 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 91 CI CN4[R] 92 CI CN4[R] 94 CI CN4[R] 95 CI CN4[R] 96 CI CN4[R] 98 CI CN4[R] 99 CI CN4[R] 91 CI CN4[R] 91 CI CN4[R] 92 CI	SN4   S4		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 69 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 65 ACP_VSYS 68 ACP_VSYS 69 GND 62 SPR_SDIO_CD 64 SPR_I2C2_SDA 65 SPR_I2C2_SDA 66 SPR_I2C2_SCL 66 ACP_GPO0 67 ACP_GPO0 68 ACP_GPO0 69 ACP_GPO0 60 ACP_GPO1 60 ACP_GPO1 61 SWOCLK 68 SWO 69 GND 69 SPR_I2SI_BCK 69 SPR_I2SI_BCK 60 SPR_I2SI_DATA_IN 60 SPR_I2SI_DATA_IN 60 SPR_I2SI_DATA_OUT 61 NC 61 VDD_LVS 65 3.33V	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	43 38 4	Digital Digital Analog Analog Digital Power Digital Digital Power Power Power Power Poigital Digital Power Digital Digital Power Digital Power Digital Digital Power Digital Digital Digital Power Digital Digital Digital Digital Digital Digital Power Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5247GF CXD5247GF CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B 12S1 12S1 12S1 12S1	P1i P1f P12	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X  RTC_IRQ_OUT		- GPIO GPIO GPIO GPS_EXTLD	2.5 2.5 2.5 2.5 5.25 5.25 2.5 2.5 2.5 2.	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 64 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 77 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 88 CI CN4[R] 88 CI CN4[R] 89 CI CN4[R] 89 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 91 CI CN4[R] 92 CI CN4[R] 94 CI CN4[R] 95 CI CN4[R] 96 CI CN4[R] 98 CI CN4[R] 99 CI CN4[R] 91 CI CN4[R] 91 CI CN4[R] 92 CI	SN4   S4		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 69 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS 67 ACP_VSYS 68 ACP_VSYS 69 GND 60 SPR_IZC2_SDA 60 SPR_IZC2_SDA 61 SPR_IZC2_SDA 62 SPR_SDIO_CD 64 SPR_IZC2_SDA 65 SPR_IZC2_SDA 66 SPR_IZC2_SDA 67 ACP_GPO1 68 ACP_GPO1 69 ACP_GPO1 60 ACP_GPO1 60 SWOCLK 61 SWOCLK 62 SPR_IZS1_LRCK 63 SPR_IZS1_LRCK 64 SPR_IZS1_LRCK 65 SPR_IZS1_LATA_IN 65 SPR_IZS1_DATA_IN 66 SPR_IZS1_DATA_OUT 67 NC 68 NC	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X PIN_SPI3_CSO_X PIN_RTC_IRQ_OUT	43 38 4	Digital Digital Analog Analog Digital Power Digital Digital Digital Power Power Power Power Poigital Digital Power Digital Digital Power Digital Digital Digital Digital Power Digital Digital Digital Digital Digital Digital Digital Digital Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5247GF CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B 12S1 12S1 12S1	P1i P1f P12	GPIO GPIO GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT  GPI0 SDI0_CD I2C2_BDT I2C2_BCK I2S1_BCK I2S1_LRCK I2S1_LRCK I2S1_DATA_OUT		- GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	2.5 2.5 2.5 2.5 5.25 5.25 2.5 2.5 2.5 2.	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 66 CI CN4[R] 66 CI CN4[R] 70 CI CN4[R] 77 CI CN4[R] 77 CI CN4[R] 78 CI CN4[R] 78 CI CN4[R] 88 CI CN4[R] 89 CI CN4[R] 88 CI CN4[R] 89 CI CN4[R] 88 CI CN4[R] 89 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 91 CI CN4[R] 92 CI CN4[R] 92 CI CN4[R] 94 CI CN4[R] 95 CI CN4[R] 96 CI CN4[R] 97 CI CN4[R] 98 CI CN4[R] 98 CI CN4[R] 99 CI CN4[R] 99 CI CN4[R] 91 CI CN4[R] 91 CI CN4[R] 91 CI CN4[R] 92 CI	SN4   S4		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 69 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 65 ACP_VSYS 68 ACP_VSYS 69 GND 62 SPR_SDIO_CD 64 SPR_I2C2_SDA 65 SPR_I2C2_SDA 66 SPR_I2C2_SCL 66 ACP_GPO0 67 ACP_GPO0 68 ACP_GPO0 69 ACP_GPO0 60 ACP_GPO1 60 ACP_GPO1 61 SWOCLK 68 SWO 69 GND 69 SPR_I2SI_BCK 69 SPR_I2SI_BCK 60 SPR_I2SI_DATA_IN 60 SPR_I2SI_DATA_IN 60 SPR_I2SI_DATA_OUT 61 NC 61 VDD_LVS 65 3.33V	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	43 38 4	Digital Digital Analog Analog Digital Power Digital Digital Power Power Power Power Poigital Digital Power Digital Digital Power Digital Power Digital Digital Power Digital Digital Digital Power Digital Digital Digital Digital Digital Digital Power Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z - Hi-Z - Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B 12S1 12S1 12S1 12S1	P1i P1f P12	GPIO GPIO GPIO	SPI3_MISO SPI3_CSO_X RTC_IRQ_OUT			2.5 2.5 2.5 5.25 5.25 2.5 2.5 2.5 2.5 2.	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力
CN4[R] 56 CI CN4[R] 58 CI CN4[R] 60 CI CN4[R] 62 CI CN4[R] 66 CI CN4[R] 66 CI CN4[R] 68 CI CN4[R] 70 CI CN4[R] 74 CI CN4[R] 76 CI CN4[R] 78 CI CN4[R] 80 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 91 CI CN4[R] 92 CI CN4[R] 92 CI CN4[R] 93 CI CN4[R] 94 CI CN4[R] 95 CI CN4[R] 96 CI CN4[R] 97 CI CN4[R] 98 CI CN4[R] 98 CI CN4[R] 98 CI CN4[R] 98 CI CN4[R] 99 CI CN4[R] 90 CI CN4[R] 90 CI CN4[R] 91 CI CN4[R] 92 CI	CN4[R] 54 CN4[R] 55 CN4[R] 56 CN4[R] 56 CN4[R] 60 CN4[R] 62 CN4[R] 64 CN4[R] 66 CN4[R] 66 CN4[R] 70 CN4[R] 72 CN4[R] 74 CN4[R] 78 CN4[R] 80 CN4[R] 82 CN4[R] 84 CN4[R] 89 CN4[R] 90 CN4[R] 92 CN4[R] 94 CN4[R] 96 CN4[R] 98 CN4[R] 99 CN4[R] 99 CN4[R] 98 CN4[R] 99 CN4[R] 98 CN4[R]		CN4[R]	56	66 SPR_SPI3_MISO 68 SPR_SPI3_CSO_X 69 SPR_SPI3_CSO_X 60 SPR_RTC_IRQ_OUT 62 SPR_USB_DM 64 SPR_USB_DP 66 SPR_GPS_EXTLD 68 GND 60 ACP_GPO5 64 ACP_GPO6 64 ACP_GPO7 66 ACP_VSYS 67 ACP_VSYS 68 ACP_VSYS 69 GND 60 SPR_IZC2_SDA 60 SPR_IZC2_SDA 61 SPR_IZC2_SDA 62 SPR_SDIO_CD 64 SPR_IZC2_SDA 65 SPR_IZC2_SDA 66 SPR_IZC2_SDA 67 ACP_GPO1 68 ACP_GPO1 69 ACP_GPO1 60 ACP_GPO1 60 SWOCLK 61 SWOCLK 62 SPR_IZS1_LRCK 63 SPR_IZS1_LRCK 64 SPR_IZS1_LRCK 65 SPR_IZS1_LATA_IN 65 SPR_IZS1_DATA_IN 66 SPR_IZS1_DATA_OUT 67 NC 68 NC	D30 D32 D41	PIN_SPI3_MISO PIN_SPI3_CSO_X PIN_SPI3_CSO_X PIN_RTC_IRQ_OUT	43 38 4 32 87 20 19 97 98 99 100	Digital Digital Analog Analog Digital Power Digital Digital Power Power Power Poigital Digital Power Digital Power Digital Power Digital Power Digital Power Digital Digital Power Digital Digital Digital Power Digital Digital Digital Power Digital	1/0	1.8	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z - Hi-Z - Hi-Z Hi-Z	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5247GF CXD5602GG	SPI3 SPI3_CSO_X RTC_IRQ_OUT  HIF_GPI00 SDI0B SPI0B SPI0B	P1i P1f P1g	GPIO GPIO GPIO  GPIO  - GPIO - GPIO	SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT		- GPIO - GPS_EXTLD	2.5 2.5 2.5 5.25 5.25 2.5 2.5 2.5 2.5 2.	オーディオ3.3V出力 LTE用電源出力 (LTE拡張ボードのみ) メインボード側からの LTE用電源出力	Highで出力 Highで出力

	JP3 8	1	1	1 1			5V IN PIN		_		1	ı ı			1		1 1		1		1	T	l	_			T	1
			+	-				- 40		_	_	-			-	_	-		-		_	-	-		-	7.5		
-	JP4 1						SPR_SEN_AIN2	A0	LPADC0	-	Analog		~5 I		-		-		CXD5602GG			SEN_AIN2	SEN_AIN2	SEN_AIN2	SEN_AIN2	7.5		
	JP4 2				CN6	2	SPR_SEN_AIN3	A1	LPADC1	-	Analog	1	~5 I	_		-	ı	_	CXD5602GG	=	-	SEN_AIN3	SEN_AIN3	SEN_AIN3	SEN_AIN3	7.5		
	JP4 3				CN6	1	SPR_SEN_AIN4	A2	LPADC2	-	Analog	1	~5 I	_		-	ı	_	CXD5602GG	_	-	SEN_AIN4	SEN_AIN4	SEN_AIN4	SEN_AIN4	7.5		
	JP4 4						SPR_SEN_AIN5	A3	LPADC3	-	Analog	- 1	~5 I	_	- 1	-	I	-	CXD5602GG		-	SEN_AIN5	SEN_AIN5	SEN_AIN5	SEN_AIN5	7.5		
	JP4 5		CN9	4	CN9	4	SPR_SEN_AIN0	A4	HPADC0	-	Analog	- 1	~5 I	_	- 1	-	- 1		CXD5602GG	_	-	SEN_AIN0	SEN_AIN0	SEN_AIN0	SEN_AIN0	8.9		
	JP4 6		CN9	3	CN9	3	SPR_SEN_AIN1	A5	HPADC1	-	Analog	- 1	~5 I	-	1	-	- 1	-	CXD5602GG	-	-	SEN_AIN1	SEN_AIN1	SEN_AIN1	SEN_AIN1	8.9		
	JP2 1				CN6	12	I2C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0	5/3.3 -	High	1	High	- 1	High	CXD5602GG	I2C0	P1j	GPIO	I2C0_BCK	-	-	7		
	JP2 2				CN6	11	I2C0_SDA	D14	PIN_I2C0_BDT	45	Digital	1/0	5/3.3 -	High		High	_	High	CXD5602GG	I2C0	P1j	GPIO	I2C0_BDT	-	-	7		
	JP2 3						AREF	-	-	-	-	0	5/3.3 C	High	0	High	0	High	-	=.	-	-	-	-	-			
	JP2 4						GND	-	_	-	-	-		_	-	-	-	-	-	-	-	-	-	-	-			
	JP2 5						SPI4 SCK	D13	PIN SPI4 SCK	72	Digital	1/0	5/3.3 -	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4 SCK	-	GPIO	7		
	JP2 6		1				SPI4 MISO	D12	PIN SPI4 MISO	74	Digital	1/0	5/3.3 -	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4 MISO	-	GPIO	7		
	JP2 7						SPI4 MOSI	D11	PIN SPI4 MOSI	73	Digital		5/3.3 -	Hi-Z		Hi-Z	-	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4 MOSI	_	GPIO	7		
	JP2 8	<del>                                     </del>	+	+			SPI4 CS X	D10	PIN_SPI4_CS_X	71	Digital		5/3.3 -	Hi-Z	_	Hi-Z	l _ l	Hi-Z	CXD5602GG	SPI4	P10	GPIO	SPI4 CS X	_	GPI0	7		
	JP2 9	+ +	CN9	12	CN9	13	PWM2	D09	PIN_PWM2	48	Digital		5/3.3 -	Hi-Z		Hi-Z		Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM2	I2C1_BCK	-	7		
$\vdash$	JP2 9	<del>                                     </del>	CIND	10	CIND	10	SPI2_MISO	D09	PIN_SPI2_MISO	30	Digital		5/3.3 -	Hi-Z		Hi-Z	H_	Hi-Z	CXD5602GG CXD5602GG	SPI2B	P01	GPIO GPIO	SPI2_MISO	UARTO_RTS	GPIO	7		+
$\vdash$		<del>                                     </del>	+	++		$\vdash$									_		$\vdash$							UARTU_RTS	- GPIO	7	-	+
<b> </b>	JP13 1	<del>                                     </del>	CNIO		CNIO	0	SPI3_CS1_X PWM0	D07	PIN_SPI3_CS1_X	39	Digital		5/3.3 -	Hi-Z		Hi-Z	H	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO GPIO	SPI3_CS1_X	_	=	7		
	JP13 2		CN9	9	CN9	9		D06	PIN_PWM0	46	Digital		5/3.3 -	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPI0	PWM0					
	JP13 3		CN9	11	CN9	11	PWM1	D05	PIN_PWM1	47	Digital		5/3.3 -	Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	PWMA	P1k	GPI0	PWM1	GPIO	_	7		
	JP13 4						SPI2_MOSI	D04	PIN_SPI2_MOSI	29	Digital		5/3.3 -	Hi-Z		Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MOSI	UARTO_CTS	GPIO	7		
	JP13 5		CN9	15	CN9	15	PWM3	D03	PIN_PWM3	49	Digital	1/0	5/3.3 -	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM3	I2C1_BDT	=	7		
	JP13 6		CN9	7	CN9	7	HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0	5/3.3	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	HIF_IRQ_OUT	P02	GPIO	HIF_IRQ_OUT	HIF_IRQ_OUT (Open Drain)	GNSS_1PPS_OUT	7		
	JP13 7				CN6	14	UART2 TX	D01	PIN UART2 TX	67	Digital	1/0	5/3.3 -	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2 TX	-	GPIO	7		
	JP13 8		1		CN6	13	UART2 RX	D00	PIN UART2 RX	68	Digital	1/0	5/3.3 -	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2 RX	-	GPIO	7		
	JP10 1		1		CN6	15	GND	-		-	Power	_		_	_	_	-	_	_	_	-	_	_	-	_			
	JP10 2			1 1	CN6	16	UART SEL	-	_	-	Digital		5/3.3 I	_		-		_	_	_	_	_	_	_	-			UART2の選択信号
	JP10 3			1 1			ACP MICD	_	_	_	Analog		- 1	_	1	_			CXD5247GF	-		_	_	_	_			
	JP10 4		+	<del>     </del>			ACP MICC	-	_	_	Analog		_ <u> </u>		<u> </u>	_	<u> </u>		CXD5247GF	_	_	_	_	_	_			
	JP10 5		+				ACP MICBIASB	_	_	_	Analog	0	2 0		0	_	0		CXD5247GF	_		_	_	_	_			
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<del>                                     </del>	JP10 6	<del>                                     </del>	+	+ +		$\vdash$	AGND MIC	_	_	_	Power	_		<del>-</del>	_	<del> </del>	_		- CAD5247GF		+ _		_	_	_			
<del></del>	JP10 7 JP10 8	<del>                                     </del>	+	++		$\vdash$	AGND_MIC  AGND MIC	_		_	Power	⊢ <u>∸</u> +		+	$+\bar{-}$	_	HĒ		-		<del>-</del> -	_	_	_			-	+
		<del>                                     </del>	CNIC	1	CNIC	2	_			_	1	_			+-	_	_				_	_		_				
	31 10 3	1	CN6	1	CN6	3	ACP_MICB	-	_		Analog		-   !	+	+ :-	_			CXD5247GF	-	+		-					
	JP10 10	<del>                                     </del>	CN6	2	CN6	4	ACP_MICA	-	-	-	Analog	1	- 1			_	1		CXD5247GF	_		-	-	-	_			
$\vdash$	JP10 11	<del>                                     </del>	CN6	3	CN6	5	ACP_MICBIASA		-	-	Analog	0	2 C		0	-	0		CXD5247GF	-		-	-	-	-			
	JP10 12		CN6	4	CN6	6	ACP_MICBIASA	-	-	-	Analog	0	2 C	_	0	-	0		CXD5247GF		-	-	-	-	-			
	JP10 13		CN6	5	CN6	7	AGND_MIC	-	-	-	Power	-	-   -			_	_		-	_		-	-	-	_			
	JP10 14		CN6	6	CN6	8	AGND_MIC	-	-	-	Power	-	-   -		_	-			-	-		-	-	-	-			
	JP10 15		CN6	7	CN6	9	1.8V	-	-	-	Power	0	1.8 C	_	0	-	0	-	-	-	-	-	-	-	-			
	JP10 16		CN6	8	CN6	10	ACP_CLK_DMIC	-	-	-	Digital	0	1.8 C	Hi-Z	0	Hi-Z	0	Hi-Z	CXD5247GF	_	_	-	-	-	-			
			CN9	8	CN9	8	SPI3_SCK	D29	PIN_SPI3_SCK	41	Digital	1/0	5/3.3 -	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_SCK	-	-	7		
			CN9	10	CN9	10	SPI3_MISO	D30	PIN_SPI3_MISO	43	Digital	1/0	5/3.3 -	Hi-Z		Hi-Z		Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_MISO	-	-	7		
			CN9	12	CN9	12	SPI3_MOSI	D31	PIN_SPI3_MOSI	42	Digital	1/0	5/3.3 -	Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_MOSI	-	-	7		
			CN9	14	CN9	14	SPI3_CS0_X	D32	PIN_SPI3_CS0_X	38	Digital	1/0	5/3.3 -	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	SPI3_CS0_X	P1f	GPIO	SPI3_CS0_X	-	_	7		
			CN9	16	CN9	16	SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0	5/3.3 -	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	-	-	7		
			S1	81	S1	81	SPR_SDIO_CMDDIR	D33	PIN_SDIO_CMDDIR	89	Digital	1/0	1.8 -	Hi-Z	-	Hi-Z	0	Hi-Z	CXD5602GG	SDIOC	P1t	GPI0	SDIO_CMDDIR	GPIO	GPIO	2.5		
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日付	内容
2020.11.13	第一稿
2021.4.14	ACP_GPO1~7のArduinoおよびSDK起動後の初期値を訂正。
2021.4.14	XRST(SPR_RST_X)の各初期値を訂正。
2021.12.20	CXD5247GFに接続されるピンの電圧範囲を3.6-4.4Vに訂正。
2021.12.20	MCLKの電源投入後およびAruduino起動後の初期値を訂正。
2022.9.7	SPR_SPI2_SCKのArduinoIDE上の名称を訂正。
2024.3.21	メインボードのLED0~3とLTE拡張ボードのS1を追加
2024.6.6	LTE(LM2)追加。Refとピン番号の書式を変更。拡張ボードとLTE
2024.0.0	ボードのオーディオ関係ピンを追加。