

A

B

C

D

E

F

A

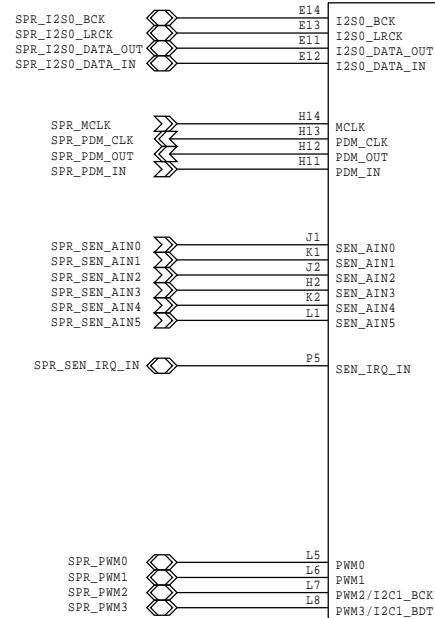
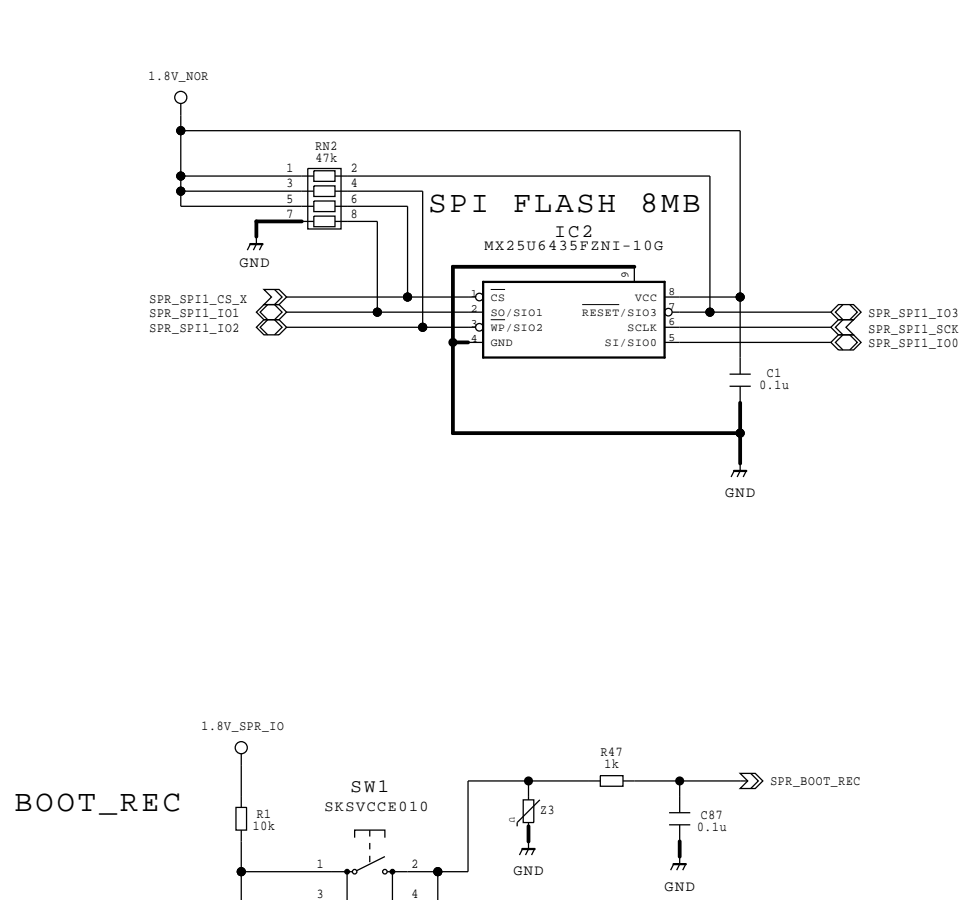
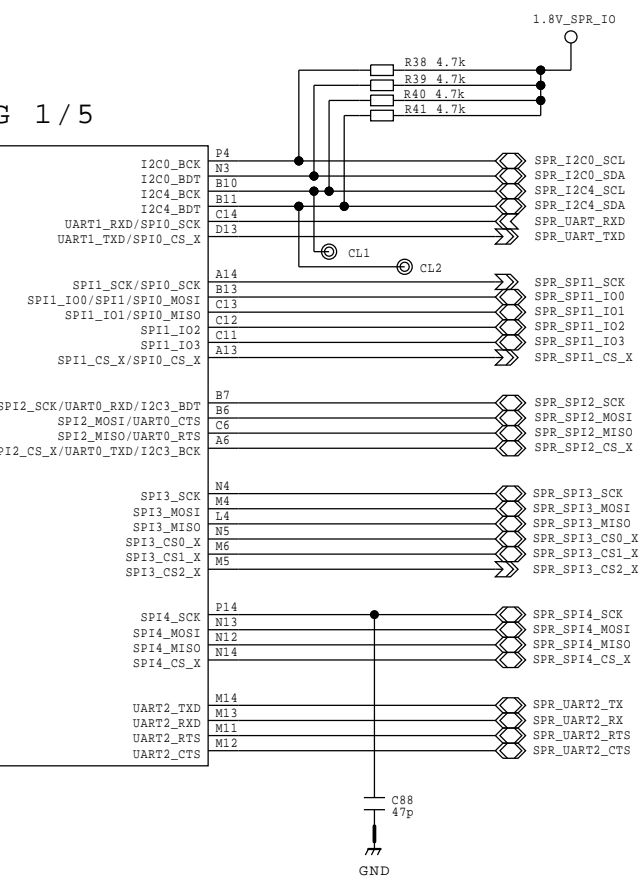
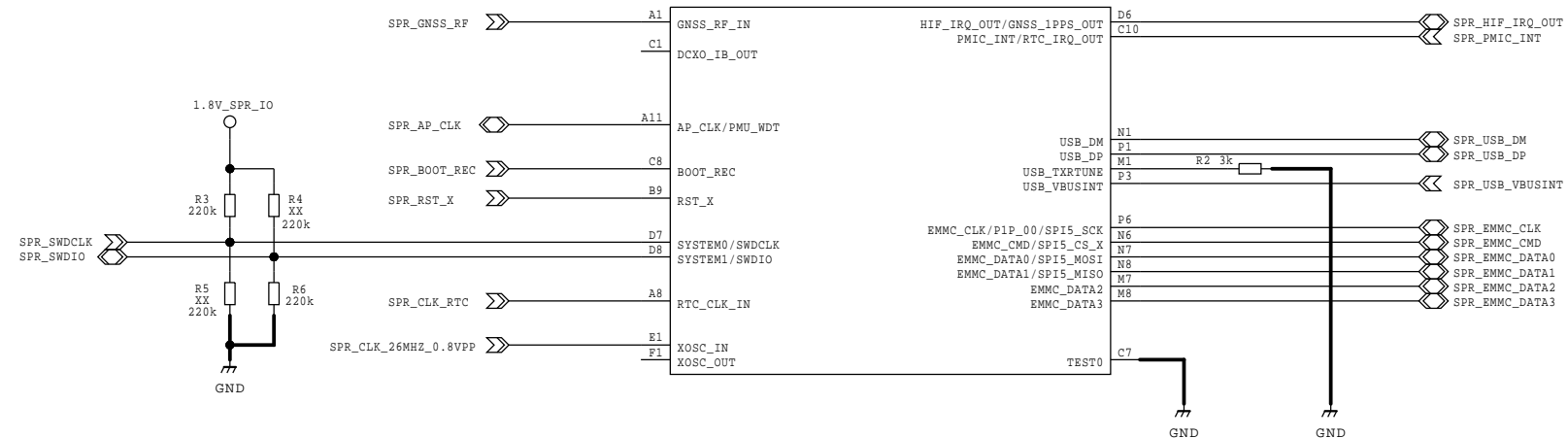
B

C

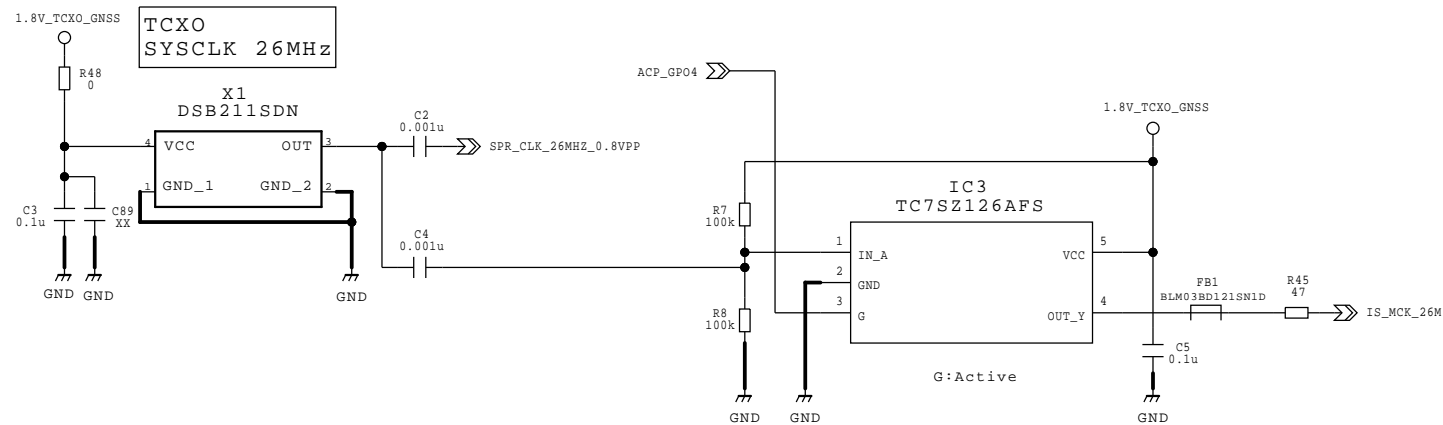
D

E

F

IC1  
CXD5602GG 1/5IC1  
CXD5602GG 2/5

Note) "XX" stands for NM



Purchase recycled resins and wire rods only from the business partners that Sony approves as Green Partners.  
再生樹脂・被覆電線はグリーンパートナー認定取引先から調達すること。

△3 x						GENERAL TOLERANCE		DATE OF ISSUE	
△2 x								DOCUMENT TYPE	
△1 x								CIRCUIT DIAGRAM	
HISTORY X COUNT		SUFFIX /REPL.		RELEASE NO. DATE		REVISION		SIGN.	
LEGAL OWNER						MATERIAL(COLOR)		FINISH(COLOR) 2018.7.10	
RESPONSIBLE DEPARTMENT						SIZE A3		UNIT mm	
PLANNED BY						CHECKED BY		APPROVED BY	
DRAWN BY						PRJ.METHOD		TENTATIVE PART NO.	
TENTATIVE MODEL SPRESENSE						DESCRIPTION (E) Main Board (CXD5602PWBMAIN1)		DESCRIPTION (J)	
PART NO.						PART NO.		SHEET/PAGES 1/8	

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ALL resistors are in ohms,  
unless otherwise noted.

ALL capacitors are in uF(p.pF)  
unless otherwise noted.

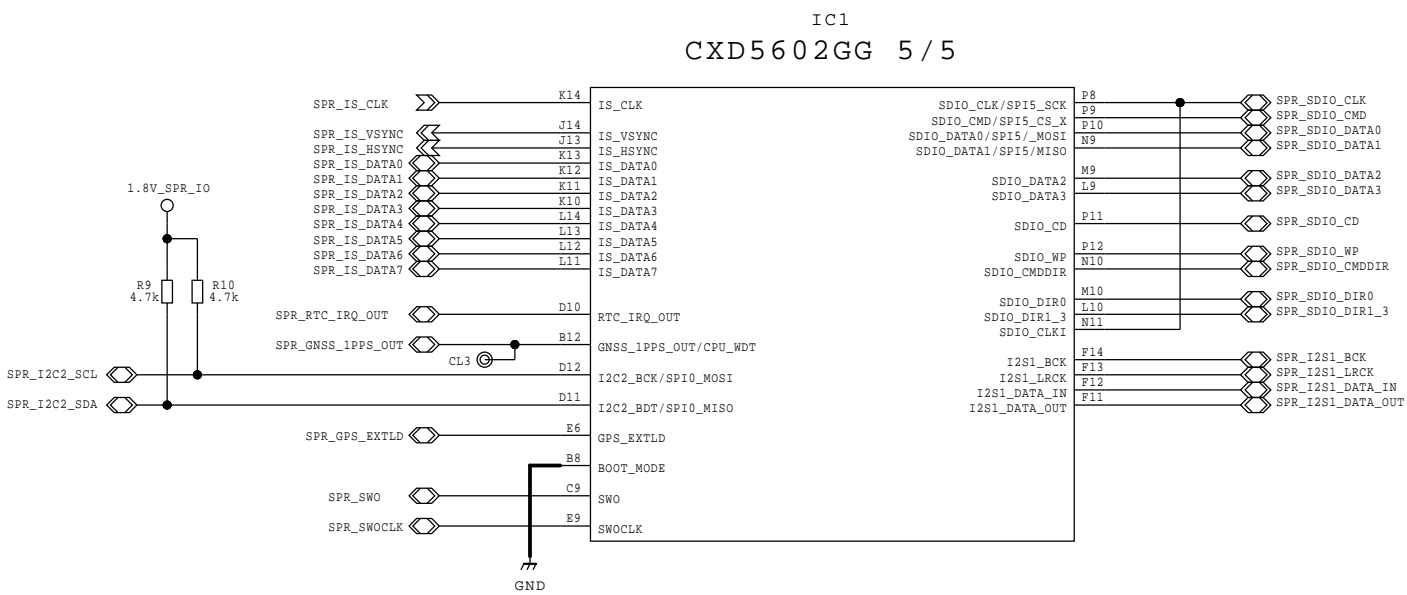
ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	Main Board (CXD5602PWBMAIN1)	
PART NO.	2018.7.10	SHEET
		2 / 8

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ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	Main Board (CXD5602PWBMAIN1)	
PART NO.	2018.6.26	SHEET 3 / 8

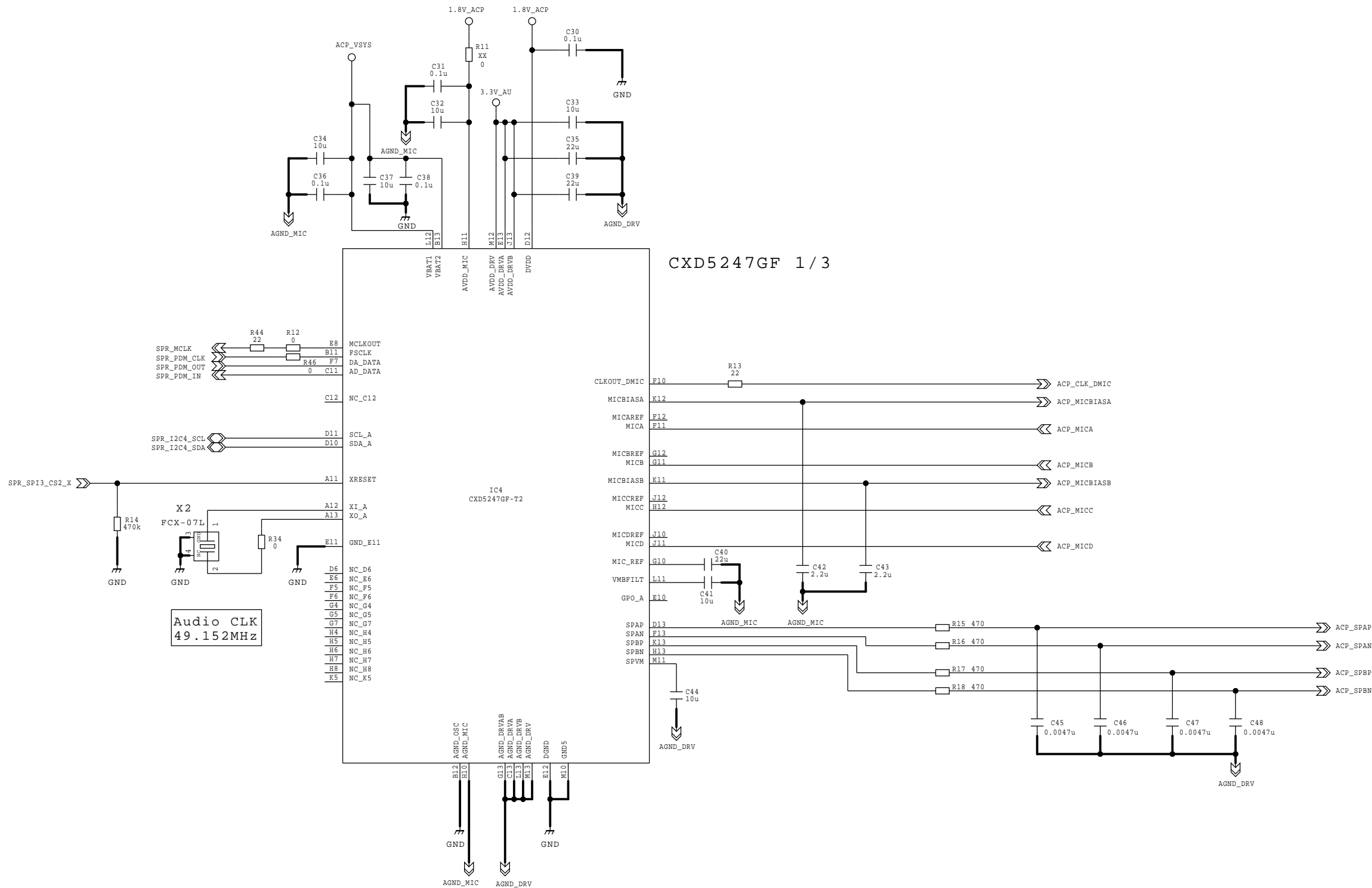


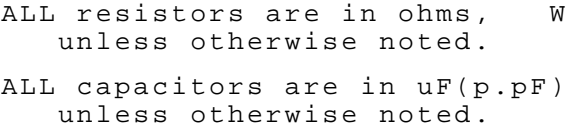
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ALL capacitors are in uF(p.pF)  
unless otherwise noted.

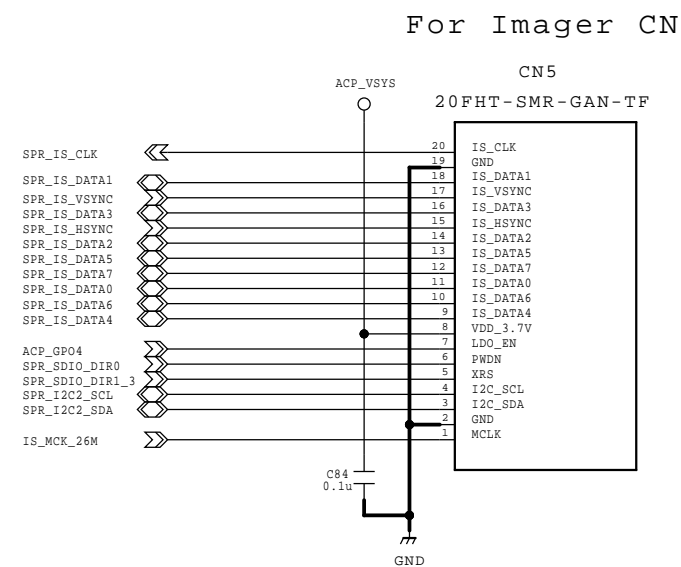
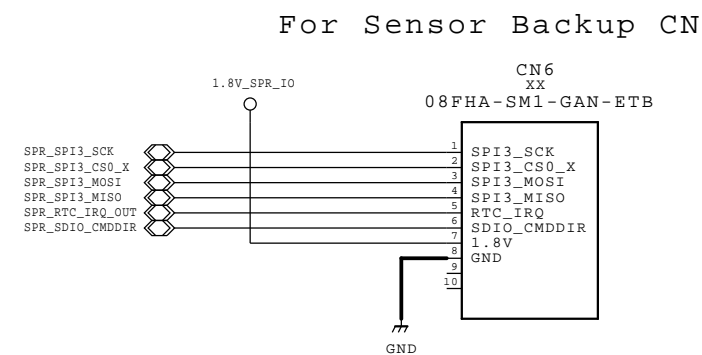
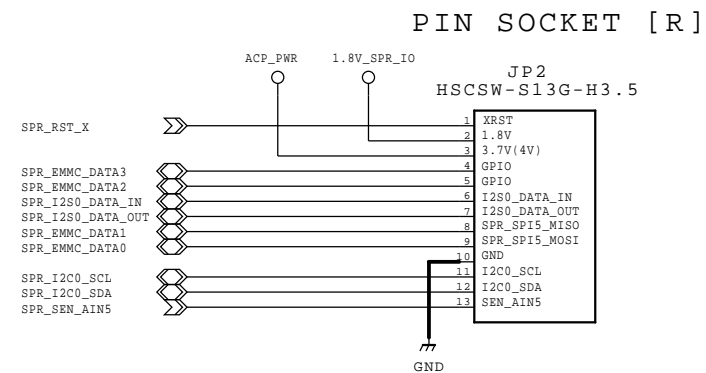
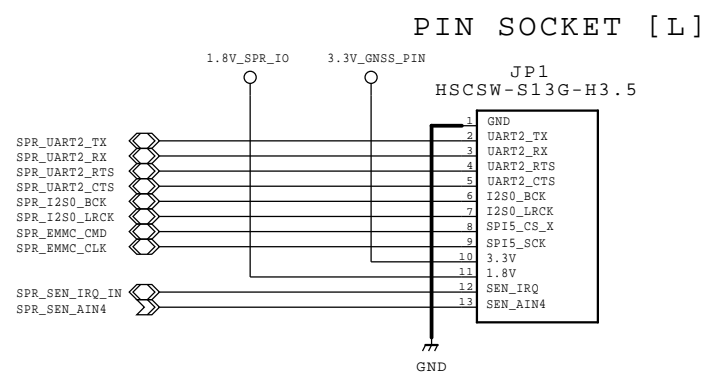
ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	Main Board (CXD5602PWBMAIN1)	
PART NO.	2018.7.10	SHEET
		4 / 8



A 3







ALL resistors are in ohms, W  
unless otherwise noted.

ALL capacitors are in uF(p.pF)  
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
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DESCRIPTION		
Main Board (CXD5602PWBMAIN1)		
PART NO.	2018.7.9	SHEET  8 / 8

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