-	D - t		D:		N		r III INO. OII	1			1		A.C		A.C			D's assess			CVDECOSO	O saint franchisco			F	
h	Ref	l, re	Pin	Name on schematic	Name on	Name on SDK	SDK	Туре	dir	Voltage	After turni	ng on the power	<u> </u>	ı			Connection	Pin group		1.0	1	G pin function		Maximum absolute rating Voltage(V)	Features on the	explanation
Main Ext	Cam	LIE	No.	ONE	ArduinoIDE		ピン釆早	-			dir	Init val	dir	init val	dir ir	nit val		Mode name	doc.	mode0	mode1	mode2	mode3	rating voltage(v)	extension board	
JP1			1	GND		- DIN HADTO TVD	-	Power	-	1.0		-	_	-	 -		- 0VDE00000		_ D1	-	- - -	_	-	0.5		
JP1			2	UART2_TX	D01	PIN_UART2_TXD	67	Digital	1/0	1.8	 -	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	UART2	P1n	GPIO OPIO	UART2_TXD	_	GPI0	2.5		
JP1			3	UART2_RX	D00	PIN_UART2_RXD	68	Digital	1/0	1.8	 -	Hi-Z	_	Hi-Z	+ - +	Hi-Z	CXD5602GG	UART2	P1n	GPIO OPIO	UART2_RXD	_	GPIO OPIO	2.5		
JP1			4	UART2_RTS	D28	PIN_UART2_RTS	70	Digital	1/0	1.8	 -	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	UART2	P1n	GPIO CPIO	UART2_RTS	_	GPIO GPIO	2.5		
JP1			5	UART2_CTS	D27	PIN_UART2_CTS	69	Digital	1/0	1.8	 -	Hi-Z		Hi-Z		Hi-Z	CXD5602GG	UART2	P1n	GPIO CPIO	UART2_CTS	_	GPIO GPIO	2.5		
JP1			7	I2SO_BCK	D26	PIN_I2SO_BCK	93	Digital	1/0	1.8	<u> </u>	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	12\$0	P1v	GPIO CDIO	I2SO_BCK	_	GPIO CRIO	2.5		
JP1			0	I2S0_LRCK	D25	PIN_I2SO_LRCK	94	Digital	1/0	1.8	 -	Hi-Z		Hi-Z	 -	HI-Z	CXD5602GG	12S0	P1v	GPIO CRIO	I2SO_LRCK	- CDIE CC V	GPIO GPIO	2.5		
JP1			0	SPI5_CS_X	D24	PIN_SPI5_CS_X	76	Digital	1/0	1.8	<u> </u>	Hi-Z		Hi-Z		ΠI-Z	CXD5602GG	EMMCA EMMCA	P1p	GPIO CRIO	EMMC_CMD	SPI5_CS_X	GPIO GPIO	2.5		
JP1			10	SPI5_SCK	D23	PIN_SPI5_SCK	75	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	ПІ-Д	CXD5602GG		P1p _	GPIO	EMMC_CLK _	SPI5_SCK		2.5		
JP1 JP1			10	3.3V	 		_	Power	0	3.3	0		0	_	0			_		_		_				
			12	1.8V			27	Power	1/0	1.8	0		U		+ + +	11: 7			-					2.5		
JP1			12	SEN_IRQ	D22	PIN_SEN_IRQ_IN	31	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SEN_IRQ_IN	P1e	GPIO	SEN_IRQ_IN	_		2.5		
JP1			15	SEN_AIN4	A2	LPADC2	_	Analog	1	~0.7	'	1 000	0				CXD5602GG	_	_	_		_		1.05		
JP2			1	XRST		(SPR_RST_X)		Digital	0	1.8	_	Low	0	High	0	High	CXD5602GG		_	_		_				
JP2			2	1.8V	_	_	_	Power	1/0	1.8	0	_	0	_	0		_	_	_	_	_	_	_	7		
JP2			3	3.7V(4.0V)		- DINI ENAMO DATAO	-	Power	1/0	3.6-4.4	0	-	0	-	0		- -	-	_ D1	-	- - -	_	-	7		
JP2			4	GPIO	D21	PIN_EMMC_DATA3	80	Digital	1/0	1.8	 -	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	EMMCB	P1q	GPIO OPIO	EMMC_DATA3	_	GPIO	2.5		
JP2	1	1	5	GPIO	D20	PIN_EMMC_DATA2	/9	Digital	1/0	1.8	<u> </u>	Hi-Z	_	Hi-Z	 -	Hi-Z	CXD5602GG	EMMCB	P1q	GPIO CPIO	EMMC_DATA	_	GPIO GPIO	2.5		
JP2			6	I2SO_DATA_IN	D19	PIN_I2SO_DATA_IN	95	Digital	1/0	1.8	 -	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	12\$0	P1v	GPIO OPIO	I2SO_DATA_OUT	_	GPIO OPIO	2.5		
JP2	1	1	/	I2SO_DATA_OUT	D18	PIN_I2SO_DATA_OUT	96	Digital	1/0	1.8	<u> </u>	Hi-Z	_	Hi-Z	 -	HI-Z	CXD5602GG	12S0	P1v	GPIO CDIO	I2SO_DATA_OUT	- CDIE MICO	GPIO GPIO	2.5		
JP2	1		δ	SPR_SPI5_MISO	D17	PIN_SPI5_MISO	18	Digital	1/0	1.8	 -	Hi-Z	_	Hi-Z	+ - +	HI-Z	CXD5602GG	EMMCA	P1p	GPIO CDIO	EMMC_DATA1	SPI5_MISO	GPIO GPIO	2.5		
JP2	1		10	SPR_SPI5_MOSI	D16	PIN_SPI5_MOSI	//	Digital	1/0	1.8	 -	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_DATA0	SPI5_MOSI	GPIO	2.5		
JP2	1	1	10	GND			_	Power	-	1.0	<u> </u>	- 11: -1-	-	- 11:	-		- CVDFC0000	-	- D1:	-	- 1200 DOK	_	_			
JP2	-		11	I2C0_SCL	D15	PIN_I2CO_BCK	44	Digital	1/0	1.8	 -	High		High		High	CXD5602GG	12C0	P1j	GPIO OPIO	I2C0_BCK	_	_	2.5		
JP2			12	I2C0_SDA	D14	PIN_I2CO_BDT	45	Digital	1/0	1.8	 -	High		High		High	CXD5602GG	12C0	P1j	GPIO	I2C0_BDT	_	_	2.5		
JP2	01:1		13	SEN_AIN5	A3	LPADC3	_	Analog		~0.7	 			-			CXD5602GG	_	_	_	_	_	_	1.05		
CN5	CN1		1	MCLK	_	_	_	Digital	0	1.8	 -	_	0	Hi-Z	0	Hi-Z	26MHz TCXO	-	_	_	_	_	_	2.5		
CN5	CN1	1	2	GND			_	Power	-	-		-	_		-	_	- 0\/D50000	-	-	- ODIO	-	-	_ 			
CN5	CN1		3	I2C_SDA	_	PIN_SPI0_MISO	20	Digital	+	1.8	_	High	_	High		High	CXD5602GG	12C2	P17	GPIO	I2C2_BDT	SPI0_MISO	GPIO	2.5		
CN5	CN1		4	I2C_SCL	_	PIN_SPI0_MOSI	19	Digital	+	1.8		High	-	High		High	CXD5602GG	12C2	P17	GPIO	I2C2_BCK	SPI0_MOSI	GPIO	2.5		
CN5	CN1		5	XRS	D35	PIN_SDIO_DIR1_3	91	Digital	+ .	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SDIOC	P1t	GPIO	SDIO_DIR1_3	GPI0	GPIO	2.5		
CN5	CN1		6	PWDN	D34	PIN_SDIO_DIR0	90	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	SDIOC	P1t	GPIO	SDIO_DIR0	GPI0	GPI0	2.5		
CN5	CN1		7	LDO_EN	_	(ACP_GPO4)	_	Digital	0	3.6-4.4	+	Hi-Z	0	Low	0	Low	CXD5247GF	-	_	_	_	_	_			
CN5	CN1		8	VDD_3.7V	_	-	_	Power	0	3.6-4.4	0	_	0	_	0	_	_	-	_	_	_	-	_			
CN5	CN1		9	IS_DATA4	_	PIN_IS_DATA4	63	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA4	GPIO	GPIO	2.5		
CN5	CN1		10	IS_DATA6	_	PIN_IS_DATA6	65	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA6	GPIO	GPIO	2.5		
CN5	CN1		11	IS_DATA0	_	PIN_IS_DATA0	59	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	1	_	CXD5602GG	IS	P1m	GPIO	IS_DATA0	GPIO	GPIO	2.5		
CN5	CN1		12	IS_DATA7	_	PIN_IS_DATA7	66	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	ı	_	CXD5602GG	IS	P1m	GPIO	IS_DATA7	GPIO	GPIO	2.5		
CN5	CN1		13	IS_DATA5	_	PIN_IS_DATA5	64	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA5	GPIO	GPIO	2.5		
CN5	CN1		14	IS_DATA2	_	PIN_IS_DATA2	61	Digital	1/0	1.8		Hi-Z	_	Hi-Z	ı	_	CXD5602GG	IS	P1m	GPIO	IS_DATA2	GPIO	GPIO	2.5		
CN5	CN1		15	IS_HSYNC	_	PIN_IS_HSYNC	58	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	ı	_	CXD5602GG	IS	P1m	GPIO	IS_HSYNC	GPIO	GPIO	2.5		
CN5	CN1		16	IS_DATA3	_	PIN_IS_DATA3	62	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	ı	_	CXD5602GG	IS	P1m	GPIO	IS_DATA3	GPIO	GPIO	2.5		
CN5	CN1		17	IS_VSYNC	_	PIN_IS_VSYNC	57	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	I	_	CXD5602GG	IS	P1m	GPIO	IS_VSYNC	GPIO	GPIO	2.5		
CN5	CN1		18	IS_DATA1	_	PIN_IS_DATA1	60	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA1	GPI0	GPIO	2.5		
CN5	CN1		19	GND	_	-	_	Power	_		_		_	_	<u> </u>	_	-	-		-	-	-	_			
CN5	CN1		20	IS_CLK	_	PIN_IS_CLK	56	Digital		1.8		_	I	_		_	CXD5602GG	IS	P1m	GPIO	IS_CLK	GPIO	GPIO	2.5		
CN4[L] CN4[L]		CN4[L]	1	3.3V_AU	_	-	_	Power	0	3.3	I	_	I	_	ı	_	-	_	_	_	_	_				
CN4[L] CN4[L]		CN4[L]	3	3.3V_AU	_	-		Power	0	3.3	Ι	_	I	_	ı		-	-	_	_	_	_	_			
CN4[L] CN4[L]		CN4[L]	5	ACP_MICA	_	-	_	Analog	I	_	I	_	I	_		_	CXD5247GF	-	_	_	_	-	_			
CN4[L] CN4[L]		CN4[L]	7	ACP_MICB	_	-	_	Analog		_		_	I	_		_	CXD5247GF	_	_	_	_	_	_			
CN4[L] CN4[L]		CN4[L]	9	ACP_MICC	_	-		Analog	Ι	_	Ι	_	I	_	ı		CXD5247GF	_	_	_	_	_	_			
CN4[L] CN4[L]		CN4[L]	11	ACP_MICD	_	-	_	Analog	١	_	I	_	I	_	ı	_	CXD5247GF	_	_	_	_	_	_			
CN4[L] CN4[L]		CN4[L]	13	ACP_MICBIASA	_	-	_	Analog	0	2	0		0		0	_	CXD5247GF	_	_	_	_	_	_			
CN4[L] CN4[L]		CN4[L]	15	ACP_MICBIASB	_	-	_	Analog	0	2	0	_	0	_	0	_	CXD5247GF	_	_	_	_	_	_			
CN4[L] CN4[L]		CN4[L]	17	AGND_MIC	_	-		Power	_	_	_	_		_	<u> </u>	_	_	_	_	_	_	_	_			
CN4[L] CN4[L]		CN4[L]	19	SPR_I2C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0	1.8	_	High	I	High	I	High	CXD5602GG	12C0	P1j	GPIO	I2C0_BCK	-	_	2.5		
CN4[L] CN4[L]		CN4[L]	21	SPR_I2C0_SDA	D14	PIN_I2C0_BDT	45	Digital	1/0	1.8	_	High	Ι	High		High	CXD5602GG	12C0	P1j	GPIO	I2C0_BDT	-	_	2.5		
CN4[L] CN4[L]		CN4[L]	23	SPR_SPI4_SCK	D13	PIN_SPI4_SCK	72	Digital	1/0	1.8		Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_SCK	_	GPIO	2.5		
CN4[L] CN4[L]		CN4[L]	25	SPR_SPI4_MISO	D12	PIN_SPI4_MISO	74	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_MISO	-	GPIO	2.5		
CN4[L] CN4[L]		CN4[L]	27	SPR_SPI4_MOSI	D11	PIN_SPI4_MOSI	73	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_MOSI	-	GPIO	2.5		
CN4[L] CN4[L]		CN4[L]	29	SPR_SPI4_CS_X	D10	PIN_SPI4_CS_X	71	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_CS_X	_	GPIO	2.5		
CN4[L] CN4[L]		CN4[L]	31	SPR_PWM2	D09	PIN_PWM2	48	Digital	1/0	1.8	-	Hi-Z	_	Hi-Z	<u> </u>	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM2	I2C1_BCK	_	2.5		
CN4[L] CN4[L]		CN4[L]	33	SPR_I2S0_LRCK	D25	PIN_I2S0_LRCK	94	Digital	1/0	1.8	-	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	12S0	P1v	GPIO	I2S0_LRCK	_	GPIO	2.5		
CN4[L] CN4[L]		CN4[L]	35	GND	_		_	Power	<u> </u>	_	<u> </u>	_	_	_	1 – 1	_	_	_	_	_	_	-	_			
CN4[L] CN4[L]	1	CN4[L]	37	SPR_I2S0_DATA_OUT	Γ D18	PIN_I2S0_DATA_OUT	96	Digital	1/0	1.8	† –	Hi-Z	_	Hi-Z	1 - 1	Hi-Z	CXD5602GG	12S0	P1v	GPIO	I2S0_DATA_OUT	_	GPIO	2.5		
CN4[L] CN4[L]		CN4[L]	39	SPR_PWM0	D06	PIN_PWM0	46	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	 	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM0	_	_	2.5		
CN4[L] CN4[L]		CN4[L]	41	SPR_PWM1	D05	PIN_PWM1	47	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	 	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM1	GPIO	_	2.5		
CN4[L] CN4[L]		CN4[L]	43	SPR_I2SO_DATA_IN	-	PIN_I2SO_DATA_IN	95	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	 	Hi-Z	CXD5602GG	1280	P1v	GPIO	I2S0_DATA_IN	— — — — — — — — — — — — — — — — — — —	GPIO	2.5		
CN4[L] CN4[L]	+	CN4[L]	45	SPR PWM3	D03	PIN PWM3	49	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	+ _ +	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM3	I2C1_BDT		2.5		
CN4[L] CN4[L]		+		SPR_I2S0_BCK	D26	PIN_I2S0_BCK	93				 		_		+ - +		CXD5602GG		P1v	GPIO	I2S0_BCK		GPIO	2.5		
OINT[L]		OINTLL	-T1	01 1_1200_DON	1 520	1 117 17 20 70 1/	J.J.	ואואים	I '/ U	I 1.0	1	I '''-Z	1	I 111-Z		111 L	J/1000700	1200	Ι ' τν	1 4110	1,500_001/	<u> </u>	OI IU	2.3		

CNI4[L] CNI4[L]	CNIA[L]	40	CDD LIADT2 TV	D01	DINI HADTO TV	67	Digital	1/0	1 0		11: 7	T	11: 7		11: 7	CADECUSCO	UART2	D1 n	CDIO	LIADTO TV		CDIO	2.5	1
CN4[L] CN4[L] CN4[L]	CN4[L]	49 51	SPR_UART2_TX SPR_UART2_RX	D01 D00	PIN_UART2_TX PIN_UART2_RX	67 68	Digital Digital	1/0	1.8	_	Hi-Z Hi-Z		Hi-Z Hi-Z	_	Hi-Z	CXD5602GG CXD5602GG	UART2 UART2	P1n P1n	GPIO GPIO	UART2_TX UART2_RX		GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	53	SPR_UART2_RTS	D28	PIN_UART2_RTS	70	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z		Hi-7	CXD5602GG	UART2	P1n	GPIO GPIO	UART2_RTS	_	GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L]		SPR_UART2_CTS	D27	PIN_UART2_CTS	69	Digital	1/0	1.8	_	Hi-Z		Hi-Z		Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2 CTS	_	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	57	SPR_EMMC_CLK	D23	PIN_EMMC_CLK	75	Digital	1/0	1.8	_	Hi-Z		Hi-Z		Hi-Z	CXD5602GG	EMMC A	P1p	GPIO	EMMC_CLK	SPI5_SCK	GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	59	SPR_EMMC_CMD	D24	PIN_EMMC_CMD	76	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC A	P1p	GPIO	EMMC_CMD	SPI5_CS_X	GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	61	SPR_EMMC_DATA0	D16	PIN EMMC DATA0	77	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC A	P1p	GPIO	EMMC_DATA0	SPI5_MOSI	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	63	SPR_EMMC_DATA1	D17	PIN_EMMC_DATA1	78	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC A	P1p	GPIO	EMMC_DATA1	SPI5_MISO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]		SPR_EMMC_DATA2	D20	PIN_EMMC_DATA2	79	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC B	P1q	GPIO	EMMC_DATA2	_	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	67	SPR_EMMC_DATA3	D21	PIN_EMMC_DATA3	80	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC B	P1q	GPIO	EMMC_DATA3	_	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	69	GND		-	_	Power	-	_	_	-	_	_	_	_	-	-	-	-	_	_	-		
CN4[L] CN4[L]	CN4[L]	71	SPR_SPI2_SCK	D43	PIN_SPI2_SCK	28	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2A	P00	GPIO	SPI2_SCK	UART0_RXD	I2C3_BDT	2.5	
CN4[L] CN4[L]	CN4[L]	73	SPR_SPI2_MISO	D08	PIN_SPI2_MISO	30	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MISO	UARTO RTS	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	75	SPR_SPI2_MOSI	D04	PIN_SPI2_MOSI	29	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MOSI	UARTO_CTS	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	77	SPR_SPI2_CS_X	D42	PIN_SPI2_CS_X	27	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2A	P00	GPIO	SPI2_CS_X	UART0_TXD	I2C3_BCK	2.5	
CN4[L] CN4[L]	CN4[L]	79	1.8V	_		_	Power	0	1.8	0	_	0	_	0	_	_	_	_	_			_		
CN4[L] CN4[L]	CN4[L]	81	SPR_SDIO_CMDDIR	D33	PIN_SDIO_CMDDIR	89	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Hi-Z	CXD5602GG	SDIOC	P1t	GPIO	SDIO_CMDDIR	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	83	SPR_SDIO_CLK	D38	PIN_SDIO_CLK	81	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1u	GPIO	SDIO_CLK	SPI5_SCK	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]		SPR_SDIO_CMD	_	PIN SDIO CMD	82	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Hi-Z	CXD5602GG	SDIOA	P1r	GPIO	SDIO_CMD	SPI5_CS_X	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]		SPR_SDIO_DATA0	_	PIN_SDIO_DATA0	83	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA0	SPI5_MOSI	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	89	SPR_SDIO_DATA1	_	PIN_SDIO_DATA1	84	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA1	SPI5_MISO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	91	SPR_SDIO_DATA2	_	PIN_SDIO_DATA2	85	Digital	1/0	1.8	_	Hi-Z		Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA2	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	93	SPR_SDIO_DATA3	_	PIN_SDIO_DATA3	86	Digital	1/0	1.8	_	Hi-Z		Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA3	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]		GND	_	_	_	Power	_	_	_	_		_	-	_	_	_	_	_	_	_	_		
CN4[L] CN4[L]	CN4[L]	97	SPR_SDIO_WP	D37	PIN_SDIO_WP	88	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SDIOB	P1s	GPIO	SDIO_WP	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	99	GND	_		_	Power	_	_	_	_	_	_	-	_	_	_	_	-	_	_	_		
CN4[R] CN4[R]	CN4[R]	2	5V	_	_	_	Power	1/0	5	0	_	0	_	0	_	_	_	_	-	_	_	_	6	
CN4[R] CN4[R]	CN4[R]		5V	_	_	_	Power	1/0	5	0	_	0	_	0	_	_	_	_	-	-	_	_	6	
CN4[R] CN4[R]	CN4[R]	6	ACP_SPAP	_	-	_	Analog	0	3.3	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5247GF	_	_	_	_	_	_		
CN4[R] CN4[R]	CN4[R]		ACP_SPAN	_	_	_	Analog	0	3.3	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5247GF	_	_	-	-	_	_		
CN4[R] CN4[R]	CN4[R]	10	ACP_SPBN	_	_	_	Analog	0	3.3	- 1	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5247GF	_	_	_	-	_	_		
CN4[R] CN4[R]	CN4[R]	12	ACP_SPBP	_	-	_	Analog	0	3.3	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5247GF	-	_	_	_	_	_		
CN4[R] CN4[R]	CN4[R]	14	AGND_DRV	_	_	_	Power	_	_	- 1	_	-	-	-	_	_	_	_	_	-	_	_		
CN4[R] CN4[R]	CN4[R]	16	SPR_SWDIO	_	-	_	Digital	1/0	1.8	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	_	_	_	_	_	_	2.5	
CN4[R] CN4[R]	CN4[R]	18	SPR_SWDCLK	_	-	_	Digital	I	1.8	I	Hi-Z	ı	Hi-Z	I	Hi-Z	CXD5602GG	-	_	-	-	_	_	2.5	
CN4[R] CN4[R]	CN4[R]	20	ACP_CLK_DMIC	_	-	_	Digital	0	1.8	0	Hi-Z	0	Hi-Z	0	Hi-Z	CXD5247GF	-	_	_	-	_	_		
CN4[R] CN4[R]	CN4[R]	22	XRS_PWON	_	_	_	Power	1/0	3.3	1/0	Low	1/0	Low	1/0	Low	CXD5247GF	_	_	_	_	_	_	7	
CMA[D] CMA[D]	CNA[D]	2.4	CDD CNCC 1DDC OUT	D44	DIN CNCC 1DDC OUT	C	D: «:+ a.l	1/0	1.0		11: 7		11: 7		11: 7	CADECUSOC	CNCC 1DDC OUT	D1/	CDIO	CNCC 1DDC OUT	CDII WDT	CPU_WDT	2.5	
CN4[R] CN4[R]	CN4[R]	24	SPR_GNSS_1PPS_OUT	D44	PIN_GNSS_1PPS_OUT	б	Digital	1/0	1.8	-	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	GNSS_1PPS_OUT	P14	GPIO	GNSS_1PPS_OUT	CPU_WDT	(Open Drain)	2.5	
CN4[R] CN4[R]	CN4[R]	26	SPR_SEN_IRQ_IN	D22	PIN_SEN_IRQ_IN	37	Digital	1/0	1.8	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	SEN_IRQ_IN	P1e	SEN_IRQ_IN	SEN_IRQ_IN	SEN_IRQ_IN	SEN_IRQ_IN	2.5	
	CN4[D]	20	CDD LUE IDO OUT	D02	DIN LUE IDO OUT	21	Distrib	1/0	1.0		11: 7		11: 7		11: 7	CVDECOSCO	LUEIDO	DOO	CDIO	LUE IDO OUT	HIF_IRQ_OUT	CNICC 1DDC OUT	2.5	
CN4[R] CN4[R]	CN4[R]	28	SPR_HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0	1.8	_	Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	HIFIRQ	P02	GPIO	HIF_IRQ_OUT	(Open Drain)	GNSS_1PPS_OUT	2.5	
CN4[R] CN4[R]	CN4[R]	30	GND	_	_	_	Power	-	_	_	_	-	-	-	_	_	-	_	_	-	_	_		
CN4[R] CN4[R]	CN4[R]	32	SPR_RST_X	_	_	_	Digital	0	1.8	_	Low	0	High	0	High	CXD5602GG	-	_	_	-	_	_		
CN4[R] CN4[R]	CN4[R]	34	SPR_AP_CLK	D40	DIN AD CLIC	E	Digital	1/0	1.8		Hi-Z		Hi-Z		Hi-Z	CXD5602GG	AD CLK	P13	GPIO	AD CLK	PMU_WDT	PMU_WDT	2.5	
CN4[K] CN4[K]	CN4[R]	54	SPK_AP_CLK	D40	PIN_AP_CLK	5	Digital	1/0	1.0	_	ПІ-Д	_	ПІ-Д	_	ПІ-Д	CADSOUZGG	AP_CLK	P13	GPIO	AP_CLK	PIVIU_VVD1	(Open Drain)	2.5	
CN4[R] CN4[R]	CN4[R]	36	SPR_SEN_AIN2	Α0	LPADC0		Analog	I	0.7	I	_	I	_	I	_	CXD5602GG		_	_	-	_		1.05	
CN4[R] CN4[R]	CN4[R]	38	SPR_SEN_AIN3	A1	LPADC1		Analog	I	0.7	I				I	_	CXD5602GG			_				1.05	
CN4[R] CN4[R]	CN4[R]	40	SPR_SEN_AIN4	A2	LPADC2		Analog	Ι	0.7	I	_	Ι	_	I	_	CXD5602GG	_	_	_	-	_		1.05	
CN4[R] CN4[R]	CN4[R]	42	SPR_SEN_AIN5	А3	LPADC3		Analog	Ι	0.7	I	_	Ι	_	I	_	CXD5602GG	_	_	_	-	_	_	1.05	
CN4[R] CN4[R]	CN4[R]	44	SPR_SEN_AIN0	A4	HPADC0		Analog	I	1.4		_	ı	_	ı	_	CXD5602GG	-	_	_	-	_	_	2.5	
CN4[R] CN4[R]	CN4[R]	46	SPR_SEN_AIN1	A5	HPADC1	_	Analog		1.4		_	I	_	ı	_	CXD5602GG	_	_	_	_	_	_	2.5	
CN4[R] CN4[R]	CN4[R]	48	GND	_	_	_	Power	_	_	_	_		_	_	_	_	_	_	_	_	_	_		
CN4[R] CN4[R]	CN4[R]	50	SPR_SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0	1.8	-	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	_	-	2.5	
CN4[R] CN4[R]	CN4[R]		SPR_SPI3_MOSI	D31	PIN_SPI3_MOSI	42	Digital	1/0	1.8	-	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_MOSI	_	-	2.5	
CN4[R] CN4[R]	CN4[R]		SPR_SPI3_SCK	D29	PIN_SPI3_SCK	41	Digital	1/0	1.8	-	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_SCK	_	-	2.5	
CN4[R] CN4[R]	CN4[R]		SPR_SPI3_MISO	D30	PIN_SPI3_MISO	43	Digital	1/0	1.8	-	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_MISO	_	-	2.5	
CN4[R] CN4[R]	CN4[R]	58	SPR_SPI3_CS0_X	D32	PIN_SPI3_CS0_X	38	Digital	1/0	1.8	-	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	SPI3_CS0_X	P1f	GPIO	SPI3_CS0_X	_	-	2.5	
CN4[R] CN4[R]	CN4[R]	60	SPR_RTC_IRQ_OUT	D41	PIN_RTC_IRQ_OUT	4	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	RTC_IRQ_OUT	P12	GPIO	RTC_IRQ_OUT	RTC_IRQ_OUT	GPIO	2.5	
				レイエ			Digital	1, 0			111 4		111 4				00	1 14	31 10	٥	(Open Drain)	G1 10		
CN4[R] CN4[R]	CN4[R]	62	SPR_USB_DM	_	_	-	Analog	1/0	3.3	_	_	-	_	-	-	CXD5602GG	-	_	_	-	_	_	5.25	
CN4[R] CN4[R]	CN4[R]	64	SPR_USB_DP	_	_		Analog	1/0	3.3	_	_	-	_	-	_	CXD5602GG	-	_	_	-	_	_	5.25	
CN4[R] CN4[R]	CN4[R]	66	SPR_GPS_EXTLD	D39	PIN_HIF_GPI00	32	Digital	1/0	1.8		Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	HIF_GPI00	P03	GPIO	GPI0	GPIO	GPS_EXTLD	2.5	
CN4[R] CN4[R]	CN4[R]		GND	_	_	_	Power	_	_	_	_	-	_	-	_	-	-	_	_	_	_	_		
CN4[R] CN4[R]	CN4[R]		ACP_GPO5	_	_	_	Digital	0	3.6-4.4		Hi-Z	0	Low	0	Low	CXD5247GF	_	-	_	_	_	_		
CN4[R] CN4[R]	CN4[R]		ACP_GPO6	_	_	_	Digital	0	3.6-4.4	-	Hi-Z	0	Low	0	Low	CXD5247GF	_	-	_	_	_	_	Headphone mute	Mute at Low
CN4[R] CN4[R]	CN4[R]		ACP_GPO7	-	_	_	Digital	0	3.6-4.4		Hi-Z	0	Low	0	Low	CXD5247GF	_	1	_	_	_	_		
CN4[R] CN4[R]	CN4[R]		ACP_VSYS	_	_		Power	1/0	3.6-4.4	1/0	_	1/0	_	1/0	_	_	-	_	_	-	_	_	7	
CN4[R] CN4[R]	CN4[R]		ACP_VSYS	-	_	_	Power	1/0	3.6-4.4	1/0	_	1/0	_	1/0	_	-	_	1	_	_	_	_	7	
	CN4[R]	80	GND	_	_	_	Power	_	_	_	_	-	_	-	_	-	_	-	_	_	_	_		
CN4[R] CN4[R]						_		1 1/0	1 0	ı I	ı – T	F	Т	1 =		_				. 7				
CN4[R] CN4[R]	CN4[R]	82	SPR_SDIO_CD	D36	PIN_SDIO_CD	87	Digital	1/0	1.8		Hi-Z		Hi-Z		High	CXD5602GG	SDIOB	P1s	GPIO	SDIO_CD	GPIO	GPIO	2.5	
	CN4[R]	82	SPR_SDIO_CD SPR_I2C2_SDA	D36 -	PIN_SDIO_CD PIN_SPI0_MISO	87 20	Digital Digital			_		_ _	Hi-Z High			CXD5602GG CXD5602GG		P1s P17	GPIO GPIO	SDIO_CD I2C2_BDT	GPIO SPI0_MISO	GPIO –	2.5 2.5	

ON 4[D]	ON4[D]		0.0	000 1000 001		DIN ODIO MOOL	10	I 5 I	1./0	1.0	I		1/0	Ι	1 .	1111	I OVERSOO I	ODIOD	D17	0.010	I 1000 DOI/	000 400		0.5	<u> </u>	
CN4[R]	CN4[R]	CN4[R]	86 88	SPR_I2C2_SCL ACP_GP00	_ 	PIN_SPI0_MOSI	19	Digital Digital	1/0	1.8	_ 	High Hi-Z	1/0	– Hi-Z		High Hi-Z	CXD5602GG CXD5247GF	SPI0B	P17	GPIO _	I2C2_BCK	SPI0_MOSI -	_	2.5		
CN4[R]	CN4[R]	CN4[R]	90	ACP_GP00 ACP GP01			 	Digital	0	3.6-4.4 3.6-4.4	0	Hi-Z	0		0		CXD5247GF CXD5247GF		_	_		_	_		Audio 3.3V output	Output at High
CN4[N]	CN4[N]	CN4[N]	90	ACF_GFU1	_		_	Digital	U	3.0-4.4	U	111-2	0	Low	0	Low	CAD5247GI				_	_	_		Power output for LTE	Output at riigii
CN4[R]	CN4[R]	CN4[R]	92	ACP_GPO2	_	_	_	Digital	0	3.6-4.4	0	Hi-Z	0	Low	0	Low	CXD5247GF	_	_	_	_	_	-		(Only LTE extension)	Output at High
																									LTE power output from	
CN4[R]	CN4[R]	CN4[R]	94	ACP_GPO3	_	_	_	Digital	0	3.6-4.4	0	Hi-Z	0	Low	0	Low	CXD5247GF	_	_	_	_	_	_			Output at High
				_																					(Only LTE extension)	
CN4[R]	CN4[R]	CN4[R]	96	SWOCLK	_	-	_	Digital	0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	_	_	_	_	_	_	2.5		
CN4[R]	CN4[R]	CN4[R]	98	SWO	_	-	_	Digital	0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	_	-	_	-	_	_	2.5		
CN4[R]	CN4[R]	CN4[R]	100	GND	_	_	_	Power	_	-	_	_	_	_	_	_	_	-	_	_	_	_	_			
D7				SPR_I2S1_BCK	LED0	PIN_I2S1_BCK	97	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	I2S1	P1w	GPI0	I2S1_BCK	_	GPIO	2.5		
D6				SPR_I2S1_LRCK	LED1	PIN_I2S1_LRCK	98	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	12S1	P1w	GPI0	I2S1_LRCK	-	GPIO	2.5		
D5				SPR_I2S1_DATA_IN	LED2	PIN_I2S1_DATA_IN	99	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	I2S1	P1w	GPIO	I2S1_DATA_IN	-	GPIO	2.5		
D4				SPR_I2S1_DATA_OUT	LED3	PIN_I2S1_DATA_OUT	100	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	I2S1	P1w	GPI0	I2S1_DATA_OUT	_	GPIO	2.5		
	JP3		1	NC	_	-	_	_	_	-	-	_	_	_	_	_	_		_	_	_	_				
	JP3		2	VDD_LVS	_	_	_	Power	0	5/3.3	0	_	0	_	0	_	_		_	_	-	_	_			
	JP3		3	XRS_PWON	_	_	_	Digital	1/0	3.3	1/0	_	1/0	_	1/0	_	_	_	_	_	_	_	_	7		
	JP3		4	3.3V	_	_	_	Power	0	3.3	0	_	0		0	_	_	_	_	_	_	_	_			
	JP3		5 .c	MAIN_POWER	_			Power	1/0	5	1/0	_	1/0	_	1/0	_	_		_			_	_	ь		
	JP3 JP3		η 7	GND GND	-		_	Power	_		_		<u> </u>		_	_	_					_	_			
	JP3		l Q	5V IN PIN			 	Power _			_		-		_		_		_	_	-	_	_			
	JP4		1	SPR_SEN_AIN2	_ A0	LPADC0		- Analog	- 	~5	_ 	_	I	_	1	_	CXD5602GG		_	SEN AIN2	SEN_AIN2	SEN_AIN2	SEN_AIN2	7.5		
	JP4		2	SPR_SEN_AIN3	A1	LPADC1	_	Analog		~5	'	_	'	_	'	_	CXD5602GG		_	SEN_AIN3	SEN_AIN3	SEN_AIN3	SEN_AIN3	7.5		
	JP4		3	SPR_SEN_AIN4	A2	LPADC2	_	Analog	· 	~5	'		'	_	'	_	CXD5602GG		_	SEN_AIN4	SEN_AIN4	SEN_AIN4	SEN_AIN4	7.5		
	JP4		4	SPR_SEN_AIN5	A3	LPADC3	_	Analog	·	~5	i	_	i	_	<u> </u>	_	CXD5602GG	_	_	SEN_AIN5	SEN_AIN5	SEN_AIN5	SEN_AIN5	7.5		
	JP4		5	SPR_SEN_AIN0	A4	HPADC0	_	Analog	i	~5	i	_	i	_	i	_	CXD5602GG	_	_	SEN_AIN0	SEN_AIN0	SEN_AIN0	SEN_AIN0	8.9		
	JP4		6	SPR_SEN_AIN1	A5	HPADC1	_	Analog	- 1	~5		_	1	_	1	_	CXD5602GG		_	SEN_AIN1	SEN_AIN1	SEN_AIN1	SEN_AIN1	8.9		
	JP2		1	I2C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0	5/3.3	_	High	I	High	I	High	CXD5602GG	12C0	P1j	GPIO	I2C0_BCK	_	_	7		
	JP2		2	I2C0_SDA	D14	PIN_I2C0_BDT	45	Digital	1/0	5/3.3	_	High	I	High	I	High	CXD5602GG	12C0	P1j	GPIO	I2C0_BDT	_	-	7		
	JP2		3	AREF	_	-	_	_	0	5/3.3	0	High	0	High	0	High	_	_	-	_	-	_	_			
	JP2		4	GND	_	_	_	_	_	_	_	_	-	_	_	_	_	-	_	_	-	_	_			
	JP2		5	SPI4_SCK	D13	PIN_SPI4_SCK	72	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPI0	SPI4_SCK	_	GPIO	7		
	JP2		6	SPI4_MISO	D12	PIN_SPI4_MISO	74	Digital	1/0		_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_MISO	-	GPIO	7		
	JP2		7	SPI4_MOSI	D11	PIN_SPI4_MOSI	73	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPI0	SPI4_MOSI	_	GPI0	7		
	JP2		8	SPI4_CS_X	D10	PIN_SPI4_CS_X	71	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPI0	SPI4_CS_X	-	GPI0	7		
	JP2		9	PWM2	D09	PIN_PWM2	48	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPI0	PWM2	I2C1_BCK	-	7		
	JP2		10	SPI2_MISO	D08	PIN_SPI2_MISO	30	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MISO	UARTO_RTS	GPI0			
	JP13		1	SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	_	_	7		
	JP13		2	PWM0 PWM1	D06	PIN_PWM0	46 47	Digital	I/0 I/0	5/3.3 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM0	- GPIO	_	7		
	JP13 JP13		Л	SPI2_MOSI	D05	PIN_PWM1 PIN_SPI2_MOSI	29	Digital Digital	1/0		_	Hi-Z Hi-Z		Hi-Z Hi-Z	_	Hi-Z Hi-Z	CXD5602GG CXD5602GG	PWMA SPI2B	P1k P01	GPIO GPIO	PWM1 SPI2_MOSI	UARTO_CTS	GPIO			
	JP13		5	PWM3	D04	PIN_PWM3	49	Digital			_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM3	I2C1_BDT	— — — — — — — — — — — — — — — — — — —	7		
			3		D03	_	77	Digital								111-4			1 11	GI IO		HIF IRO OUT		1		
	JP13		6	HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	HIF_IRQ_OUT	P02	GPI0	HIF_IRQ_OUT	(Open Drain)	GNSS_1PPS_OUT	7		
	JP13		7	UART_TX	D01	PIN_UART2_TX	67	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_TX		GPIO	7		
	JP13		8	UART_RX	D00	PIN_UART2_RX	68	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPI0	UART2_RX	_	GPIO	7		
		CN9	1	MAIN_POWER			<u> </u>	Power	0	4~5	0	High	0	High	0	High	-	-	_	_	-	_	_			
		CN9	2	GND				Power	_	_	_	High		High	_	High	-	_	_	_	_	_	-			
		CN9	3	SPR_SEN_AIN1	A5	HPADC1	-	Analog	I	~5		_		_		Hi-Z	CXD5602GG	_	_	-	_	_	_	8.9		
		CN9	4	SPR_SEN_AIN0	A4	HPADC0	_	Analog	I	~5		_		_		Hi-Z	CXD5602GG	-	_	_	-	_	_	8.9		
		CN9	5	3.3V				Power	0	3.3	0	_	0	_	0	_	_		_	_	_	_	_			
		CN9	6	GND				Power	_	_	_	_	_	_	_	_	_		_	_	_	-	_			
		CN9	7	HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	HIF_IRQ_OUT	P02	GPIO	HIF_IRQ_OUT	HIF_IRQ_OUT	GNSS_1PPS_OUT	7		
																						(Open Drain)				
		CN9	8	SPI3_SCK	D29	PIN_SPI3_SCK	41	Digital	1/0	5/3.3	_	Hi-Z	 -	Hi-Z	_	Hi-Z ⊔: 7	CXD5602GG	SPI3	P1i	GPIO	SPI3_SCK	_	_			
		CN9 CN9	10	PWM0	D06	PIN_PWM0	46	Digital Digital	I/O I/O	5/3.3 5/3.3		Hi-Z Hi-Z	<u> </u>	Hi-Z Hi-Z		Hi-Z	CXD5602GG	PWMA SPI3	P1k P1i	GPIO GPIO	PWM0	_	_	7		
		CN9	11	SPI3_MISO PWM1	D30	PIN_SPI3_MISO PIN_PWM1	43	Digital Digital	1/0	5/3.3		HI-Z Hi-Z	-	HI-Z Hi-Z	<u> </u>	Hi-Z Hi-Z	CXD5602GG CXD5602GG	PWMA	P1I P1k	GPIO GPIO	SPI3_MISO PWM1	GPIO	_	7		
		CN9	12	SPI3_MOSI	D05	PIN_SPI3_MOSI	47	Digital			_	Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG CXD5602GG	SPI3	P1k P1i	GPIO GPIO	SPI3_MOSI	GP10 _	_	7		
		CN9	13	PWM2	D09	PIN_PWM2	48	Digital			_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM2	I2C1_BCK	_	7		
		CN9	14	SPI3_CS0_X	D32	PIN_SPI3_CS0_X	38	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3_CS0_X	P1f	GPI0	SPI3_CS0_X		_	7		
		CN9	15	PWM3	D03	PIN_PWM3	49	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	PWMB	P1I	GPI0	PWM3	I2C1_BDT	_	7		
		CN9	16	SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	_	-	7		
		S1	81	SPR_SDIO_CMDDIR	D33	PIN_SDIO_CMDDIR	89	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Hi-Z	CXD5602GG	SDIOC	P1t	GPIO	SDIO_CMDDIR	GPIO	GPIO	2.5		
		-				•	-					-	-	-	•				-	-	•		!		•	

Date	Contents
2020.11.13	First draft.
2021.4.14	Corrected the initial value of ACP_GPO1 to 7 after starting Arduino and SDK. Corrected each initial value of XRST (SPR_RST_X).
2021.12.20	Corrected the voltage range of the pins connected to the CXD5247GF to 3.6-4.4V. Corrected the initial values after turning on the power of MCLK and after starting Aruduino.
2022.9.7	Corrected the name of SPR_SPI2_SCK on ArduinoIDE.
2024.3.21	Added LED0 to 3 on the main board and S1 on the LTE extension board.