	Ref	Pin	Name	Name on	Name of CDK	Pin No. on	Т	After turni	ng on the power After	starting Ardui	no After s	starting SDK	Commention	Pin grou	р		CXD5602G	G pin function		Maximum absolute	Features on the	
Main	Ext Cam	LTE No.	Name on schematic	ArduinoIDE	Name on SDK	SDK	Type	dir Voltage dir	Init val dir	init val	dir	init val	Connection	Mode name	doc.	mode0	mode1	mode2	mode3	rating Voltage(V)	extension board	explanation
JP1 JP1		1	GND UART2_TX	 D01	PIN_UART2_TXD	- 67	Power Digital		— — — — — — — — — — — — — — — — — — —	Hi-Z		Hi-Z	- CXD5602GG	UART2	_ Р1п	- GPIO	UART2_TXD	_	- GPIO	2.5		
JP1		3	UART2_RX	D01	PIN_UART2_RXD	68	Digital	1/0 1.8 -	Hi-Z –	Hi-Z		Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RXD	_	GPIO GPIO	2.5		
JP1		4	UART2_RTS	D28	PIN_UART2_RTS	70	Digital	1/0 1.8 -	Hi-Z –	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RTS	_	GPIO	2.5		
JP1		5	UART2_CTS	D27	PIN_UART2_CTS	69	Digital	I/O 1.8 –	Hi-Z –	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_CTS	_	GPIO	2.5		
JP1		6	I2S0_BCK	D26	PIN_I2S0_BCK	93	Digital	1/0 1.8 -	Hi-Z –	Hi-Z		Hi-Z	CXD5602GG	12S0	P1v	GPIO GPIO	I2S0_BCK	_	GPIO OPIO	2.5		
JP1 JP1		8	I2S0_LRCK SPI5_CS_X	D25 D24	PIN_I2S0_LRCK PIN_SPI5_CS_X	76	Digital Digital	I/O	Hi-Z –	Hi-Z Hi-Z		Hi-Z Hi-Z	CXD5602GG CXD5602GG	I2S0 EMMCA	P1v P1p	GPIO GPIO	I2S0_LRCK EMMC_CMD	SPI5_CS_X	GPIO GPIO	2.5		
JP1		9	SPI5_SCK	D23	PIN_SPI5_SCK	75	Digital	I/O 1.8 –	Hi-Z –	Hi-Z	_	Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_CLK	SPI5_SCK	GPIO	2.5		
JP1		10	3.3V	-	_	_	Power	O 3.3 O	- O	_	0	_	_	_	_	_	_	-	_			
JP1		11	1.8V	_	_	_	Power	0 1.8 0	- O	_	0	_	_		_	-	_	-	_			
JP1 JP1		12 13	SEN_IRQ SEN_AIN4	D22 A2	PIN_SEN_IRQ_IN LPADC2	37	Digital Analog	I/O	Hi-Z –	Hi-Z	<u> </u>	Hi-Z	CXD5602GG CXD5602GG	SEN_IRQ_IN	P1e	GPIO _	SEN_IRQ_IN _	_		2.5 1.05		
JP2		13	XRST	— AZ	(SPR_RST_X)	_	Digital	0 1.8 -	Low O	High	0	High	CXD5602GG	_	_	_	_	_		1.05		
JP2		2	1.8V	_	-	_	Power	O 1.8 O	- O	-	0	_	-	_	_	_	_	_	_			
JP2		3	3.7V(4.0V)	-	-	_	Power	I/O 3.6-4.4 O	- 0	_	0	_	_	_	_	_	_	-	_	7		
JP2		4	GPIO	D21	PIN_EMMC_DATA3	80	Digital	I/O 1.8 –	Hi-Z –	Hi-Z		Hi-Z	CXD5602GG	EMMCB	P1q	GPIO	EMMC_DATA3	-	GPIO	2.5		
JP2 JP2		5	GPIO I2SO_DATA_IN	D20	PIN_EMMC_DATA2 PIN_I2S0_DATA_IN	79	Digital	I/O	Hi-Z –	Hi-Z Hi-Z		Hi-Z Hi-Z	CXD5602GG CXD5602GG	EMMCB	P1q P1v	GPIO GPIO	EMMC_DATA2 I2S0_DATA_IN	_	GPIO GPIO	2.5		
JP2		7	I2SO_DATA_IN	D19	PIN_I2S0_DATA_IN	96	Digital Digital	1/0 1.8 -	Hi-Z –	Hi-Z		Hi-Z	CXD5602GG	12S0 12S0	P1v	GPIO GPIO	I2SO_DATA_IN	_	GPIO GPIO	2.5		
JP2		8	SPR_SPI5_MISO	D17	PIN_SPI5_MISO	78	Digital	I/O 1.8 -	Hi-Z –	Hi-Z	_	Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_DATA1	SPI5_MISO	GPIO	2.5		
JP2		9	SPR_SPI5_MOSI	D16	PIN_SPI5_MOSI	77	Digital	I/O 1.8 –	Hi-Z –	Hi-Z	_	Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_DATA0	SPI5_MOSI	GPIO	2.5		
JP2		10	GND	_	-	_	Power						-	-		-	-	-	_	_		
JP2		11	12C0_SCL	D15	PIN_I2C0_BCK	44	Digital	I/O 1.8 -	High I	High		High	CXD5602GG	12C0	P1j	GPIO GPIO	I2C0_BCK	_		2.5		
JP2 JP2		12	I2CO_SDA SEN_AIN5	D14 A3	PIN_I2C0_BDT LPADC3	45 —	Digital Analog	I/O	High I	High –		High –	CXD5602GG CXD5602GG	12C0 _	P1j _	GPIO –	I2C0_BDT -	_		2.5		
CN5	CN1	1	MCLK		— — — — — — — — — — — — — — — — — — —	_	Digital	O 1.8 -	- O	Hi-Z	0	Hi-Z	26MHz TCXO	_	_	_	_	_	_	2.5		
CN5	CN1	2	GND	-	-	_	Power			_	_	_	_	_	_	_	_	_	_			
CN5	CN1	3	I2C_SDA	-	PIN_SPI0_MISO	20	Digital	I/O 1.8 –	High –	High	I	High	CXD5602GG	I2C2	P17	GPIO	I2C2_BDT	SPI0_MISO	GPIO	2.5		
CN5	CN1	4	I2C_SCL	_ 	PIN_SPI0_MOSI	19	Digital	1/0 1.8 -	High –	High		High	CXD5602GG	12C2	P17	GPIO GPIO	I2C2_BCK	SPI0_MOSI	GPIO GPIO	2.5		
CN5 CN5	CN1	6	XRS PWDN	D35	PIN_SDIO_DIR1_3 PIN_SDIO_DIR0	91	Digital Digital	I/O 1.8 - I/O 1.8 -	Hi-Z –	Hi-Z Hi-Z		Hi-Z Hi-Z	CXD5602GG CXD5602GG	SDIOC SDIOC	P1t P1t	GPIO GPIO	SDIO_DIR1_3 SDIO_DIR0	GPIO GPIO	GPIO GPIO	2.5		
CN5	CN1	7	LDO_EN	-	(ACP_GPO4)		Digital	O 3.6-4.4 O	Hi-Z O	Low	_	Low	CXD5002GG CXD5247GF	-	—	— — — — — — — — — — — — — — — — — — —	- -	-	<u> </u>	2.3		
CN5	CN1	8	VDD_3.7V	_		_	Power	O 3.6-4.4 O	- O	_	0	_	_	_	_	_	_	-	_			
CN5	CN1	9	IS_DATA4	_	PIN_IS_DATA4	63	Digital	I/O 1.8 –	Hi-Z –	Hi-Z	I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA4	GPIO	GPIO	2.5		
CN5	CN1	10	IS_DATA6	_	PIN_IS_DATA6	65	Digital	1/0 1.8 -	Hi-Z –	Hi-Z		_	CXD5602GG	IS	P1m	GPIO	IS_DATA6	GPIO	GPI0	2.5		
CN5 CN5	CN1	11 12	IS_DATA0 IS_DATA7		PIN_IS_DATA0 PIN_IS_DATA7	59	Digital Digital	I/O 1.8 - I/O 1.8 -	Hi-Z –	Hi-Z			CXD5602GG CXD5602GG	IS IS	P1m P1m	GPIO GPIO	IS_DATA0 IS_DATA7	GPIO GPIO	GPIO GPIO	2.5		
CN5	CN1	13	IS_DATA7	_	PIN_IS_DATA7	64	Digital	1/0 1.8 -	Hi-Z –	Hi-Z		_	CXD5602GG	IS	P1m	GPIO	IS_DATA7	GPIO	GPIO	2.5		
CN5	CN1	14	IS_DATA2	_	PIN_IS_DATA2	61	Digital	I/O 1.8 -	Hi-Z –	Hi-Z	I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA2	GPIO	GPIO	2.5		
CN5	CN1	15	IS_HSYNC	_	PIN_IS_HSYNC	58	Digital	I/O 1.8 –	Hi-Z –	Hi-Z	I	_	CXD5602GG	IS	P1m	GPIO	IS_HSYNC	GPIO	GPIO	2.5		
CN5	CN1	16	IS_DATA3	_	PIN_IS_DATA3	62	Digital	1/0 1.8 -	Hi-Z –	Hi-Z		_	CXD5602GG	IS	P1m	GPIO	IS_DATA3	GPIO	GPIO OPIO	2.5		
CN5 CN5	CN1	17 18	IS_VSYNC IS_DATA1		PIN_IS_VSYNC PIN_IS_DATA1	60	Digital Digital	I/O 1.8 - I/O 1.8 -	Hi-Z –	Hi-Z Hi-Z			CXD5602GG CXD5602GG	IS IS	P1m P1m	GPIO GPIO	IS_VSYNC IS_DATA1	GPIO GPIO	GPIO GPIO	2.5		
CN5	CN1	19	GND	_			Power					_	- -	_	-	— — — — — — — — — — — — — — — — — — —		-	<u> </u>	2.3		
CN5	CN1	20	IS_CLK	_	PIN_IS_CLK	56	Digital	I 1.8 I	<u> </u>	_	I	_	CXD5602GG	IS	P1m	GPIO	IS_CLK	GPIO	GPIO	2.5		
CN4[L]	CN4[L]	CN4[L] 1	3.3V_AU		-	_	Power	O 3.3 I	- 1	_	I	_	_	_	_	-	-	_	_			
CN4[L]	CN4[L]	CN4[L] 3	3.3V_AU		_	_	Power	0 3.3 I	- I			_	- CVDE04705		_	_	_	_				
CN4[L]	CN4[L]	CN4[L] 5 CN4[L] 7	ACP_MICA ACP_MICB			_	Analog Analog	- 1	_ I			_	CXD5247GF CXD5247GF		_	_		_				
CN4[L]	CN4[L]	CN4[L] 9	ACP_MICC	_	_	_	Analog	-	- 1		'	_	CXD5247GF	_	_	_	_	_	_			
CN4[L]	CN4[L]	CN4[L] 11	ACP_MICD	_	_	_	Analog	I – I	- I	_	I	_	CXD5247GF	_	_	-	_	_	_			
CN4[L]	CN4[L]	CN4[L] 13	ACP_MICBIASA	_	_	_	Analog	0 2 0	- 0	_	0	_	CXD5247GF	_	_	_	_	_	_			
	CN4[L]	CN4[L] 15	ACP_MICBIASB	_		_	Analog	0 2 0	- O		0	_	CXD5247GF		_	_	_	_				
CN4[L]	CN4[L]	CN4[L] 17 CN4[L] 19	AGND_MIC SPR_I2C0_SCL	D15	PIN_I2C0_BCK	44	Power Digital		High I	– High		– High	CXD5602GG	 I2C0	P1j	GPIO	- I2C0_BCK	_		2.5		
CN4[L]	CN4[L]	CN4[L] 21	SPR_I2C0_SDA	D13	PIN_I2C0_BDT	45	Digital	I/O 1.8 –	High I	High		High	CXD5602GG	12C0	P1j	GPIO	I2C0_BDT	_		2.5		
	CN4[L]	CN4[L] 23	SPR_SPI4_SCK	D13	PIN_SPI4_SCK	72	Digital	I/O 1.8 –	Hi-Z –	Hi-Z		Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_SCK	-	GPIO	2.5		
	CN4[L]	CN4[L] 25	SPR_SPI4_MISO	D12	PIN_SPI4_MISO	74	Digital	I/O 1.8 –	Hi-Z –	Hi-Z		Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_MISO	_	GPI0	2.5		
CN4[L]	CN4[L]	CN4[L] 27	SPR_SPI4_MOSI	D11	PIN_SPI4_MOSI	73	Digital	1/0 1.8 -	Hi-Z –	Hi-Z		Hi-Z	CXD5602GG	SPI4	P1o	GPIO GPIO	SPI4_MOSI	_	GPIO GPIO	2.5		
CN4[L]	CN4[L]	CN4[L] 29 CN4[L] 31	SPR_SPI4_CS_X SPR_PWM2	D10 D09	PIN_SPI4_CS_X PIN_PWM2	48	Digital Digital	I/O 1.8 - I/O 1.8 -	Hi-Z –	Hi-Z		Hi-Z Hi-Z	CXD5602GG CXD5602GG	SPI4 PWMB	P1o P1I	GPIO GPIO	SPI4_CS_X PWM2	I2C1_BCK	GPIO –	2.5		
	CN4[L]	CN4[L] 33	SPR_I2S0_LRCK	D25	PIN_I2S0_LRCK	94	Digital	I/O 1.8 –	Hi-Z –	Hi-Z		Hi-Z	CXD5602GG	1280	P1v	GPIO	I2S0_LRCK		GPIO	2.5		
	CN4[L]	CN4[L] 35	GND				Power				_	_	-	_	_	-	_	_	_			
CN4[L]	CN4[L]	CN4[L] 37	SPR_I2SO_DATA_OUT	D18	PIN_I2SO_DATA_OUT	96	Digital	I/O 1.8 -	Hi-Z –	Hi-Z		Hi-Z	CXD5602GG	12S0	P1v	GPIO	I2S0_DATA_OUT	_	GPIO	2.5		
CN4[L]	CN4[L]	CN4[L] 39	SPR_PWM0	D06	PIN_PWM0	46	Digital	1/0 1.8 -	Hi-Z –	Hi-Z		Hi-Z	CXD5602GG	PWMA	P1k	GPIO GPIO	PWM0	- CDIO	_	2.5		
CN4[L]	CN4[L]	CN4[L] 41 CN4[L] 43	SPR_PWM1 SPR_I2S0_DATA_IN	D05	PIN_PWM1 PIN_I2SO_DATA_IN	47 95	Digital Digital	I/O 1.8 - I/O 1.8 -	Hi-Z –	Hi-Z Hi-Z		Hi-Z Hi-Z	CXD5602GG CXD5602GG	PWMA I2S0	P1k P1v	GPIO GPIO	PWM1 I2S0_DATA_IN	GPIO –	GPIO	2.5		
CN4[L]	CN4[L]	CN4[L] 45	SPR_PWM3	D03	PIN_PWM3	49	Digital	I/O 1.8 –	Hi-Z –	Hi-Z		Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM3	I2C1_BDT	_	2.5		
			SPR_I2S0_BCK			93									P1v		I2S0_BCK	_	GPIO	2.5		

CN4[L] CN4[L]	CN4[L] 49 SPR_UART2_TX	D01	PIN_UART2_TX	67	Digital	I/O 1	.8	- H	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG	UART2	P1n	GPIO	UART2_TX	_	GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 51 SPR_UART2_RX	D00	PIN_UART2_RX	68	Digital		.8	- H	Hi-Z	-	Hi-Z	– Hi-2	CXD5602GG	UART2	P1n	GPIO	UART2_RX	-	GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 53 SPR_UART2_RTS	D28	PIN_UART2_RTS	70	Digital	I/O 1	.8	- H	Hi-Z	-	Hi-Z	– Hi-2	CXD5602GG	UART2	P1n	GPI0	UART2_RTS	_	GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 55 SPR_UART2_CTS	D27	PIN_UART2_CTS	69	Digital		.8		Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG	UART2	P1n	GPI0	UART2_CTS	_	GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 57 SPR_EMMC_CLK	D23	PIN_EMMC_CLK	75	Digital		.8		111 2	_	Hi-Z	- Hi-2	CXD5602GG	EMMC A	P1p	GPIO CPIO	EMMC_CLK	SPI5_SCK	GPIO	2.5	
CN4[L] CN4[L] CN4[L]	CN4[L] 59 SPR_EMMC_CMD CN4[L] 61 SPR_EMMC_DATA0	D24 D16	PIN_EMMC_CMD PIN_EMMC_DATA0	76 77	Digital Digital	•	.8		111 2	_	Hi-Z Hi-Z	- Hi-z	CXD5602GG CXD5602GG	EMMC A EMMC A	P1p P1p	GPIO GPIO	EMMC_CMD EMMC_DATA0	SPI5_CS_X SPI5_MOSI	GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 63 SPR_EMMC_DATA1	D17	PIN_EMMC_DATA1	78	Digital		.8		Hi-Z	_	Hi-Z	- Hi-2	CXD5602GG	EMMC A	P1p	GPI0	EMMC_DATA0	SPI5_MISO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 65 SPR_EMMC_DATA2	D20	PIN_EMMC_DATA2	79	Digital		.8	- H	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG	EMMC B	P1q	GPIO	EMMC_DATA2		GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 67 SPR_EMMC_DATA3	D21	PIN_EMMC_DATA3	80	Digital	I/O 1	.8	- H	Hi-Z	-	Hi-Z	– Hi-2	CXD5602GG	EMMC B	P1q	GPI0	EMMC_DATA3	_	GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 69 GND	_	_	_	Power		_			_	_		_	_	_	_	_	_	-		
CN4[L] CN4[L]	CN4[L] 71 SPR_SPI2_SCK	D43	PIN_SPI2_SCK	28	Digital		.8		Hi-Z	_	Hi-Z	- Hi-2	CXD5602GG	SPI2A	P00	GPIO GPIO	SPI2_SCK	UARTO_RXD	I2C3_BDT	2.5	
CN4[L] CN4[L] CN4[L]	CN4[L] 73 SPR_SPI2_MISO CN4[L] 75 SPR_SPI2_MOSI	D08	PIN_SPI2_MISO PIN_SPI2_MOSI	29	Digital Digital		.8		Hi-Z Hi-Z	_	Hi-Z Hi-Z	- Hi-z	CXD5602GG CXD5602GG	SPI2B SPI2B	P01 P01	GPIO GPIO	SPI2_MISO SPI2_MOSI	UARTO_RTS UARTO_CTS	GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 77 SPR_SPI2_CS_X	D42	PIN_SPI2_CS_X	27	Digital		.8			_	Hi-Z	– Hi-2	CXD5602GG	SPI2A	P00	GPI0	SPI2_CS_X	UARTO_TXD	I2C3_BCK	2.5	
CN4[L] CN4[L]	CN4[L] 79 1.8V	_	-	_	Power		.8	0	_	0	_	0 -	_	_	_	_	-	_			
CN4[L] CN4[L]	CN4[L] 81 SPR_SDIO_CMDDIR	D33	PIN_SDIO_CMDDIR	89	Digital	I/O 1	.8	- H	Hi-Z	_	Hi-Z	O Hi-Z	CXD5602GG	SDIOC	P1t	GPIO	SDIO_CMDDIR	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 83 SPR_SDIO_CLK	D38	PIN_SDIO_CLK	81	Digital	I/O 1	.8	- H	Hi-Z	_	Hi-Z	O Lov	CXD5602GG	SDIOA	P1u	GPIO	SDIO_CLK	SPI5_SCK	GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 85 SPR_SDIO_CMD	_	PIN_SDIO_CMD	82	Digital		.8		Hi-Z	_	Hi-Z	0 Hi-2	CXD5602GG	SDIOA	P1r	GPI0	SDIO_CMD	SPI5_CS_X	GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 87 SPR_SDIO_DATA0	_	PIN_SDIO_DATA1	83	Digital	•	.8		Hi-Z Hi-Z	_	Hi-Z	O Lov	CXD5602GG	SDIOA SDIOA	P1r	GPIO GPIO	SDIO_DATA1	SPI5_MOSI	GPIO GPIO	2.5	
CN4[L] CN4[L] CN4[L]	CN4[L] 89 SPR_SDIO_DATA1 CN4[L] 91 SPR_SDIO_DATA2		PIN_SDIO_DATA1 PIN_SDIO_DATA2	85	Digital Digital		.8		Hi-Z	_	Hi-Z Hi-Z	O Lov	CXD5602GG CXD5602GG	SDIOA	P1r P1r	GPIO GPIO	SDIO_DATA1 SDIO_DATA2	SPI5_MISO GPIO	GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 93 SPR_SDIO_DATA3	_	PIN_SDIO_DATA3	86	Digital		.8		Hi-Z	_	Hi-Z	O Lov	CXD5602GG	SDIOA	P1r	GPI0	SDIO_DATA2	GPIO GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 95 GND	_		_	Power		-	_		_	_			_	_	_		_	_		
CN4[L] CN4[L]	CN4[L] 97 SPR_SDIO_WP	D37	PIN_SDIO_WP	88	Digital	I/O 1	.8	- H	Hi-Z	-	Hi-Z	— Нi-2	CXD5602GG	SDIOB	P1s	GPIO	SDIO_WP	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L] 99 GND	_	_	_	Power		_ _	-	_	-	-		_	_	_	_	_	_	-		
CN4[R] CN4[R]	CN4[R] 2 5V	_	_	_	Power	1/0	5	0	_	0	_	0 –	_	_	_	_	_	-	-	6	
CN4[R] CN4[R] CN4[R]	CN4[R] 4 5V CN4[R] 6 ACP_SPAP				Power Analog	U 3	.3	Ů.	– Hi-Z	O	– Hi-Z	O – Hi-2	CXD5247GF				_	_	_	ь	
CN4[R] CN4[R]	CN4[R] 8 ACP_SPAN	_	_	_	Analog		.3		Hi-Z	_	Hi-Z	– Hi-2		_	_	_	_	_	_		
CN4[R] CN4[R]	CN4[R] 10 ACP_SPBN	_	_	_	Analog		.3		Hi-Z	_	Hi-Z	– Hi-2		_	_	_	_	_	_		
CN4[R] CN4[R]	CN4[R] 12 ACP_SPBP	_	_	_	Analog		.3	- H	Hi-Z	_	Hi-Z	- Hi-2	CXD5247GF	_	_	_	_	_	-		
CN4[R] CN4[R]	CN4[R] 14 AGND_DRV	_	-	_	Power		_			_	-		_	-	_	_	-	_	-		
CN4[R] CN4[R]	CN4[R] 16 SPR_SWDIO	_	_	_	Digital		.8		Hi-Z	_	Hi-Z	- Hi-2		_	_	_	-	_	_	2.5	
CN4[R] CN4[R]	CN4[R] 18 SPR_SWDCLK	_	_	_	Digital		.8		Hi-Z		Hi-Z Hi-Z	l Hi-Z	CXD5602GG CXD5247GF	_	_	_		_	_	2.5	
CN4[R] CN4[R]	CN4[R] 20 ACP_CLK_DMIC	_	_	_	Digital	0 1	.8		Hi-Z	\cup	HI-/I				_	_		_	_		
LCMAIRLLCMAIRLL	CNA[R] 22 XRS PWON	_	_	_	Power	1/0 3	3		LOW			0 Hi-2	_		_	_		_	_	7	
CN4[R] CN4[R]	CN4[R] 22 XRS_PWON	_	-	-	Power			I/O I			Low	I/O Low	CXD5247GF	-	_	_	_	-	- CPU WDT	7	
CN4[R] CN4[R] CN4[R]	CN4[R] 22 XRS_PWON CN4[R] 24 SPR_GNSS_1PPS_OUT		PIN_GNSS_1PPS_OUT	6			_	I/O I	=				CXD5247GF		P14			- CPU_WDT	- CPU_WDT (Open Drain)	7 2.5	
				6 37		1/0 1		I/O I - H		1/0	Low	I/O Low	CXD5247GF CXD5602GG	GNSS_1PPS_OUT	P14		_		CPU_WDT	7 2.5 2.5	
CN4[R] CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN	Г D44	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN		Digital Digital	I/O 1 I/O 1	.8	/O - - -	Hi-Z Hi-Z	I/O - -	Low Hi-Z Hi-Z	I/O Low	CXD5247GF CXD5602GG CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN	P1e	GPIO SEN_IRQ_IN	GNSS_1PPS_OUT SEN_IRQ_IN	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT	CPU_WDT (Open Drain) SEN_IRQ_IN	2.5	
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT	D44 D22 D02	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT		Digital Digital Digital	/O	.8	/O -	Hi-Z Hi-Z Hi-Z	 - -	Low Hi-Z Hi-Z Hi-Z	I/O Low	CXD5247GF CXD5602GG CXD5602GG CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ		GPIO SEN_IRQ_IN GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain)	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT		
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND	D44 D22 D02 -	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT -		Digital Digital Digital Power	/0	.8	/O -	Hi-Z Hi-Z Hi-Z	/O	Low Hi-Z Hi-Z Hi-Z	I/O Low	CXD5247GF CXD5602GG CXD5602GG CXD5602GG -	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ -	P1e	GPIO SEN_IRQ_IN GPIO	- GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT -	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) -	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT -	2.5	
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X	D44 D22 D02	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT		Digital Digital Digital	/0	.8	/O -	Hi-Z Hi-Z Hi-Z	/O	Low Hi-Z Hi-Z Hi-Z	I/O Low	CXD5247GF CXD5602GG CXD5602GG CXD5602GG - CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ	P1e	GPIO SEN_IRQ_IN GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) -	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5	
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND	D44 D22 D02 -	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT -		Digital Digital Digital Power Digital	/0	.8	/O -	Hi-Z Hi-Z Hi-Z Low	 - - - -	Low Hi-Z Hi-Z Hi-Z -	I/O Low	CXD5247GF CXD5602GG CXD5602GG CXD5602GG - CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ -	P1e	GPIO SEN_IRQ_IN GPIO	- GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT -	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) -	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT	2.5	
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X	D44 D22 D02	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT		Digital Digital Digital Power Digital	/O	.8 .8 .8	/O -	Hi-Z Hi-Z Low	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High	I/O Low	CXD5247GF CXD5602GG CXD5602GG CXD5602GG - CXD5602GG	- GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ	P1e P02	GPIO SEN_IRQ_IN GPIO	- GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) -	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5	
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK	D44 D22 D02 D40	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK		Digital Digital Digital Power Digital Digital	/O	.8 .8 .8 - .8	/O -	Hi-Z Hi-Z Low Hi-Z	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High	I/O Low	CXD5247GF CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ - AP_CLK -	P1e P02	GPIO SEN_IRQ_IN GPIO - GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT	2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4	D44 D22 D02 - D40 A0 A1 A2	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT - PIN_AP_CLK LPADC0 LPADC1 LPADC2		Digital Digital Digital Power Digital Digital Analog Analog Analog	/O	.8 .8 .8 .8 .8 .7 .7	/O -	Hi-Z Hi-Z Low Hi-Z	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High	I/O Low	CXD5247GF CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02	GPIO SEN_IRQ_IN GPIO - GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT	2.5 2.5 2.5 1.05 1.05 1.05	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5	D44 D22 D02 - D40 A0 A1 A2 A3	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT - PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3		Digital Digital Power Digital Digital Analog Analog Analog Analog	I/O	.8 .8 .8 .8 .8 .8 .7 .7 .7 .7	/O -	Hi-Z Hi-Z Low Hi-Z - Low	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High	I/O Low	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02	GPIO SEN_IRQ_IN GPIO - GPIO GPIO - GPIO -	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 1.05	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN0	D44 D22 D02 - D40 A0 A1 A2 A3 A4	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0		Digital Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Analog	I/O	.8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .4	/O	Hi-Z Hi-Z Low Hi-Z - Low	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High	I/O Low	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02	GPIO SEN_IRQ_IN GPIO - GPIO GPIO - GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5	D44 D22 D02 - D40 A0 A1 A2 A3	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT - PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3		Digital Digital Power Digital Digital Analog Analog Analog Analog	I/O	.8 .8 .8 .8 .8 .8 .7 .7 .7 .7	/O	Hi-Z Hi-Z Low Hi-Z - Low	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High	I/O Low	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02	GPIO SEN_IRQ_IN GPIO - GPIO - GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 1.05	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN0 CN4[R] 46 SPR_SEN_AIN1	D44 D22 D02 - D40 A0 A1 A2 A3 A4 A5	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1		Digital Digital Power Digital Digital Analog	I/O	.8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .4	/O	Hi-Z Hi-Z Low Hi-Z - Low	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High	I/O Low	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ - AP_CLK	P1e P02	GPIO SEN_IRQ_IN GPIO - GPIO - GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN0 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 —	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 -	37 31 - - 5 - - - - - -	Digital Digital Power Digital Digital Digital Analog	I/O	.8 .8 .8 .8 .8 .7 .7 .7 .7 .4 .4	/O	Hi-Z Hi-Z Low Hi-Z - Low	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High Hi-Z - High	I/O Low	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02 P13	GPIO SEN_IRQ_IN GPIO - GPIO - GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 42 SPR_SEN_AIN0 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 52 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_SCK	D44 D22 D02	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK	37 31	Digital Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Digital	I/O	.8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .4 .48 .8 .8 .8	/O	Hi-Z Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High Hi-Z - High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O Low - Hi-z - Hi-z - - O High - - I -	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ - AP_CLK SPI3_CS1_X SPI3 SPI3	P1e P02 P13 P1g P1i P1i	GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN0 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 52 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_MISO	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO	37 31	Digital Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Digital Digital	I/O	.8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .4 .4 .8 .8 .8 .8	/O	Hi-Z Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High Hi-Z - High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O Low - Hi-z - Hi-z - - O High - - I -	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i	GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 42 SPR_SEN_AIN0 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 52 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_SCK	D44 D22 D02	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK	37 31	Digital Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Digital	I/O	.8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .4 .48 .8 .8 .8	/O	Hi-Z Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High Hi-Z - High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O Low - Hi-z - Hi-z - - O High - - I -	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ - AP_CLK SPI3_CS1_X SPI3 SPI3	P1e P02 P13 P1g P1i P1i	GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN0 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 52 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_MISO	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO	37 31	Digital Digital Power Digital Digital Digital Analog Analog Analog Analog Analog Digital	I/O	.8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .4 .4 .8 .8 .8 .8	/O	Hi-Z Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z High Hi-Z - High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O Low - Hi-z - Hi-z - - O High - - I -	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i	GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_SEN_AIN2 CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN0 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 52 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_SCK CN4[R] 56 SPR_SPI3_MISO CN4[R] 58 SPR_SPI3_CS0_X	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X	37 31	Digital Digital Power Digital Digital Digital Analog Analog Analog Analog Analog Digital	I/O	.8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .4 .4 .8 .8 .8 .8 .8 .8	/O	Hi-Z Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	 - - - - - 0	Low Hi-Z Hi-Z Hi-Z Hish Hi-Z - High Hin-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O Low - Hi-z - Hi-z - Hi-z - O Hi-z I - I </td <td>CXD5247GF CXD5602GG CXD5602GG</td> <td>GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3_CS0_X</td> <td>P1e P02 P13 P1g P1i P1i P1i P1i</td> <td>GPIO SEN_IRQ_IN GPIO - GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO</td> <td>GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X</td> <td>CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT</td> <td>CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)</td> <td>2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5</td> <td></td>	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3_CS0_X	P1e P02 P13 P1g P1i P1i P1i P1i	GPIO SEN_IRQ_IN GPIO - GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN2 CN4[R] 40 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN0 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 52 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_SCK CN4[R] 56 SPR_SPI3_CS0_X CN4[R] 58 SPR_SPI3_CS0_X CN4[R] 60 SPR_RTC_IRQ_OUT	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	37 31	Digital Digital Power Digital Digital Digital Analog Analog Analog Analog Analog Digital	I/O	.8 .8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .7 .4 .4 .48 .8 .8 .8 .8 .8 .8	/O	Hi-Z Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O	Low Hi-Z Hi-Z Hi-Z Hish Hi-Z - High Hin-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O Low - Hi-z - Hi-z - O Hi-z Hi-z I - <	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3_CS0_X RTC_IRQ_OUT	P1e P02 P13 P1g P1i P1i P1i P1i	GPIO SEN_IRQ_IN GPIO - GPIO - GPIO - GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN2 CN4[R] 40 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN1 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 52 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_SCK CN4[R] 56 SPR_SPI3_CS0_X CN4[R] 58 SPR_SPI3_CS0_X CN4[R] 60 SPR_RSP_SP_SP_SP_SP_SP_SP_SP_SP_SP_SP_SP_	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D41	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT -	37 31	Digital Digital Power Digital Digital Digital Analog Analog Analog Analog Analog Digital Analog Analog	I/O	.8 .8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .4 .4 .48 .8 .8 .8 .8 .8 .8 .8 .8	/O	Hi-Z Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O	Low Hi-Z Hi-Z Hi-Z Hish Hi-Z - High Hin-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O Low - Hi-z - Hi-z - O Hi-z Hi-z I - <	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ - AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i P1i	GPIO SEN_IRQ_IN GPIO - GPIO - GPIO - GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT -	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN2 CN4[R] 40 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN0 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 46 SPR_SEN_AIN5 CN4[R] 46 SPR_SEN_AIN5 CN4[R] 46 SPR_SEN_AIN5 CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 54 SPR_SPI3_MISO CN4[R] 58 SPR_SPI3_CS0_X CN4[R] 60 SPR_RSPI3_CS0_X	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_SCK PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	37 31 5 39 42 41 43 38 4	Digital Digital Power Digital Digital Digital Digital Analog Analog Analog Analog Analog Analog Digital Power Analog Analog	I/O	.8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .7 .4 .4 .4 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8	/O	Hi-Z Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-	I/O	Low Hi-Z Hi-Z Hi-Z High Hi-Z Hih-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi	I/O Low - Hi-z - Hi-z - O Hi-z - Hi-z I - I </td <td>CXD5247GF CXD5602GG CXD5602GG</td> <td>GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3</td> <td>P1e P02 P13 P1g P1i P1i P1i P1i P1i</td> <td>GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO</td> <td>GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT </td> <td>CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) </td> <td>CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT </td> <td>2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.</td> <td></td>	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i P1i P1i	GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain)	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN3 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN1 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 52 SPR_SPI3_SCK CN4[R] 54 SPR_SPI3_SCK CN4[R] 56 SPR_SPI3_CS0_X CN4[R] 58 SPR_SPI3_CS0_X CN4[R] 60 SPR_RTC_IRQ_OUT CN4[R] 64 SPR_USB_DM	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D39 D30 D32 D41 - D39 - D39 -	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0 PIN_HIF_GPIO0	37 31 5 39 42 41 43 38 4	Digital Digital Power Digital Digital Digital Digital Analog Analog Analog Analog Analog Analog Digital	I/O	.8 .8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .7 .4 .4 .48 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8	/ O	Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O	Low Hi-Z Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi	I/O Low - Hi-z - Hi-z - O Hi-z Hi-z I - I - I - I - I - I - Hi-z - - Hi-z - Hi-z - Hi-z - Hi-z - Hi-z - - - Hi-z - - - <	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ - AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i P1i P1i	GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO - GPIO GPIO	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain)	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN3 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN1 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 52 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_SCK CN4[R] 56 SPR_SPI3_CS0_X CN4[R] 58 SPR_SPI3_CS0_X CN4[R] 60 SPR_RTC_IRQ_OUT CN4[R] 62 SPR_USB_DM	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D07 D31 D29 D30 D32 D41 D- D39 D30 D32 D41 D39 D39	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIOO - PIN_HIF_GPIOO	37 31 5 39 42 41 43 38 4	Digital Digital Power Digital Digital Digital Digital Analog Analog Analog Analog Analog Analog Digital	I/O	.8 .8 .8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .4 .4 .48 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8	/ O	Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-	I/O	Low Hi-Z Hi-Z High Hi-Z - High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi	I/O Low - Hi-z - Hi-z - - O High - - I -	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i P1i P1i	GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPI	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO - GPIO -	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute Mute at Low
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN2 CN4[R] 40 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN0 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 50 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_SCK CN4[R] 54 SPR_SPI3_SCK CN4[R] 56 SPR_SPI3_CS0_X CN4[R] 58 SPR_SPI3_CS0_X CN4[R] 60 SPR_RTC_IRQ_OUT CN4[R] 62 SPR_USB_DM	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D39 D30 D32 D41 - D39 D39 - D39 - D39	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_MSO PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIOO	37 31 5 39 42 41 43 38 4	Digital Digital Power Digital Digital Digital Digital Analog Analog Analog Analog Analog Analog Digital	I/O	.8 .8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .4 .4 .4 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8	/ O	Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi	I/O	Low Hi-Z Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi	I/O Low - Hi-z - Hi-z - O Hi-z Hi-z I - I - I - I - I - I - Hi-z - - Hi-z - Hi-z - Hi-z - Hi-z - Hi-z - - - Hi-z - - - <	CXD5247GF CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ - AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i P1i P1i	GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPI	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO GPIO	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute Mute at Low
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN3 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN1 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 52 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_SCK CN4[R] 56 SPR_SPI3_CS0_X CN4[R] 58 SPR_SPI3_CS0_X CN4[R] 60 SPR_RTC_IRQ_OUT CN4[R] 62 SPR_USB_DM	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D- D39 D30 D32 D41 - D39 D- D39 - D39 - D- D39	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0	37 31 5 39 42 41 43 38 4	Digital Digital Power Digital Digital Digital Digital Analog Analog Analog Analog Analog Analog Digital	I/O	.8 .8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .7 .4 .4 .4 .9 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8	/O	Hi-Z Hi-Z Low Hi-Z - Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O	Low Hi-Z Hi-Z High Hi-Z - High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi	I/O Low - Hi-z - Hi-z - O Hi-z Hi-z I -	CXD5247GF CXD5602GG CXD5602GG	- GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ	P1e P02 P13 P1g P1i P1i P1i P1i P1i	GPIO SEN_IRQ_IN GPIO GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO - GPIO	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute Mute at Low
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 40 SPR_SEN_AIN5 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 50 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_SCK CN4[R] 54 SPR_SPI3_CS0_X CN4[R] 56 SPR_SPI3_CS0_X CN4[R] 60 SPR_RTC_IRQ_OUT CN4[R] 62 SPR_USB_DM	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D- D39 D39 D- D39 D- D39 D- D- D39 D- D- D- D39 D- D- D- D39 D-	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIOO	37 31 5 39 42 41 43 38 4	Digital Digital Power Digital Digital Digital Digital Analog Power Digital Power Digital Power	I/O	.8 .8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .7 .4 .4 .4 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8	/ O	Hi-Z Hi-Z Low Hi-Z - Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O	Low Hi-Z Hi-Z High Hi-Z - High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi	I/O Low - Hi-z - Hi-z - O I -	CXD5247GF CXD5602GG	- GNSS_1PPS_OUT SEN_IRQ_IN HIFIRQ	P1e P02 P13 P1g P1i P1i P1i P1i P1i	GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	GNSS_1PPS_OUT SEN_IRQ_IN HIF_IRQ_OUT AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO	CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute Mute at Low
CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 40 SPR_SEN_AIN5 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN1 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 50 SPR_SPI3_MOSI CN4[R] 54 SPR_SPI3_SCK CN4[R] 54 SPR_SPI3_CS0_X CN4[R] 56 SPR_SPI3_CS0_X CN4[R] 60 SPR_RTC_IRQ_OUT CN4[R] 64 SPR_USB_DP	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D- D39 D30 D32 D41 D39 D39 D39 D36	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0 PIN_HIF_GPIO0 PIN_SPIS_CD	37 31	Digital Digital Power Digital Digital Digital Digital Analog Analog Analog Analog Analog Analog Digital Power Digital Power Digital Digital Power Digital Digital	I/O 1 I/O 1 I/O 1 I/O 1 I/O 1 I 0 I 0 I 0 I 1 I/O 1 I/O 1 I/O 1 I/O 1 I/O 1 I/O 3 I/O 3	.8 .8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .7 .4 .4 .4 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8	/ O	Hi-Z Hi-Z Low Hi-Z - Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O	Low Hi-Z Hi-Z High Hi-Z - - - Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O Low - Hi-Z - Hi-Z - O I - I - I - I - I - I - I - I - I - I - I - I - I I	CXD5247GF CXD5602GG		P1e P02 P13	GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO		CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain)	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute Mute at Low
CN4[R] CN4[R] CN4[R]	CN4[R] 24 SPR_GNSS_1PPS_OUT CN4[R] 26 SPR_SEN_IRQ_IN CN4[R] 28 SPR_HIF_IRQ_OUT CN4[R] 30 GND CN4[R] 32 SPR_RST_X CN4[R] 34 SPR_AP_CLK CN4[R] 36 SPR_SEN_AIN2 CN4[R] 38 SPR_SEN_AIN3 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 40 SPR_SEN_AIN4 CN4[R] 42 SPR_SEN_AIN5 CN4[R] 44 SPR_SEN_AIN1 CN4[R] 46 SPR_SEN_AIN1 CN4[R] 48 GND CN4[R] 48 GND CN4[R] 50 SPR_SPI3_CS1_X CN4[R] 50 SPR_SPI3_GCS1_X CN4[R] 54 SPR_SPI3_SCK CN4[R] 56 SPR_SPI3_CS0_X CN4[R] 58 SPR_SPI3_CS0_X CN4[R] 60 SPR_RTC_IRQ_OUT CN4[R] 62 SPR_USB_DP	D44 D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D- D39 D30 D32 D41 D39 D39 D39 D36	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT PIN_HIF_GPIO0	37 31	Digital Digital Power Digital Digital Digital Digital Analog Analog Analog Analog Analog Analog Digital Power Digital Power Digital Digital Power Digital Digital	I/O 1 I/O 1 I/O 1 I/O 1 I/O 1 I 0 I 0 I 0 I 1 I/O 1 I/O 1 I/O 1 I/O 1 I/O 1 I/O 3 I/O 3	.8 .8 .8 .8 .8 .8 .8 .8 .7 .7 .7 .7 .7 .4 .4 .4 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8 .8	/ O	Hi-Z Hi-Z Low Hi-Z - Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O	Low Hi-Z Hi-Z High Hi-Z - - - Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	I/O Low - Hi-Z - Hi-Z - O I - I - I - I - I - I - I - I - I - I - I - I - I I	CXD5247GF CXD5602GG		P1e P02 P13	GPIO SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO		CPU_WDT SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	CPU_WDT (Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute Mute at Low

CN4[R]	CN4[R]	CN4[R]	86	SPR_I2C2_SCL		PIN_SPI0_MOSI	19	Digital	I/O 1.8	_	High	1/0	_	ı T	High	CXD5602GG	SPI0B	P17	GPIO	I2C2_BCK	SPI0_MOSI	_ [2.5		
		+						Digital			High	1/0		0				PII					2.0		
CN4[R]	CN4[R]		88	ACP_GP00		_	_	Digital	0 3.6-4.4		Hi-Z	0	Hi-Z	0	Hi-Z	CXD5247GF	_	_	_	_	_	_		A 11 0 01/	0
CN4[R]	CN4[R]	CN4[R]	90	ACP_GP01	_	_	_	Digital	O 3.6-4.4	0	Hi-Z	0	Low	0	Low	CXD5247GF	_	_	_	_	_	_		Audio 3.3V output	Output at High
CN4[R]	CN4[R]	CN4[R]	92	ACP_GPO2	_	_	_	Digital	O 3.6-4.4	0	Hi-Z	0	Low	0	Low	CXD5247GF	_	_	_	_	_	_		Power output for LTE	Output at High
OTTIERS	0111[13]	OTTICITY	32	7.01 _01 02				Digital	0.0 1.1	Ü	111 2	Ŭ	2011	Ü	2011	0/\D02 17 G1								(Only LTE extension)	output at mgm
																								LTE power output from	
CN4[R]	CN4[R]	CN4[R]	94	ACP_GPO3	_	_	_	Digital	O 3.6-4.4	Ο	Hi-Z	Ο	Low	Ο	Low	CXD5247GF	_	_	_	_	_	_		the main board	Output at High
																								(Only LTE extension)	
CN4[R]	CN4[R]	CN4[R]	96	SWOCLK	_	_	_	Digital	O 1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	_	_	_	_	_	_	2.5		
CN4[R]	CN4[R]	+	98	SWO		_	_	Digital	0 1.8		Hi-Z		Hi-Z	_	Hi-Z	CXD5602GG	_	_	_	_	_	_	2.5		
		+							0 1.0						111-2	CADSOUZUU							2.3		
CN4[R]	CN4[R]	CN4[R]	100	GND		-	_	Power		_	_	_	_	_	_	_	_	_	_	_	_	_			
	JP3		1	NC	_	_	_	_		_	_	_	_	_	_	_	_	_	_	_	_	_			
	JP3		2	VDD_LVS	_	_	_	Power	O 5/3.3	0	_	0	_	0	_	_	_	-	_	_	_	_			
	JP3		3	XRS_PWON	_	_	_	Digital	1/0 3.3	1/0	-	1/0	-	1/0	_	_	_	_	_	_	_	_	7		
	JP3		4	3.3V	_	_	_	Power	O 3.3	Ο	_	Ο	-	Ο	_	_	_	_	_	_	_	_			
	JP3		5	MAIN_POWER	_	-	_	Power	I/O 5	1/0	-	1/0	-	1/0	_	_	_	_	-	_	_	-	6		
	JP3		6	GND	_	-	_	Power		_	-	-	_	_	_	_	_	_	-	_	_	-			
	JP3		7	GND	_	_	_	Power	_ _	_	_	_	_	_	_	_	_	_	_	_	_	_			
	JP3	+	8	5V_IN_PIN		_	_	_	_	_	_	_	_	_	_	_	_	_	_	_	_	_			
	JP4	+ +	1		A0			Analaz	· ~5	ı	_		_		_	CYDEGOOO							7.5		
			т	SPR_SEN_AIN2		LPADC0	_	Analog							_	CXD5602GG	-	_	SEN_AIN2	SEN_AIN2	SEN_AIN2	SEN_AIN2			
	JP4		2	SPR_SEN_AIN3	A1	LPADC1	_	Analog	l ∼5		_	1	_	1	_	CXD5602GG	_	_	SEN_AIN3	SEN_AIN3	SEN_AIN3	SEN_AIN3	7.5		
	JP4		3	SPR_SEN_AIN4	A2	LPADC2	_	Analog	l ∼5		_		_	1	_	CXD5602GG	_	_	SEN_AIN4	SEN_AIN4	SEN_AIN4	SEN_AIN4	7.5		
	JP4		4	SPR_SEN_AIN5	А3	LPADC3	_	Analog	I ∼5	- 1	_		-	ı	_	CXD5602GG	_	_	SEN_AIN5	SEN_AIN5	SEN_AIN5	SEN_AIN5	7.5		
	JP4		5	SPR_SEN_AIN0	A4	HPADC0	_	Analog	I ∼5	I	_		_	1	_	CXD5602GG	_	_	SEN_AIN0	SEN_AIN0	SEN_AIN0	SEN_AIN0	8.9		
	JP4		6	SPR_SEN_AIN1	A5	HPADC1		Analog	I ∼5	I	_		-	I	_	CXD5602GG	ı		SEN_AIN1	SEN_AIN1	SEN_AIN1	SEN_AIN1	8.9		
	JP2		1	I2C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0 5/3.3	_	High	1	High		High	CXD5602GG	I2C0	P1j	GPIO	I2C0_BCK	_	_	7		
	JP2		2	I2C0_SDA	D14	PIN_I2C0_BDT	45	Digital	1/0 5/3.3	_	High	1	High	1	High	CXD5602GG	12C0	P1j	GPIO	I2C0_BDT	_	_	7		
	JP2		3	AREF	_	_	_	_	O 5/3.3	0	High	0	High	0	High	_	_	_	_	_	_	_			
	JP2		4	GND	_	_	_	_		_	_	_	_	_	_	_	_	_	_	_	_	_			
	JP2		5	SPI4_SCK	D13	PIN_SPI4_SCK	72	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_SCK	_	GPIO	7		
	JP2		6				7.1				Hi-Z						SPI4	_	GPIO	_		GPIO	7		
			7	SPI4_MISO	D12	PIN_SPI4_MISO	74	Digital					Hi-Z		Hi-Z	CXD5602GG		P1o		SPI4_MISO	_		<u>'</u>		
	JP2		7	SPI4_MOSI	D11	PIN_SPI4_MOSI	73	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPI0	SPI4_MOSI	_	GPI0			
	JP2		8	SPI4_CS_X	D10	PIN_SPI4_CS_X	/1	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_CS_X	_	GPIO	/		
	JP2		9	PWM2	D09	PIN_PWM2	48	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM2	I2C1_BCK	_	7		
	JP2		10	SPI2_MISO	D08	PIN_SPI2_MISO	30	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MISO	UART0_RTS	GPIO	7		
	JP13		1	SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	_	_	7		
	JP13		2	PWM0	D06	PIN_PWM0	46	Digital	1/0 5/3.3	_	Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM0	_	_	7		
	JP13		3	PWM1	D05	PIN_PWM1	47	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM1	GPIO	-	7		
	JP13		4	SPI2_MOSI	D04	PIN_SPI2_MOSI	29	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MOSI	UARTO_CTS	GPIO	7		
	JP13		5	PWM3	D03	PIN_PWM3	49	Digital	I/O 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM3	I2C1_BDT	_	7		
			-				.,	0	3, 3, 3, 3		 				, <u>-</u>				5 , 0		HIF_IRQ_OUT				
	JP13		6	HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0 5/3.3	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	HIF_IRQ_OUT	P02	GPIO	HIF_IRQ_OUT	(Open Drain)	GNSS_1PPS_OUT	7		
	ID13		7	LIADT TV	D01	DIN LIADTO TV	C7	Diwited	1/0 5/22		11: 7		LI: 7		11: 7	CYDECOSOS	LIADTO	D1	CDIO	LIADTO TV		CDIO	7		
	JP13		1	UART_TX	D01	PIN_UART2_TX	07	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO OPIO	UART2_TX	_	GPIO	- 1		
	JP13	0.115	8	UART_RX	D00	PIN_UART2_RX	80	Digital	1/0 5/3.3	_	Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RX	_	GPIO	1		
		CN9	1	MAIN_POWER				Power	0 4~5	0	High	0	High	0	High	_	_	_	_	_	_	_			
		CN9	2	GND				Power		_	High	_	High		High	_	_	_	_	_	_	_			
		CN9	3	SPR_SEN_AIN1	A5	HPADC1	_	Analog	I ~5		_		-	ı	Hi-Z	CXD5602GG	_	_	_	_	_	_	8.9		
		CN9	4	SPR_SEN_AIN0	A4	HPADC0	_	Analog	l ~5		_		_		Hi-Z	CXD5602GG	_	_	_	_	_	_	8.9		
		CN9	5	3.3V				Power	O 3.3	0	-	0	_	0	_	_	_	_	_	_	_	-			
		CN9	6	GND				Power		_	_	_	_	_	_	_	-	-	_	_	_	-			
																			_		HIF_IRQ_OUT				
		CN9	7	HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0 5/3.3	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	HIF_IRQ_OUT	P02	GPIO	HIF_IRQ_OUT	(Open Drain)	GNSS_1PPS_OUT	7		
		CN9	8	SPI3_SCK	D29	PIN_SPI3_SCK	41	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_SCK	— (Open Drain)	_	7		
		CN9	9	PWM0	D06	PIN_PWM0	16	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	PWMA	P1k	GPIO GPIO	PWM0	_	_	7		
		+	_				43				Hi-Z	_					SPI3	P1i	GPIO GPIO		_	_	7		
		CN9	10	SPI3_MISO	D30	PIN_SPI3_MISO	43	Digital	1/0 5/3.3				Hi-Z		Hi-Z	CXD5602GG				SPI3_MISO			7		
		CN9	11	PWM1	D05	PIN_PWM1	4/	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM1	GPIO	_			
		CN9	12	SPI3_MOSI	D31	PIN_SPI3_MOSI	42	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_MOSI	-	_			
		CN9	13	PWM2	D09	PIN_PWM2	48	Digital	1/0 5/3.3		Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM2	I2C1_BCK	_	7		
		CN9	14	SPI3_CS0_X	D32	PIN_SPI3_CS0_X	38	Digital	I/O 5/3.3		Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	SPI3_CS0_X	P1f	GPIO	SPI3_CS0_X	_	_	7		
		CN9	15	PWM3	D03	PIN_PWM3	49	Digital	I/O 5/3.3	_	Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM3	I2C1_BDT	_	7		
		CN9	16	SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	_	-	7		
					_																				

Date	Contents
2020.11.13	First draft.
2021.4.14	Corrected the initial value of ACP_GPO1 to 7 after starting Arduino and SDK. Corrected each initial value of XRST (SPR_RST_X).
2021.12.20	Corrected the voltage range of the pins connected to the CXD5247GF to 3.6-4.4V. Corrected the initial values after turning on the power of MCLK and after starting Aruduino.
2022.9.7	Corrected the name of SPR_SPI2_SCK on ArduinoIDE.