P	ef				ArduinoIDE		SUK FW				電流	松 7 	Arduii	no起動後	SDK起	2 新		ピングルー	. →°	I	CYD56020	GG ピン機能		最大絶対定格		
Main Ext	Cam LTE	Ľ	ピン	回路図上の名前	ArduinoiDE 上の名称	SDK上の名称	SDK上の ピン番号	タイプ	dir	電圧	电 <i>i</i> r	投入後 初期値	dir	初期値		2到後 	接続先	モード名		モード0	€ CXD30020	モード2	モード3	■ 取入紀刈足俗 電圧(V)	拡張ボード上の機能	説明
JP1			1	GND	_	_	_	Power	-	_	_	—	_		-	_	-	-	_	_	_	-	_			
JP1			2	UART2_TX	D01	PIN_UART2_TXD	67	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_TXD	-	GPIO	2.5		_
JP1			3	UART2_RX	D00	PIN_UART2_RXD	68	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RXD	-	GPIO	2.5		
JP1			4	UART2_RTS	D28	PIN_UART2_RTS	70	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RTS	_	GPIO	2.5		
JP1			5	UART2_CTS	D27	PIN_UART2_CTS	69	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO GPIO	UART2_CTS	-	GPI0	2.5		
JP1			7	I2S0_BCK	D26	PIN_I2SO_BCK	93	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z Hi-Z	CXD5602GG	1250	P1v	GPIO CRIO	I2S0_BCK	_	GPIO GPIO	2.5		
JP1 JP1			γ 2	I2S0_LRCK SPI5_CS_X	D25 D24	PIN_I2S0_LRCK PIN_SPI5_CS_X	76	Digital Digital	1/0	1.8	_	Hi-Z Hi-Z	_	Hi-Z Hi-Z	_	Hi-Z Hi-Z	CXD5602GG CXD5602GG	I2S0 EMMCA	P1v P1p	GPIO GPIO	I2S0_LRCK EMMC_CMD	SPI5_CS_X	GPIO GPIO	2.5		
JP1			9	SPI5_SCK	D23	PIN_SPI5_SCK	75	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMCA	P1p		EMMC_CLK	SPI5_SCK	GPIO	2.5		
JP1		1	10	3.3V	_	-		Power	0	3.3	0	_	0	_	0	_	-		-	_	-	-	-			
JP1		1	11	1.8V	_	_	_	Power	0	1.8	0	_	0	_	0	_	_	_	_	_	_	-	_			
JP1		1	12	SEN_IRQ	D22	PIN_SEN_IRQ_IN	37	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SEN_IRQ_IN	P1e	GPIO	SEN_IRQ_IN	-	_	2.5		
JP1		1	13	SEN_AIN4	A2	LPADC2	_	Analog	I	~0.7	I	-	I	_	I	_	CXD5602GG		-	_	_	_	_	1.05		
JP2			1	XRST	_	(SPR_RST_X)	_	Digital	0	1.8	_	Low	0	High	0	High	CXD5602GG		_	_	_	-	_			
JP2			2	1.8V	_	-	_	Power	0	1.8	0		0	_	0		_			_	_	_	_	ļ		
JP2			3	3.7V(4.0V)	- D21	DINI EMMO DATAS	- 00	Power	1/0	3.6-4.4	0	- 11: 7	0	- 11: 7	0	- 11: 7	- -			-		_	-	/		
JP2 JP2			5	GPIO GPIO	D21 D20	PIN_EMMC_DATA3 PIN_EMMC_DATA2	80 70	Digital Digital	1/0	1.8	_	Hi-Z Hi-Z	_	Hi-Z Hi-Z	_	Hi-Z Hi-Z	CXD5602GG CXD5602GG	EMMCB EMMCB	P1q P1q	GPIO GPIO	EMMC_DATA3 EMMC_DATA2	_	GPIO GPIO	2.5		
JP2			6	I2SO_DATA_IN	D20 D19	PIN_I2SO_DATA_IN	95	Digital	1/0	1.8	_	Hi-Z	 	Hi-Z	_	Hi-Z	CXD5602GG CXD5602GG	1280	P1q P1v	GPIO GPIO	I2SO_DATA_IN	_	GPIO GPIO	2.5		
JP2			7	I2S0_DATA_OUT	D18	PIN_I2S0_DATA_OUT	96	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	1250	P1v	GPI0	I2S0_DATA_OUT	_	GPIO GPIO	2.5		
JP2				SPR_SPI5_MISO	D17	PIN_SPI5_MISO	78	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_DATA1	SPI5_MISO	GPIO	2.5		
JP2				SPR_SPI5_MOSI	D16	PIN_SPI5_MOSI	77	Digital	1/0	1.8	_	Hi-Z	<u>_</u>	Hi-Z	_	Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_DATA0	SPI5_MOSI	GPIO	2.5		
JP2		1	10	GND	_	-	_	Power	_	_	_	-	_	_	-	_	-	_	-	_	_	-	_			
JP2			11	I2C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0	1.8	-	High		High		High	CXD5602GG	I2C0	P1j	GPIO	I2C0_BCK	_	_	2.5		
JP2			12	I2C0_SDA	D14	PIN_I2CO_BDT	45	Digital	1/0	1.8	-	High		High		High	CXD5602GG	12C0	P1j	GPIO	I2C0_BDT	-	_	2.5		
JP2	ONI1	1	13	SEN_AIN5	A3	LPADC3	_	Analog		~0.7		_		-		-	CXD5602GG	_	_	-	_	_	_	1.05		
CN5 CN5	CN1 CN1		2	MCLK GND	_			Digital Power	U _	T.8	_		0	Hi-Z	_	HI-Z	26MHz TCXO					_		2.5		
CN5	CN1		3	I2C_SDA		PIN_SPI0_MISO	20	Digital	1/0	1.8	_	High	_	High	_	High	CXD5602GG	12C2	P17	GPIO	I2C2_BDT	SPI0_MISO	GPI0	2.5		
CN5	CN1		4	I2C_SCL	_	PIN_SPI0_MOSI	19	Digital	1/0	1.8	_	High	_	High	' I	High	CXD5602GG	12C2	P17	GPIO	I2C2_BCK	SPI0_MOSI	GPIO	2.5		
CN5	CN1		5	_ XRS	D35	PIN_SDIO_DIR1_3	91	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SDIOC	P1t	GPIO	SDIO_DIR1_3	GPIO	GPIO	2.5		
CN5	CN1		6	PWDN	D34	PIN_SDIO_DIR0	90	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SDIOC	P1t	GPIO	SDIO_DIR0	GPI0	GPI0	2.5		
CN5	CN1		7	LDO_EN	_	(ACP_GPO4)	_	Digital	0	3.6-4.4	0	Hi-Z	0	Low	0	Low	CXD5247GF	_	_	_	_	-	_			
CN5	CN1		8	VDD_3.7V	_	_	-	Power	0	3.6-4.4	0	_	0	_	0	_	-	_	_	_	_	_	_			
CN5	CN1		9	IS_DATA4	_	PIN_IS_DATA4	63	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	1	_	CXD5602GG	IS	P1m	GPIO	IS_DATA4	GPI0	GPIO	2.5		
CN5	CN1	1	10	IS_DATA6	_	PIN_IS_DATA6	65	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	1	_	CXD5602GG	IS	P1m	GPIO OPIO	IS_DATA6	GPIO	GPI0	2.5		
CN5	CN1	1	12	IS_DATA0	_	PIN_IS_DATA7	59	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z		_	CXD5602GG	IS	P1m	GPIO CRIO	IS_DATA7	GPIO CDIO	GPIO	2.5		
CN5 CN5	CN1 CN1		12 13	IS_DATA7 IS_DATA5		PIN_IS_DATA7 PIN_IS_DATA5	64	Digital Digital	1/0	1.8	_	Hi-Z Hi-Z	_	Hi-Z Hi-Z	1		CXD5602GG CXD5602GG	IS IS	P1m P1m	GPIO GPIO	IS_DATA7 IS_DATA5	GPIO GPIO	GPIO GPIO	2.5		
CN5	CN1		14	IS_DATA2	_	PIN_IS_DATA2	61	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	'		CXD5602GG	IS	P1m	GPI0	IS_DATA2	GPIO	GPIO	2.5		
CN5	CN1	1	15	IS_HSYNC	_	PIN_IS_HSYNC	58	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	i	_	CXD5602GG	IS	P1m	GPIO	IS_HSYNC	GPIO GPIO	GPIO	2.5		
CN5	CN1	1	16	IS_DATA3	_	PIN_IS_DATA3	62	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	ı	_	CXD5602GG	IS	P1m	GPIO	IS_DATA3	GPIO	GPIO	2.5		
CN5	CN1	1	17	IS_VSYNC	-	PIN_IS_VSYNC	57	Digital	1/0	1.8	-	Hi-Z	_	Hi-Z	I	-	CXD5602GG	IS	P1m	GPIO	IS_VSYNC	GPIO	GPIO	2.5		
CN5	CN1	1	18	IS_DATA1	_	PIN_IS_DATA1	60	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA1	GPIO	GPI0	2.5		
CN5	CN1		19	GND	_	_	_	Power	-	_	-		-	_	-	_	_	_	-	_	_	_	_			
CN5	CN1		20	IS_CLK	_	PIN_IS_CLK	56	Digital		1.8		-		_			CXD5602GG	IS	P1m	GPIO	IS_CLK	GPI0	GPIO	2.5		
CN4[L] CN4[L]	CN4		1	3.3V_AU	_	_	<u> </u>	Power	0	3.3		_		_			-	-	-	-	_	_	-			
CN4[L] CN4[L] CN4[L]	CN4 CN4		5	3.3V_AU ACP_MICA	_	_		Power Analog	U	3.3				_			CXD5247GF					_				
CN4[L] CN4[L]	CN ²		7	ACP_MICB		_	<u> </u>	Analog			'	_		_	'		CXD5247GF CXD5247GF					_				
CN4[L] CN4[L]	CN ²		9	ACP_MICC	_	_	_	Analog			<u>'</u>	_	 	_	'		CXD5247GF		_	_	_	_	_			
CN4[L] CN4[L]	CN ²		11	ACP_MICD	-	_	_	Analog		_		_	1	_		_	CXD5247GF		_	-	_	_	_			
CN4[L] CN4[L]	CN4		13	ACP_MICBIASA		-		Analog	0	2	0		0		0		CXD5247GF	-	<u> </u>	_	-	_	_			
CN4[L] CN4[L]	CN ²	4[L] 1	15	ACP_MICBIASB	_	-		Analog	0	2	0	_	0	_	0	_	CXD5247GF	_		_	_	-	_			
CN4[L] CN4[L]	CN4		17	AGND_MIC	_	_	_	Power	-	_	_	_	_	_	-	_	_	-	_	_	_	_	_			
CN4[L] CN4[L]	CN4		19	SPR_I2C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0	1.8	-	High		High		High	CXD5602GG	12C0	P1j	GPI0	I2C0_BCK	_	_	2.5		
CN4[L] CN4[L]	CN4		21	SPR_I2CO_SDA	D14	PIN_I2CO_BDT	45	Digital	1/0	1.8		High		High		High	CXD5602GG	1200	P1j	GPIO CRIO	I2C0_BDT	_	- CDIO	2.5		
CN4[L] CN4[L]	CN/		23	SPR_SPI4_SCK	D13	PIN_SPI4_SCK PIN_SPI4_MISO	71	Digital	1/0	1.8	_	Hi-Z	<u> </u>	Hi-Z		Hi-Z Hi-Z	CXD5602GG	SPI4 SPI4	P10	GPIO GPIO	SPI4_SCK SPI4_MISO	_	GPIO GPIO	2.5		
CN4[L] CN4[L] CN4[L]	CN4 CN4			SPR_SPI4_MISO SPR_SPI4_MOSI	D12 D11	PIN_SPI4_MISO PIN_SPI4_MOSI	73	Digital Digital	1/0	1.8	_	Hi-Z Hi-Z	 	Hi-Z Hi-Z	_	HI-Z Hi-Z	CXD5602GG CXD5602GG	SPI4 SPI4	P1o P1o	GPIO GPIO	SPI4_MISO SPI4_MOSI	_	GPIO GPIO	2.5		
CN4[L] CN4[L]	CN ²			SPR_SPI4_CS_X	D11	PIN_SPI4_CS_X	71	Digital	1/0	1.8	_	Hi-Z	 	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P10	GPIO	SPI4_IVIOSI	_	GPIO	2.5		
CN4[L] CN4[L]	CN ²		31	SPR_PWM2	D09	PIN_PWM2	48	Digital	1/0	1.8	_	Hi-Z	 	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPI0	PWM2	I2C1_BCK		2.5		
CN4[L] CN4[L]	CN4		33	SPR_I2S0_LRCK	D25	PIN_I2S0_LRCK	94	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	12S0	P1v	GPIO	I2S0_LRCK	-	GPIO	2.5		
CN4[L] CN4[L]	CN4		35	GND	_			Power		_	_		_				_	_		_	_	_				
CN4[L] CN4[L]	CN ²	4[L] 3	37 SF	PR_I2SO_DATA_OUT	D18	PIN_I2SO_DATA_OUT	96	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	12S0	P1v	GPIO	I2S0_DATA_OUT	-	GPIO	2.5		
CN4[L] CN4[L]	CN4		39	SPR_PWM0	D06	PIN_PWM0	46	Digital	1/0	1.8		Hi-Z		Hi-Z	-	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM0	_	_	2.5		
CN4[L] CN4[L]	CN ²		41	SPR_PWM1	D05	PIN_PWM1	47	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	PWMA	P1k	GPI0	PWM1	GPIO	-	2.5		
CN4[L] CN4[L]	CN4		_	SPR_I2SO_DATA_IN	D19	PIN_I2SO_DATA_IN	95	Digital	1/0	1.8	_	Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	12S0	P1v	GPIO OPIO	I2SO_DATA_IN	- 1201 DDT	GPI0	2.5		
CN4[L] CN4[L]	CN4		45	SPR_PWM3	D03	PIN_PWM3	49	Digital	1/0	1.8		Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	PWMB	P1I	GPIO CRIO	PWM3	I2C1_BDT	-	2.5		
CN4[L] CN4[L]	CN4	4[L] /	4/	SPR_I2S0_BCK	D26	PIN_I2S0_BCK	93	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	12S0	P1v	GPI0	I2S0_BCK	_	GPI0	2.5		

L LINDII I NIZI	CNALL	40	CDD LIADTO TV	D01	DIN LIADTA TV	67	Digital	1/0	1 0	1	11: 7		11: 7	11:-	CVDEGO3CC	LIADTO	D1.0	CDIO	LIADT2 TV	Ι	CDIO	2.5	
CN4[L] CN4[L] CN4[L]	CN4[L]	49 51	SPR_UART2_TX SPR_UART2_RX	D01 D00	PIN_UART2_TX PIN_UART2_RX	67 68	Digital Digital	1/0	1.8	<u> </u>	Hi-Z Hi-Z	_	Hi-Z Hi-Z	- Hi-2	CXD5602GG CXD5602GG	UART2 UART2	P1n P1n	GPIO GPIO	UART2_TX UART2_RX		GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	53	SPR_UART2_RTS	D28	PIN_UART2_RTS	70	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	— Hi-7	CXD5602GG		P1n	GPIO	UART2_RTS	_	GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L]		SPR_UART2_CTS	D27	PIN_UART2_CTS	69	Digital	1/0	1.8		Hi-Z	_	Hi-Z	- Hi-2	CXD5602GG	UART2	P1n	GPIO	UART2 CTS	_	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	57	SPR_EMMC_CLK	D23	PIN_EMMC_CLK	75	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	- Hi-2	CXD5602GG		P1p	GPIO	EMMC_CLK	SPI5_SCK	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	59	SPR_EMMC_CMD	D24	PIN_EMMC_CMD	76	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG	EMMC A	P1p	GPI0	EMMC_CMD	SPI5_CS_X	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	61	SPR_EMMC_DATA0	D16	PIN EMMC DATA0	77	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG		P1p	GPIO	EMMC_DATA0	SPI5_MOSI	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	63	SPR_EMMC_DATA1	D17	PIN_EMMC_DATA1	78	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG	_	P1p	GPIO	EMMC_DATA1	SPI5_MISO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]		SPR_EMMC_DATA2	D20	PIN_EMMC_DATA2	79	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG	+	P1q	GPIO	EMMC_DATA2	_	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	67	SPR_EMMC_DATA3	D21	PIN_EMMC_DATA3	80	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG	_	P1q	GPIO	EMMC_DATA3	_	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	69	GND		-	_	Power	-	_	<u> </u>	-	_	_		-	_	-	-	-	_	_		
CN4[L] CN4[L]	CN4[L]	71	SPR_SPI2_SCK	D43	PIN_SPI2_SCK	28	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG	SPI2A	P00	GPIO	SPI2_SCK	UART0_RXD	I2C3_BDT	2.5	
CN4[L] CN4[L]	CN4[L]	73	SPR_SPI2_MISO	D08	PIN_SPI2_MISO	30	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG		P01	GPIO	SPI2_MISO	UARTO RTS	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	75	SPR_SPI2_MOSI	D04	PIN_SPI2_MOSI	29	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG	SPI2B	P01	GPIO	SPI2_MOSI	UARTO_CTS	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	77	SPR_SPI2_CS_X	D42	PIN_SPI2_CS_X	27	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	– Hi- <u>z</u>	CXD5602GG		P00	GPIO	SPI2_CS_X	UART0_TXD	I2C3_BCK	2.5	
CN4[L] CN4[L]	CN4[L]	79	1.8V	_		_	Power	0	1.8	0	_	0	_	0 -	_	_	_	_		_	_		
CN4[L] CN4[L]	CN4[L]	81	SPR_SDIO_CMDDIR	D33	PIN_SDIO_CMDDIR	89	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	0 Hi-2	CXD5602GG	SDIOC	P1t	GPIO	SDIO_CMDDIR	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	83	SPR_SDIO_CLK	D38	PIN_SDIO_CLK	81	Digital	1/0	1.8	 	Hi-Z	_	Hi-Z	O Lov	CXD5602GG		P1u	GPIO	SDIO_CLK	SPI5_SCK	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	85	SPR_SDIO_CMD	_	PIN_SDIO_CMD	82	Digital	1/0	1.8	<u> </u>	Hi-Z	_	Hi-Z	0 Hi-2	CXD5602GG	_	P1r	GPIO	SDIO_CMD	SPI5_CS_X	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]		SPR_SDIO_DATA0	_	PIN_SDIO_DATA0	83	Digital	1/0	1.8	<u> </u>	Hi-Z	_	Hi-Z	O Lov	CXD5602GG		P1r	GPIO	SDIO_DATA0	SPI5_MOSI	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	89	SPR_SDIO_DATA1	_	PIN_SDIO_DATA1	84	Digital	1/0	1.8	<u> </u>	Hi-Z	_	Hi-Z	O Lov	CXD5602GG		P1r	GPIO	SDIO_DATA1	SPI5_MISO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	91	SPR_SDIO_DATA2	_	PIN_SDIO_DATA2	85	Digital	1/0	1.8	-	Hi-Z	_	Hi-Z	O Lov	CXD5602GG		P1r	GPIO	SDIO_DATA2	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	93	SPR_SDIO_DATA3	_	PIN_SDIO_DATA3	86	Digital	1/0	1.8	-	Hi-Z	_	Hi-Z	O Lov	CXD5602GG	+	P1r	GPIO	SDIO_DATA3	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]		GND	_	_	_	Power	 -	-	-	_	_	_		_	_	_	_	_	_	_		
CN4[L] CN4[L]	CN4[L]	97	SPR_SDIO_WP	D37	PIN_SDIO_WP	88	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG	SDIOB	P1s	GPIO	SDIO_WP	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	99	GND	_		_	Power	 	_	_	_	_	_		_	_	_	_	_	_	_		
CN4[R] CN4[R]	CN4[R]	2	5V	_	_	_	Power	1/0	5	0	_	0	_	0 -	_	_	_	_	_	_	_	6	
CN4[R] CN4[R]	CN4[R]		5V	_	_	_	Power	1/0	5	0	_	0	_	0 -	_	_	_	_	_	_	_	6	
CN4[R] CN4[R]	CN4[R]	6	ACP_SPAP	_	_	_	Analog	0	3.3	_	Hi-Z	-	Hi-Z	– Hi-2	CXD5247GF	_	_	_	-	_	_		
CN4[R] CN4[R]	CN4[R]		ACP_SPAN	_	_	_	Analog	0	3.3	-	Hi-Z	_	Hi-Z	- Hi-2	CXD5247GF		_	_	_	_	_		
CN4[R] CN4[R]	CN4[R]		ACP_SPBN	_	_	_	Analog	0	3.3	_	Hi-Z	_	Hi-Z	- Hi-2	CXD5247GF		_	_	_	_	_		
CN4[R] CN4[R]	CN4[R]	12	ACP_SPBP	_	_	_	Analog	0	3.3	-	Hi-Z	-	Hi-Z	– Hi-2	CXD5247GF	_	_	_	_	_	_		
CN4[R] CN4[R]	CN4[R]	14	AGND_DRV	_	_	_	Power	-	-	† –	-	-	_	- -	_	_	_	_	_	_	_		
CN4[R] CN4[R]	CN4[R]	16	SPR_SWDIO	_	_	_	Digital	1/0	1.8	_	Hi-Z	-	Hi-Z	– Hi-2	CXD5602GG	-	_	_	_	_	_	2.5	
CN4[R] CN4[R]	CN4[R]	18	SPR_SWDCLK	-	-	_	Digital	I	1.8	I	Hi-Z	I	Hi-Z	l Hi-Z	CXD5602GG	_	-	_	-	-	-	2.5	
CN4[R] CN4[R]	CN4[R]	20	ACP_CLK_DMIC	_	_	_	Digital	0	1.8	0	Hi-Z	0	Hi-Z	0 Hi-2	CXD5247GF	_	_	_	-	-	_		
CN4[R] CN4[R]	CN4[R]	22	XRS_PWON	-	-	_	Power	1/0	3.3	1/0	Low	1/0	Low	I/O Lov	CXD5247GF	_	_	_	-	-	-	7	
CN4[R] CN4[R]	CN4[R]	2/	SPR_GNSS_1PPS_OUT	D44	PIN_GNSS_1PPS_OUT	6	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	– Hi-2	CADEEUSCC	GNSS_1PPS_OUT	P14	GPIO	GNSS_1PPS_OUT	CPU_WDT	CPU_WDT	2.5	
CN4[K] CN4[K]	CN4[K]	24	SPK_GN35_1PP5_001	D44	FIIN_GINSS_1FFS_001	O	Digital	1/0	1.0	_	⊓I-Z	_	⊓1-Z	_	CAD3002GG	GN32_1FF2_001	P 14	GPIO	GN33_1FF3_001	CPO_WDI	(0 5 .)	2.0	
CN4[R] CN4[R]										1		l I									(Open Drain)		
	CN4[R]	26	SPR_SEN_IRQ_IN	D22	PIN_SEN_IRQ_IN	37	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	– Hi-2	CXD5602GG		P1e	SEN_IRQ_IN	SEN_IRQ_IN	SEN_IRQ_IN	SEN_IRQ_IN	2.5	
						37				_		-				SEN_IRQ_IN				SEN_IRQ_IN HIF_IRQ_OUT	SEN_IRQ_IN		
CN4[R] CN4[R]	CN4[R]		SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT	D22	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT	37	Digital Digital	1/0		_	Hi-Z Hi-Z	-	Hi-Z Hi-Z	- Hi-2		SEN_IRQ_IN	P1e P02	SEN_IRQ_IN GPIO	SEN_IRQ_IN HIF_IRQ_OUT				
		28				37 31 –				-		-				SEN_IRQ_IN				HIF_IRQ_OUT	SEN_IRQ_IN		
CN4[R] CN4[R]	CN4[R]	28 30	SPR_HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	37 31 - -	Digital		1.8		Hi-Z		Hi-Z	- Hi-2	CXD5602GG	SEN_IRQ_IN HIFIRQ -	P02	GPIO	HIF_IRQ_OUT	HIF_IRQ_OUT (Open Drain)	SEN_IRQ_IN GNSS_1PPS_OUT		
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R]	28 30 32	SPR_HIF_IRQ_OUT GND SPR_RST_X	D02 _ _	PIN_HIF_IRQ_OUT	37 31 - -	Digital Power Digital	I/O - 0	1.8 - 1.8	-	Hi-Z – Low		Hi-Z – High	- Hi-Z	CXD5602GG - CXD5602GG	SEN_IRQ_IN HIFIRQ	P02 _ _	GPIO	HIF_IRQ_OUT	HIF_IRQ_OUT (Open Drain) – –	SEN_IRQ_IN GNSS_1PPS_OUT -	2.5	
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R] CN4[R]	28 30 32 34	SPR_HIF_IRQ_OUT GND	D02	PIN_HIF_IRQ_OUT -	37 31 - - 5	Digital Power		1.8 - 1.8 1.8		Hi-Z		Hi-Z	- Hi-2	CXD5602GG - CXD5602GG	SEN_IRQ_IN HIFIRQ	P02 -	GPIO –	HIF_IRQ_OUT -	HIF_IRQ_OUT (Open Drain) –	SEN_IRQ_IN GNSS_1PPS_OUT		
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	28 30 32 34	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2	D02 _ _	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0	37 31 - - 5	Digital Power Digital	I/O - 0	1.8 - 1.8	-	Hi-Z – Low		Hi-Z – High	- Hi-Z	CXD5602GG - CXD5602GG CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK -	P02 _ _	GPIO	HIF_IRQ_OUT	HIF_IRQ_OUT (Open Drain) – –	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT	2.5	
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	28 30 32 34 36 38	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3	D02 D40 A0 A1	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1	37 31 - - 5 -	Digital Power Digital Digital	I/O - 0	1.8 - 1.8 1.8 0.7 0.7	-	Hi-Z Low Hi-Z		Hi-Z - High Hi-Z	- Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P02 - - P13	GPIO GPIO	HIF_IRQ_OUT - - AP_CLK	HIF_IRQ_OUT (Open Drain) - - PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT	2.5 2.5 1.05 1.05	
CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	28 30 32 34 36 38 40	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4	D02 D40	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2	37 31 - - 5 - - -	Digital Power Digital Digital Analog	I/O - 0	1.8	-	Hi-Z Low Hi-Z		Hi-Z - High Hi-Z	- Hi-Z	CXD5602GG - CXD5602GG CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P02 - - P13	GPIO GPIO	HIF_IRQ_OUT - - AP_CLK	HIF_IRQ_OUT (Open Drain) - - PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT	2.5 2.5 1.05 1.05 1.05	
CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	28 30 32 34 36 38 40 42	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5	D02 D40 A0 A1 A2 A3	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3	37 31 - - 5 - - -	Digital Power Digital Digital Analog Analog	I/O - 0	1.8	-	Hi-Z Low Hi-Z -		Hi-Z - High Hi-Z	- Hi-Z	CXD5602GG - CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P02 P13	GPIO GPIO	HIF_IRQ_OUT - AP_CLK - -	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT	2.5 2.5 1.05 1.05 1.05 1.05	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0	D02 D40 A0 A1 A2 A3 A4	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0	37 31 - - 5 - - - -	Digital Power Digital Digital Analog Analog Analog Analog Analog	I/O - 0	1.8	-	Hi-Z Low Hi-Z -		Hi-Z - High Hi-Z	- Hi-Z	CXD5602GG - CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P02 P13	GPIO GPIO	HIF_IRQ_OUT - AP_CLK - -	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 1.05 1.05 1.05 1.05 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1	D02 D40 A0 A1 A2 A3	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3	37 31 - - 5 - - - - -	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog	I/O - 0	1.8	-	Hi-Z Low Hi-Z - - - - -		Hi-Z - High Hi-Z	- Hi-Z	CXD5602GG - CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P02 P13	GPIO - GPIO	HIF_IRQ_OUT - AP_CLK - - -	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 1.05 1.05 1.05 1.05	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND	D02 D40 A0 A1 A2 A3 A4 A5 -	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 -	37 31 - - 5 - - - - -	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Power	I/O - 0	1.8	-	Hi-Z Low Hi-Z		Hi-Z High Hi-Z	- Hi-Z O High	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P02	GPIO - GPIO - GPIO	HIF_IRQ_OUT - AP_CLK - - - - - - - - - - - - -	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X	D02 D40 A0 A1 A2 A3 A4 A5 - D07	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X	37 31 5 39	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Digital	I/O	1.8	-	Hi-Z Low Hi-Z Hi-Z		Hi-Z High Hi-Z Hinch Hi-Z	- Hi-Z O High	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X	P02 P13 P1g	GPIO - GPIO - GPIO GPIO	HIF_IRQ_OUT - AP_CLK - - - SPI3_CS1_X	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI	37 31 5 39 42	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Digital Digital Digital	I/O	1.8	-	Hi-Z Low Hi-Z - - - - - Hi-Z Hi-Z Hi-Z		Hi-Z High Hi-Z HiBH Hi-Z Hi-Z Hi-Z	- Hi-Z O High - Hi-Z	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3	P02 P13 P1g P1i	GPIO - GPIO - GPIO - GPIO - GPIO GPIO GPIO	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital	I/O	1.8 - 1.8 1.8 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8	-	Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- Hi-Z O High - Hi-Z - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3	P02	GPIO - GPIO - GPIO - GPIO GPIO GPIO GPIO	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital	I/O	1.8	-	Hi-Z Low Hi-Z - - - - - Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- Hi-Z - O High - Hi-Z - I - I - I - I - I - I - I - I - I - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3	P02	GPIO - GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT - PMU_WDT (Open Drain)	2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital	I/O	1.8 - 1.8 1.8 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8	-	Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- Hi-Z O High - Hi-Z - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3	P02	GPIO - GPIO - GPIO - GPIO GPIO GPIO GPIO	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_CS0_X	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital	I/O	1.8	-	Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- Hi-Z - O High - Hi-Z - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3_CS0_X	P02 P13 P1g P1i P1i P1i P1f	GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Digital	I/O	1.8		Hi-Z Low Hi-Z - - - - - Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- Hi-Z - O High - Hi-Z - I - I - I - I - I - I - I - I - I - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P02	GPIO - GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_SCK SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Analog	I/O	1.8		Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- Hi-Z - O High - Hi-Z - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P02 P13 P1g P1i P1i P1i P1f	GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DP	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32 D41	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_SCK PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Analog	I/O	1.8		Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- 0 - 1 1 1 - - - -	Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- Hi-Z - O High - Hi-Z - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P02	GPIO - GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPI	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32 D41 -	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT -	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Digital	I/O	1.8		Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- 0 - 1 1 1 - - - -	Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- Hi-Z - O High - Hi-Z - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P02	GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	HIF_IRQ_OUT - AP_CLK - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT -	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_BOSI SPR_SPI3_SCK SPR_SPI3_SCK SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32 D41	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_SCK PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Digital Digital Power	I/O	1.8		Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- 0 - 1 1 1 - - - -	Hi-Z High Hi-Z	- Hi-Z - O High - Hi-Z - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P02	GPIO - GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPI	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_BOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32 D41 - D39	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_SCK PIN_SPI3_CS0_X PIN_SPI3_CS0_X PIN_HIF_GPIO0	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Analog Digital	I/O	1.8		Hi-Z Low Hi-Z	- 0 - 1 1 1 - - - -	Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- Hi-Z - O High - Hi-Z - Hi-Z - I - I - I - I - I - I - I - I - Hi-Z	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P02	GPIO - GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPI	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO	HIF_IRQ_OUT (Open Drain) PMU_WDT RTC_IRQ_OUT (Open Drain) - GPIO	SEN_IRQ_IN GNSS_1PPS_OUT	2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32 D41 - D39 -	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIOO -	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Analog Digital	I/O	1.8		Hi-Z Low Hi-Z	- 0 - 1 1 1 - - - -	Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low	- Hi-Z - O High - Hi-Z - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 CS0_X RTC_IRQ_OUT - HIF_GPIO0	P02 P13	GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO -	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	Lowでミュート
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32 D41 D39	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIOO	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Analog Digital	I/O	1.8		Hi-Z Low Hi-Z	- O O O O O O O	Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- Hi-2 - O High - Hi-2 - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 CS0_X RTC_IRQ_OUT - HIF_GPIO0	P02	GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO GPIO	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain) GPIO - GPS_EXTLD	2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	Lowでミュート
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_MOSI SPR_SPI3_MSO SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7 ACP_VSYS	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32 D41 D39	PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_MISO PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIOO	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Analog Analog Digital Power Digital Power Digital	I/O	1.8		Hi-Z Low Hi-Z	- 0 - 1 - 1 - 1	Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low	- Hi-Z - O High - Hi-Z - O High - Hi-Z - I - I - I - I - I - I - I - I - Hi-Z - Hi-Z - Hi-Z - Hi-Z - Hi-Z - Hi-Z - O Lov O Lov O Lov O Lov	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 CS0_X RTC_IRQ_OUT - HIF_GPIO0	P02	GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	HIF_IRQ_OUT - AP_CLK - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO GPIO	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT	2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	Lowでミュート
CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76 78	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_BMOSI SPR_SPI3_BMOSI SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7 ACP_VSYS ACP_VSYS	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32 D41 D39	PIN_HIF_IRQ_OUT	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Analog Analog Digital Power Digital Power Digital Power Power	I/O	1.8		Hi-Z Low Hi-Z	- O O O O O O O	Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low	- Hi-2 - O High - Hi-2 - I	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 CS0_X RTC_IRQ_OUT - HIF_GPIO0	P02	GPIO - GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPI	HIF_IRQ_OUT AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO	HIF_IRQ_OUT (Open Drain)	SEN_IRQ_IN GNSS_1PPS_OUT	2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	Lowでミュート
CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76 78 80	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_BMOSI SPR_SPI3_SCK SPR_SPI3_SCK SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7 ACP_VSYS ACP_VSYS GND	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32 D41 D39	PIN_HIF_IRQ_OUT	41	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Analog Analog Digital Power Digital Power Power	I/O	1.8		Hi-Z Low Hi-Z	- 0 - 1 - 1 - 1	Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low Low Low	- Hi-Z - O High - Hi-Z - I - I - I - I - I - I - I - I - I - I - Hi-Z - I - I - I - I - I - I - I - I - I - I	CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3-CS0_X RTC_IRQ_OUT - HIF_GPIO0	P02	GPIO - GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPI	HIF_IRQ_OUT AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT GPIO	HIF_IRQ_OUT (Open Drain) PMU_WDT GPIO	SEN_IRQ_IN GNSS_1PPS_OUT	2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	Lowでミュート
CN4[R] CN4[R]	CN4[R] CN4[R]	28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76 78 80 82	SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_BMOSI SPR_SPI3_BMOSI SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7 ACP_VSYS ACP_VSYS	D02 D40 A0 A1 A2 A3 A4 A5 - D07 D31 D29 D30 D32 D41 D39	PIN_HIF_IRQ_OUT	41 43 38 4 ——————————————————————————————————	Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog Analog Analog Digital Power Digital Power Digital Power Power	I/O	1.8		Hi-Z Low Hi-Z Hi-Z	- 0 - 1 - 1 - 1	Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low Low Low Low	- Hi-Z - O High - Hi-Z - Hi-Z - I - I - I - I - I - I - I - I - Hi-Z - H	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P02	GPIO - GPIO - GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	HIF_IRQ_OUT AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO	HIF_IRQ_OUT (Open Drain) PMU_WDT	SEN_IRQ_IN GNSS_1PPS_OUT	2.5 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5	Lowでミュート

ON 4[D]	ON4[D]		0.0	000 1000 001		DINI ODIO MOOI	10	I 5: I	1./0	1.0		111.1	1./0				Loverson	ODIOD	D17		1000 DOI/	0010 14001		0.5		
		CN4[R]	86	SPR_I2C2_SCL ACP_GP00		PIN_SPI0_MOSI	19	Digital Digital	1/0	1.8 3.6-4.4	_	High Hi-Z	1/0	– Hi-Z		High Hi-Z	+ + + + + + + + + + + + + + + + + + + +	SPI0B	P17	GPIO _	I2C2_BCK	SPI0_MOSI -	_	2.5		
-	CN4[R]	CN4[R]	88 90	ACP_GP00 ACP GP01				Digital	0	3.6-4.4	0	Hi-Z	0		0		CXD5247GF CXD5247GF					_	_		オーディオ3.3V出力	Highで出力
CN4[N]	CN4[N]	CN4[N]	90	ACF_GFU1			_	Digital	0	3.0-4.4	U	111-2	0	Low	0	Low	CAD3247GI	_	_	_		_	_		LTE用電源出力	Tiigii СШЭЭ
CN4[R]	CN4[R]	CN4[R]	92	ACP_GPO2	_	_	_	Digital	0	3.6-4.4	0	Hi-Z	0	Low	0	Low	CXD5247GF	_	_	_	_	_	-		(LTE拡張ボードのみ)	Highで出力
																									メインボード側からの	
CN4[R]	CN4[R]	CN4[R]	94	ACP_GPO3	_	_	_	Digital	0	3.6-4.4	0	Hi-Z	0	Low	0	Low	CXD5247GF	_	_	_	_	_	_		LTE用電源出力	Highで出力
																									(LTE拡張ボードのみ)	
CN4[R]	CN4[R]	CN4[R]	96	SWOCLK	_	-	_	Digital	0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	_	_	_	-	_	_	2.5		
CN4[R]	CN4[R]	CN4[R]	98	SWO	_	-	_	Digital	0	1.8	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	_	_	-	-	_	-	2.5		
CN4[R]	CN4[R]	CN4[R]	100	GND	_	_	_	Power	_	-	_	_	_	_	_	_	_	_	_	_	_	_	_			
D7				SPR_I2S1_BCK	LED0	PIN_I2S1_BCK	97	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	I2S1	P1w	GPI0	I2S1_BCK	_	GPIO	2.5		
D6				SPR_I2S1_LRCK	LED1	PIN_I2S1_LRCK	98	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	I2S1	P1w	GPI0	I2S1_LRCK	_	GPIO	2.5		
D5				SPR_I2S1_DATA_IN	LED2	PIN_I2S1_DATA_IN	99	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	I2S1	P1w	GPI0	I2S1_DATA_IN	_	GPIO	2.5		
D4				SPR_I2S1_DATA_OUT	LED3	PIN_I2S1_DATA_OUT	100	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	12S1	P1w	GPIO	I2S1_DATA_OUT	_	GPI0	2.5		
	JP3		1	NC	_	_	_	_	_		_	_	-	_	-	_	_	_	_	_	_	_	_			
	JP3		2	VDD_LVS	_	_	_	Power	0	5/3.3	0	_	0	_	1/0	_	_	_	_	_	_	_	_	7		
	JP3		3	XRS_PWON	_	_	_	Digital	1/0	3.3	1/0	_	1/0	_	1/0			_	_	_	_	_	-	1		
	JP3 JP3		4 5	3.3V MAIN_POWER			_	Power Power	1/0	3.3	0 I/0		1/0		1/0		_			_			_	6		
	JP3		6	GND		_	_	Power	-	- -	1/0	_	-	_	1/0		_			_	_	_	_	U		
	JP3		7	GND		_	_	Power	_		_	_	_	_	_		_	_	_	_	_	_	_			
	JP3		8	5V_IN_PIN	_	_	_	_	_	_	_	_		_	_	_	_	_	_	_	_	_	_			
	JP4		1	SPR_SEN_AIN2	A0	LPADC0	_	Analog		~5		_	1	_		_	CXD5602GG	_	_	SEN_AIN2	SEN_AIN2	SEN_AIN2	SEN_AIN2	7.5		
	JP4		2	SPR_SEN_AIN3	A1	LPADC1	_	Analog	I	~5			1	_		_	CXD5602GG	_	_	SEN_AIN3	SEN_AIN3	SEN_AIN3	SEN_AIN3	7.5		
	JP4		3	SPR_SEN_AIN4	A2	LPADC2	_	Analog	I	~5		_	1	_		_	CXD5602GG	_	_	SEN_AIN4	SEN_AIN4	SEN_AIN4	SEN_AIN4	7.5		
	JP4		4	SPR_SEN_AIN5	A3	LPADC3	_	Analog	ı	~5	ı	_	ı	_		_	CXD5602GG	_	_	SEN_AIN5	SEN_AIN5	SEN_AIN5	SEN_AIN5	7.5		
	JP4		5	SPR_SEN_AIN0	A4	HPADC0	_	Analog	Ι	~5	I	_	I	_	ı	_	CXD5602GG	_	_	SEN_AIN0	SEN_AIN0	SEN_AIN0	SEN_AIN0	8.9		
	JP4		6	SPR_SEN_AIN1	A5	HPADC1	_	Analog	I	~5		_	I	_	1	_	CXD5602GG	_	_	SEN_AIN1	SEN_AIN1	SEN_AIN1	SEN_AIN1	8.9		
	JP2		1	I2C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0	5/3.3	_	High	Ι	High	I	High	CXD5602GG	I2C0	P1j	GPI0	I2C0_BCK	_	_	7		
	JP2		2	I2C0_SDA	D14	PIN_I2C0_BDT	45	Digital	1/0	5/3.3	_	High	I	High	1	High	CXD5602GG	12C0	P1j	GPI0	I2C0_BDT	_	_	7		
	JP2		3	AREF	_	-	_	_	0	5/3.3	0	High	0	High	0	High	_	_	_	-	-	_	_			
	JP2		4	GND	_	-	_	_	_	_	_	_	_	-	_		_	_	_	_	_	_	_			
	JP2		5	SPI4_SCK	D13	PIN_SPI4_SCK	72	Digital	1/0		_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPI0	SPI4_SCK	_	GPI0	7		
	JP2		6	SPI4_MISO	D12	PIN_SPI4_MISO	74	Digital			_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P10	GPIO	SPI4_MISO	_	GPIO	7		
	JP2		7	SPI4_MOSI	D11	PIN_SPI4_MOSI	73	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI4	P10	GPI0	SPI4_MOSI	_	GPI0	7		
	JP2		8	SPI4_CS_X	D10	PIN_SPI4_CS_X	/1	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P10	GPIO	SPI4_CS_X	- 1201 BOK	GPIO	7		
	JP2		10	PWM2	D09	PIN_PWM2	48	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB SPI2B	P1I	GPIO GPIO	PWM2	I2C1_BCK	- GPIO	7		
	JP2 JP13		10	SPI2_MISO SPI3_CS1_X	D08	PIN_SPI2_MISO PIN_SPI3_CS1_X	39	Digital Digital	1/0	5/3.3 5/3.3	_	Hi-Z Hi-Z		Hi-Z Hi-Z	_	Hi-Z Hi-Z	CXD5602GG CXD5602GG	SPI3_CS1_X	P01 P1g	GPIO GPIO	SPI2_MISO SPI3_CS1_X	UARTO_RTS _	GP10 _			
	JP13		2	PWM0	D07	PIN PWM0	46	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM0	_	_	7		
	JP13		3	PWM1	D05	PIN_PWM1	47	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM1	GPIO	_	7		
	JP13		4	SPI2_MOSI	D04	PIN_SPI2_MOSI	29	Digital	1/0		_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MOSI	UARTO_CTS	GPIO	7		
	JP13		5	PWM3	D03	PIN_PWM3	49	Digital			_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPI0	PWM3	I2C1_BDT	-	7		
						_																HIF IRO OUT	ONIO			
	JP13		6	HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	HIF_IRQ_OUT	P02	GPI0	HIF_IRQ_OUT	(Open Drain)	GNSS_1PPS_OUT	7		
	JP13		7	UART_TX	D01	PIN_UART2_TX	67	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPI0	UART2_TX	_	GPIO	7		
	JP13		8	UART_RX	D00	PIN_UART2_RX	68	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RX	_	GPIO	7		
		CN9	1	MAIN_POWER				Power	0	4~5	0	High	0	High	0	High	_	_	_	_	-	_	-			
		CN9	2	GND				Power	_	_	_	High	-	High	-	High	_	_		_	-	_	_			
		CN9	3	SPR_SEN_AIN1	A5	HPADC1	_	Analog	I	~5		_	1	_		Hi-Z	CXD5602GG		_	_	_	_	_	8.9		
		CN9	4	SPR_SEN_AIN0	A4	HPADC0	_	Analog		~5		_		_		Hi-Z	CXD5602GG	_	_	_	-	_	-	8.9		
		CN9	5	3.3V				Power	0	3.3	0	_	0	_	0		_	_	_	_	_	_	_			
		CN9	б	GND		1	-	Power	_	_	_	_	_	_	_		_	_	_	_	_	- HIE IBO OUT	_			
		CN9	7	HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0	5/3.3	-	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	HIF_IRQ_OUT	P02	GPI0	HIF_IRQ_OUT	HIF_IRQ_OUT	GNSS_1PPS_OUT	7		
		CN9	Q	SPI3_SCK	D29	PIN_SPI3_SCK	41	Digital	1/0	5/3.3		Hi-Z	<u> </u>	Hi-Z		Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_SCK	(Open Drain) –	_	7		
		CN9	9	PWM0	D06	PIN_PWM0	41	Digital	1/0	5/3.3		Hi-Z	<u> </u>	Hi-Z		Hi-Z	CXD5602GG CXD5602GG	PWMA	P1k	GPIO GPIO	PWM0	_	_	7		
		CN9	10	SPI3_MISO	D30	PIN_SPI3_MISO	43	Digital			_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG CXD5602GG	SPI3	P1i	GPIO GPIO	SPI3_MISO	_	_	7		
		CN9	11	PWM1	D05	PIN_PWM1	47	Digital	1/0	5/3.3	_	Hi-Z	<u> </u>	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM1	GPIO	_	7		
		CN9	12	SPI3_MOSI	D31	PIN_SPI3_MOSI	42	Digital			_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_MOSI	— — — — — — — — — — — — — — — — — — —	_	7		
		CN9	13	PWM2	D09	PIN_PWM2	48	Digital			_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM2	I2C1_BCK	_	7		
		CN9	14	SPI3_CS0_X	D32	PIN_SPI3_CS0_X	38	Digital	1/0	5/3.3	_	Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	SPI3_CS0_X	P1f	GPIO	SPI3_CS0_X	_	-	7		
		CN9	15	PWM3	D03	PIN_PWM3	49	Digital	1/0	5/3.3		Hi-Z		Hi-Z		Hi-Z	CXD5602GG	PWMB	P1I	GPI0	PWM3	I2C1_BDT	-	7		
		CN9	16	SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0	5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	_	_	7		
		S1	81	SPR_SDIO_CMDDIR	D33	PIN_SDIO_CMDDIR	89	Digital	1/0	1.8	_	Hi-Z		Hi-Z	0	Hi-Z	CXD5602GG	SDIOC	P1t	GPI0	SDIO_CMDDIR	GPIO	GPI0	2.5		

日付	内容
2020.11.13	第一稿
2021.4.14	ACP_GPO1~7のArduinoおよびSDK起動後の初期値を訂正。
2021.4.14	XRST(SPR_RST_X)の各初期値を訂正。
2021.12.20	CXD5247GFに接続されるピンの電圧範囲を3.6-4.4Vに訂正。
2021.12.20	MCLKの電源投入後およびAruduino起動後の初期値を訂正。
2022.9.7	SPR_SPI2_SCKのArduinoIDE上の名称を訂正。
2024.3.21	メインボードのLED0~3とLTE拡張ボードのS1を追加