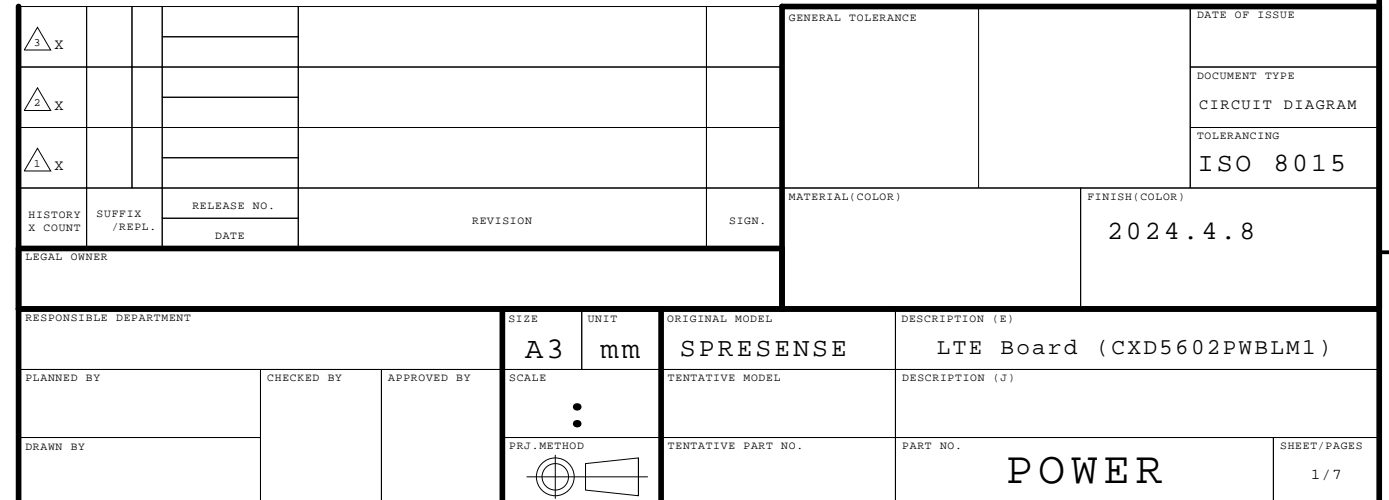
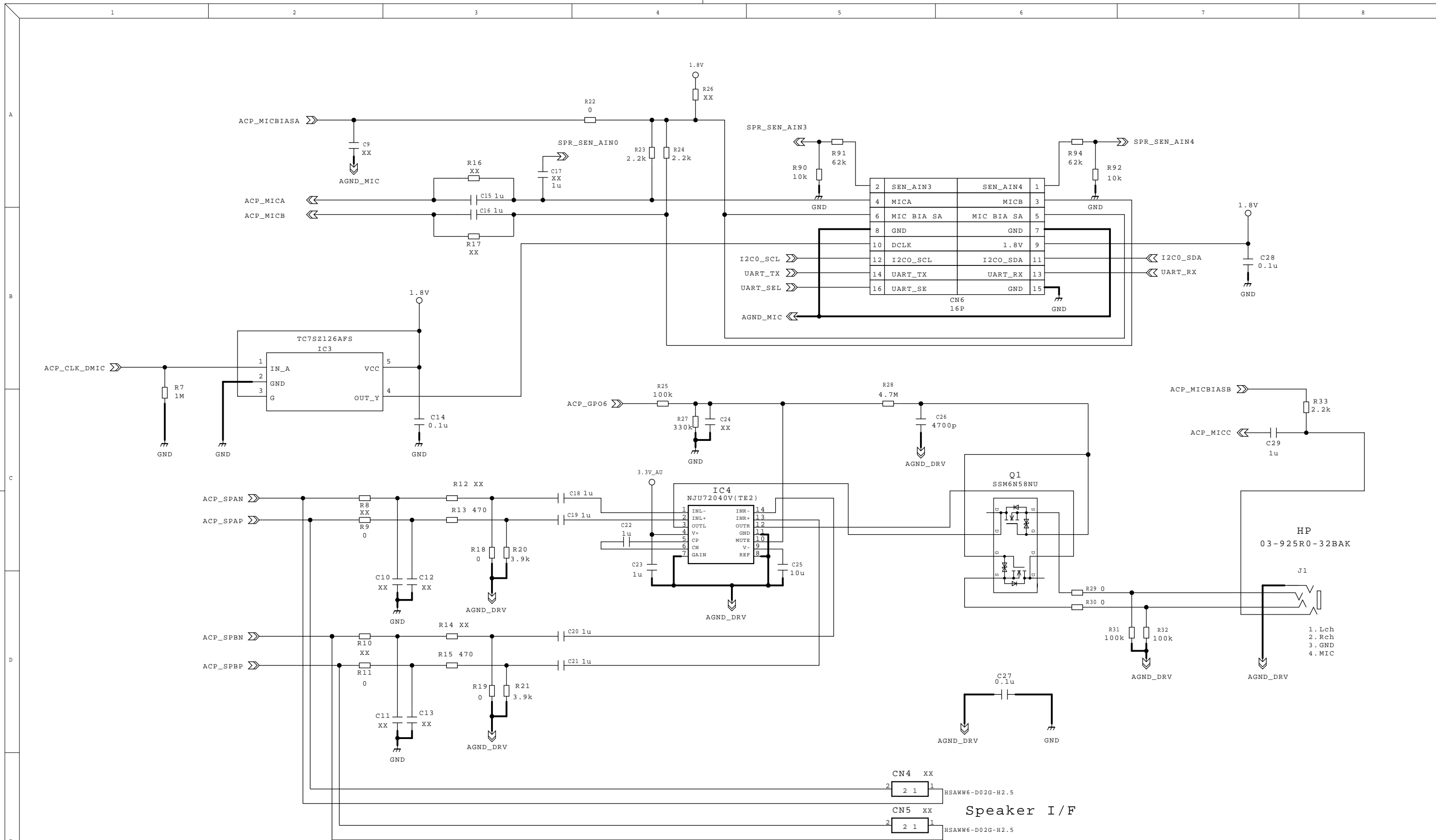


A

I/O Power SW JP



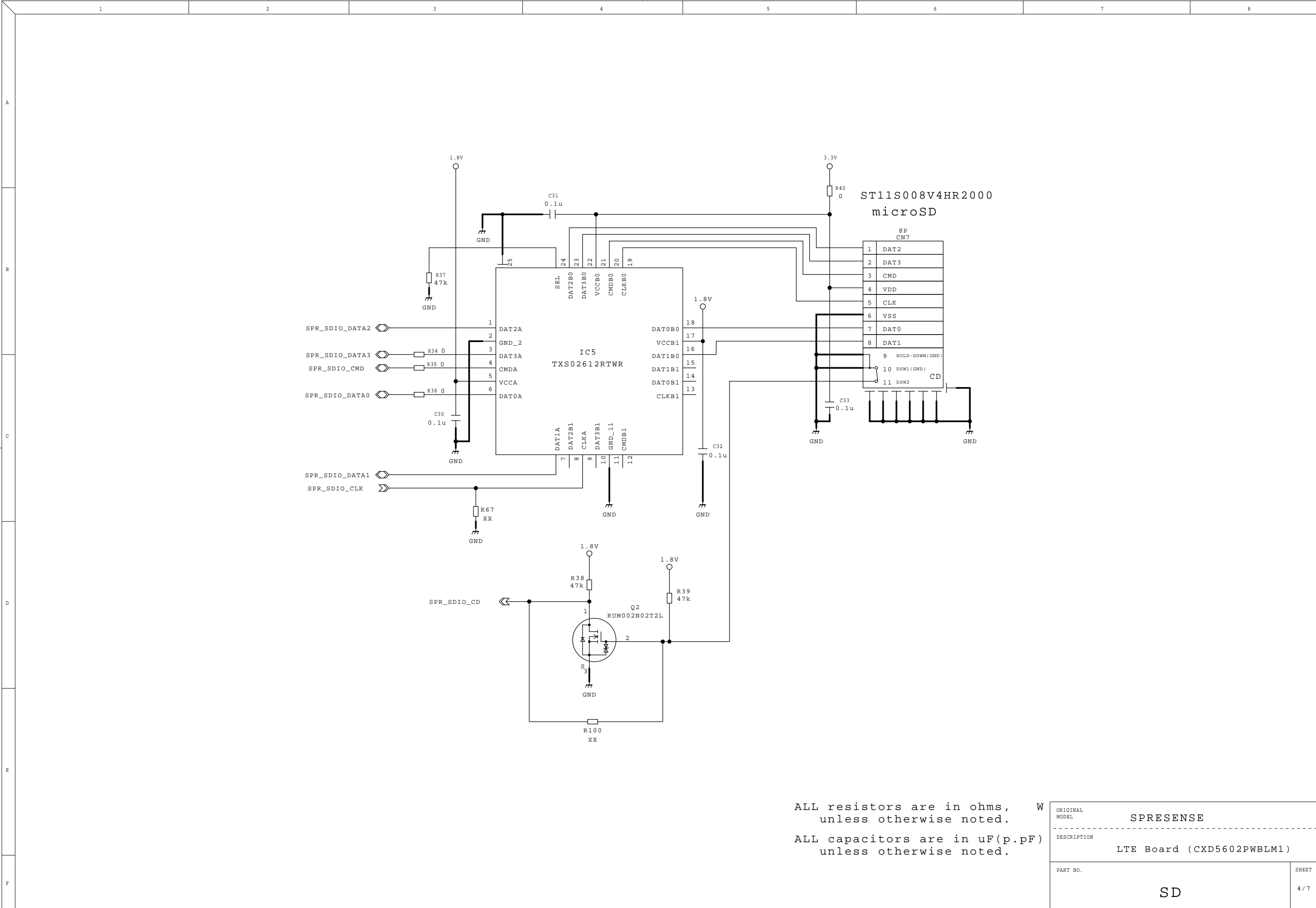
THIS DOCUMENT IS THE PROPRIETARY PROPERTY OF SONY. ITS USE IS AUTHORIZED ONLY FOR RESPONDING TO A REQUEST FOR QUOTATION, OR FOR THE PERFORMANCE OF WORK FOR SONY. ALL QUESTIONS MUST BE REFERRED TO THE LOCAL PURCHASING DEPARTMENT OF SONY. THIS PART SHOULD NOT CONTAIN ANY SUBSTANCES WHICH ARE SPECIFIED IN 88-00259-01. THE REPRODUCTION, DISTRIBUTION AND UTILIZATION OF THIS DOCUMENT AS WELL AS THE COMMUNICATION OF ITS CONTENTS TO OTHERS WITHOUT EXPRESS AUTHORIZATION IS PROHIBITED. OFFENDERS WILL BE HELD LIABLE FOR THE PAYMENT OF DAMAGES. ALL RIGHTS RESERVED IN THE EVENT OF THE GRANT OF A PATENT, UTILITY MODEL OR DESIGN. COPYRIGHT RESERVED.

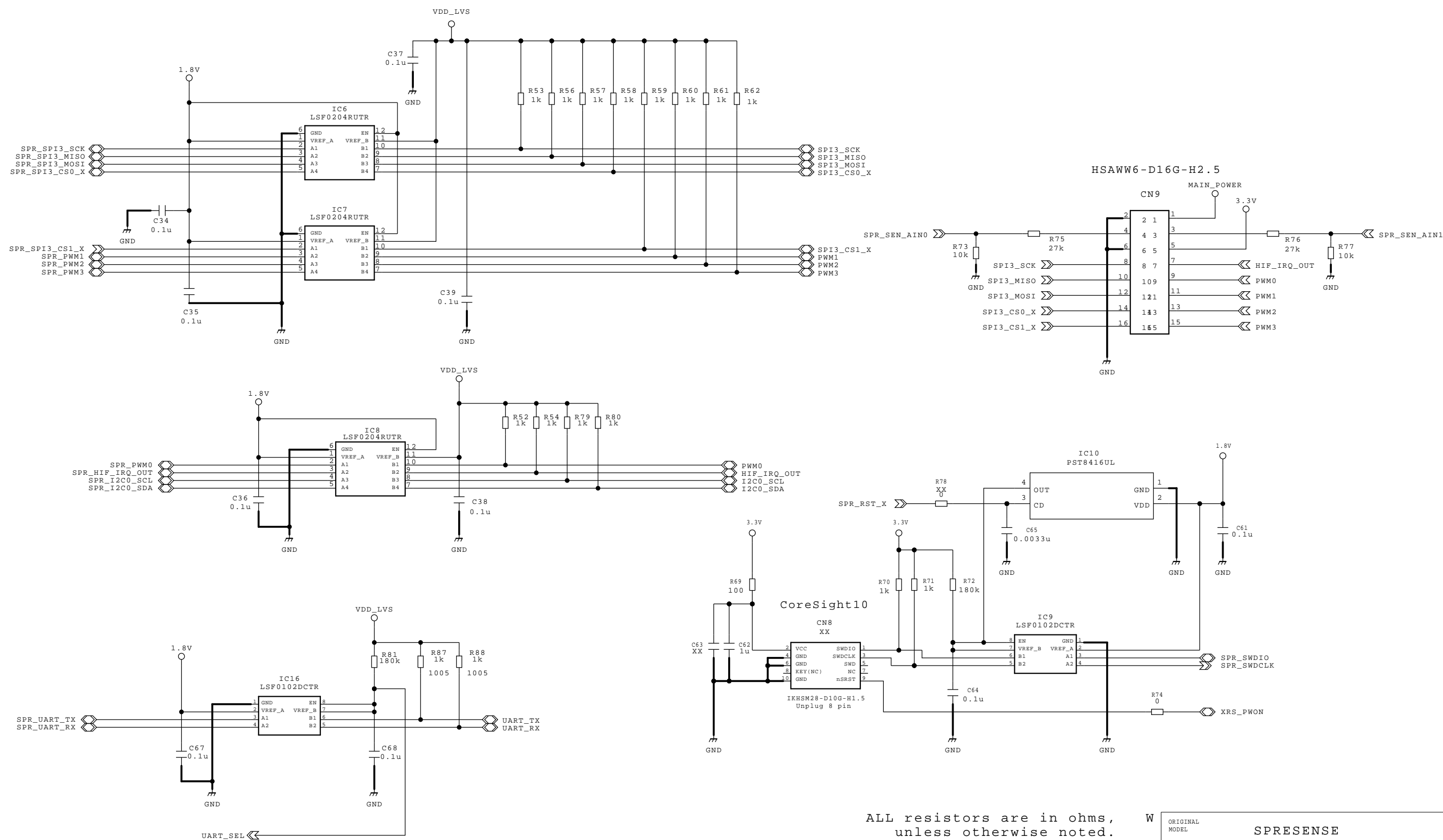


ALL resistors are in ohms, unless otherwise noted.

ALL capacitors are in uF(p.pF) unless otherwise noted.

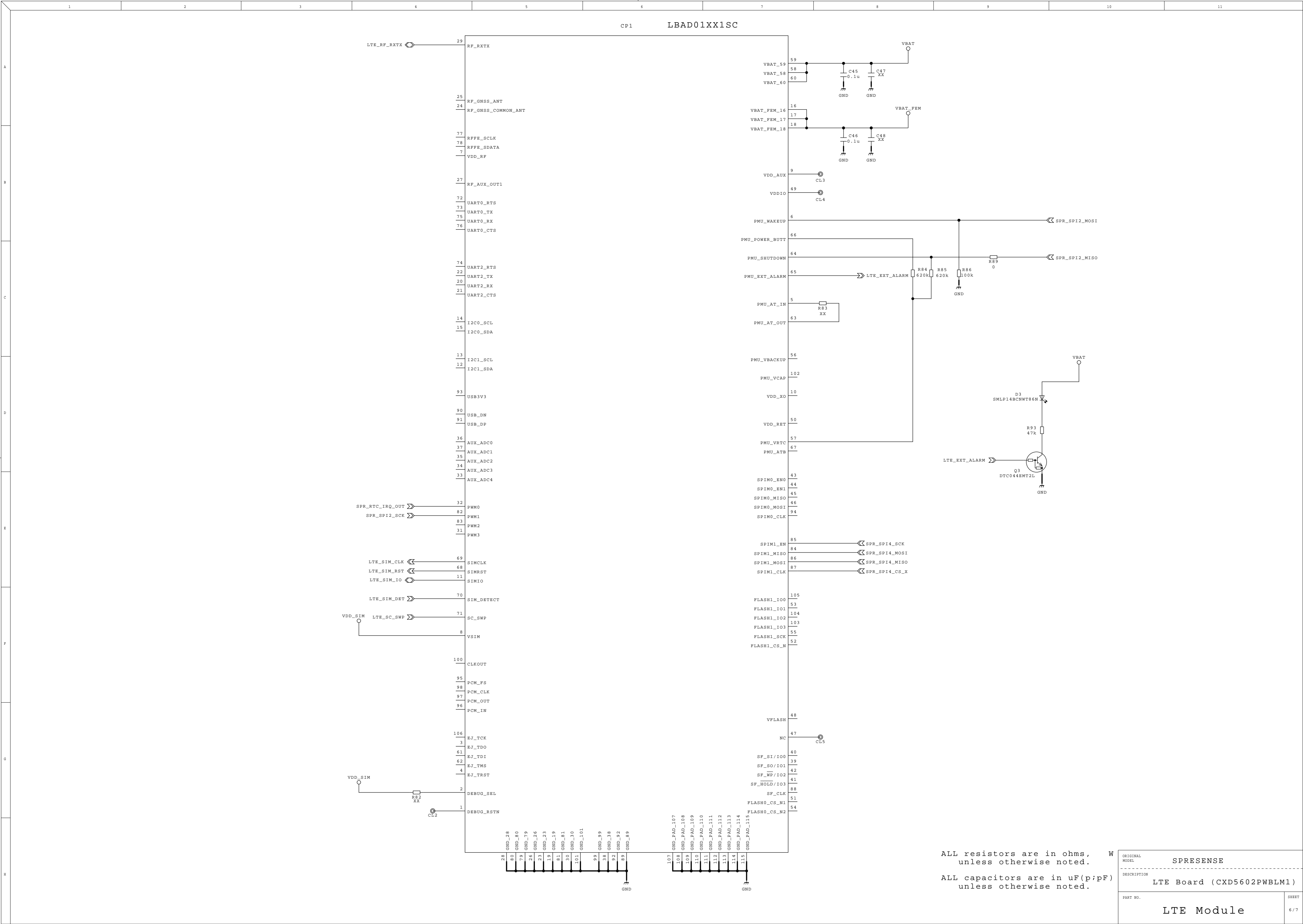
ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	LTE Board (CXD5602PWBLM1)	
PART NO.	AUDIO	
SHEET	3 / 7	

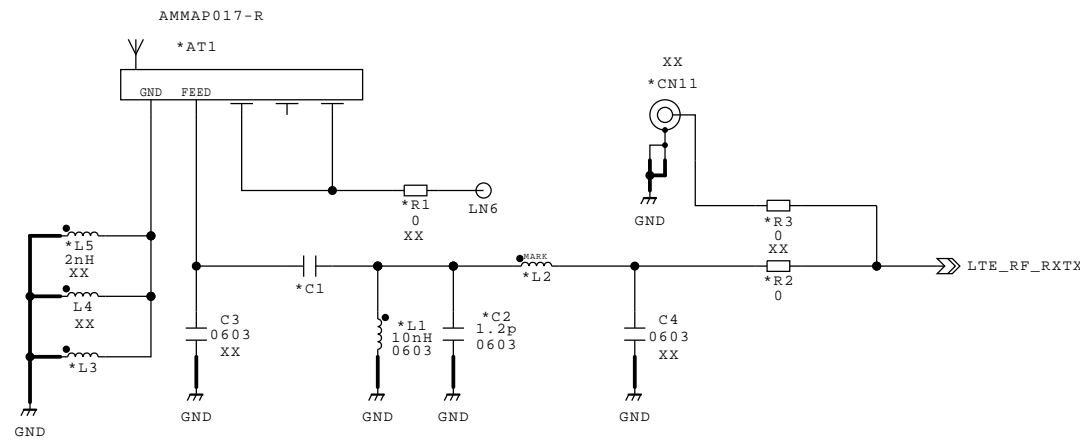




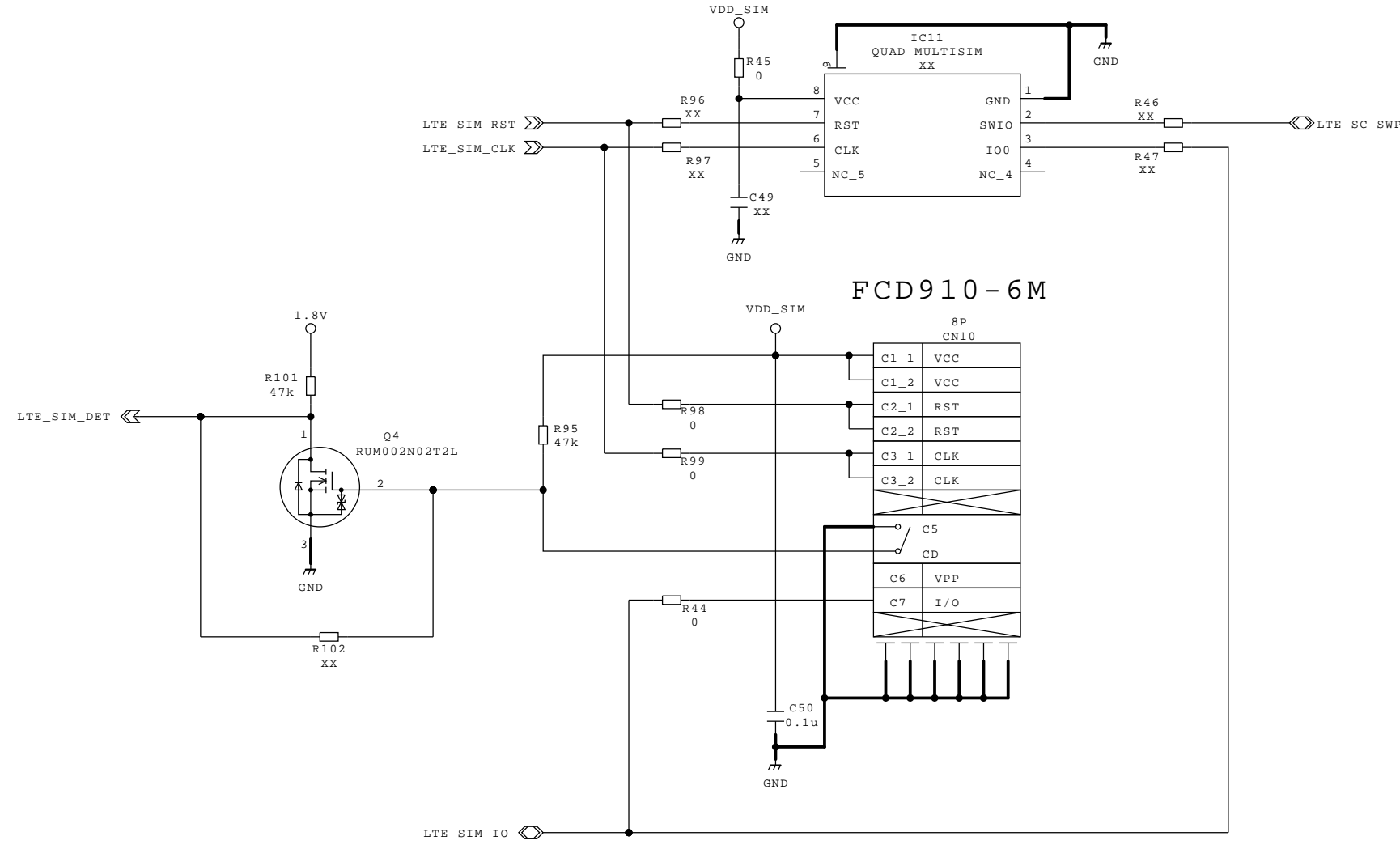
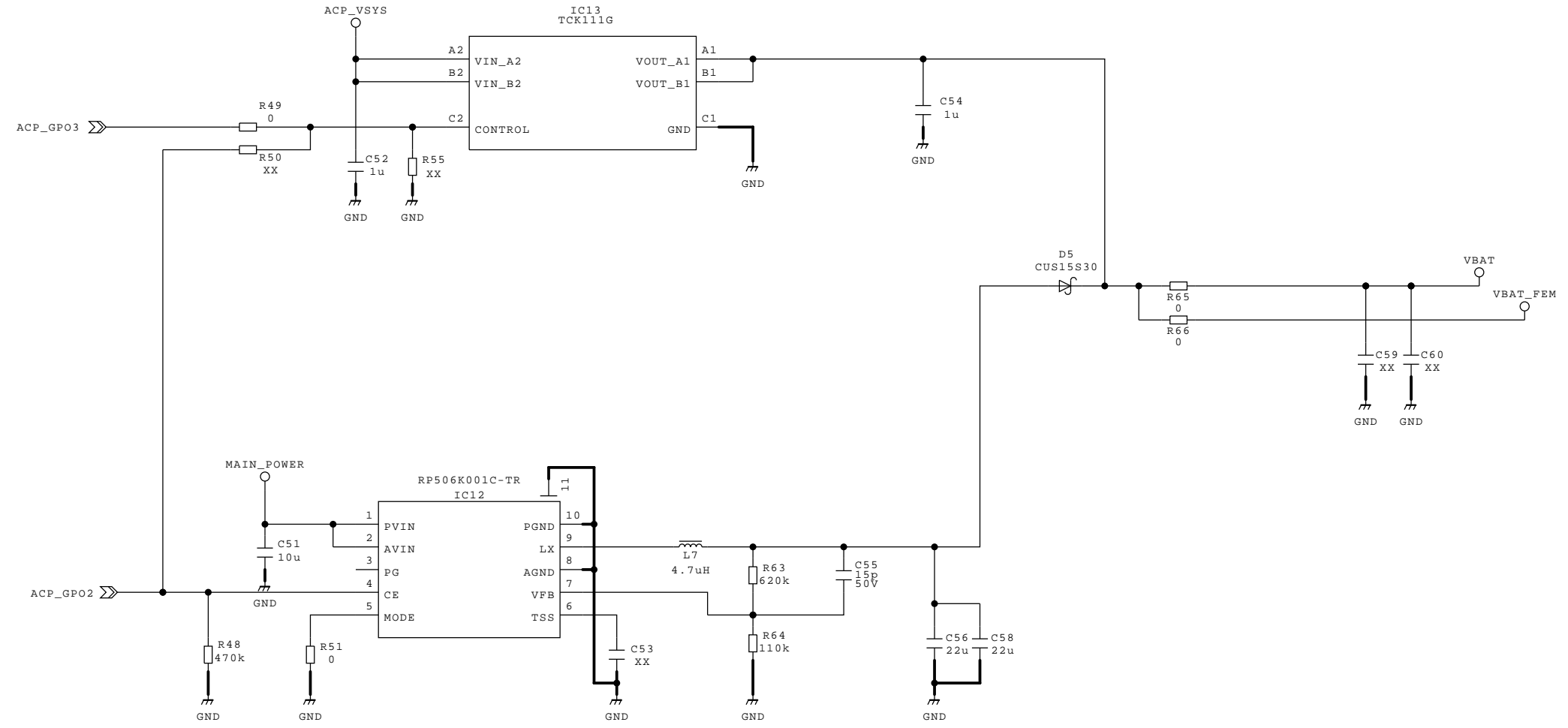
ORIGINAL MODEL	SPRESENSE	

DESCRIPTION	LTE Board (CXD5602PWBLM1)	
PART NO.	LVS / SWD	SHEET 5 / 7





	LM2JFA	LM2BABA
AT1	XX	XX
C1	XX	XX
C2	XX	XX
CN11	U.FL-R-SMT-1	U.FL-R-SMT-1
L1	XX	XX
L2	XX	XX
L3	XX	XX
L5	XX	XX
R1	XX	XX
R2	XX	XX
R3	0	0



ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p;pF)
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE
DESCRIPTION	LTE Board (CXD5602PWBLM1)
PART NO.	LTE Others
SHEET	7 / 7