To	Direction	Location	I/O Bank	VREF Group	Fitter Location
ADC_clk	Output	PIN_240	8	B8_N0	PIN_240
ADC_db[7]	Input	PIN_230	8	B8_N0	PIN_230
ADC_db[6]	Input	PIN_233	8	B8_N0	PIN_233
ADC_db[5]	Input	PIN_234	8	B8_N0	PIN_234
ADC_db[4]	Input	PIN_235	8	B8_N0	PIN_235
ADC_db[3]	Input	PIN_236	8	B8_N0	PIN_236
ADC_db[2]	Input	PIN_237	8	B8_N0	PIN_237
ADC_db[1]	Input	PIN_238	8	B8_N0	PIN_238
ADC_db[0]	Input	PIN_239	8	B8_N0	PIN_239
altera_reserved_tck	Input				PIN_27
altera_reserved_tdi	Input				PIN_26
altera_reserved_tdo	Output				PIN_29
altera_reserved_tms	Input				PIN_28
Clock	Input	PIN_31	1	B1_N0	PIN_31
LCD_dclk	Output	PIN_167	6	B6_N0	PIN_167
LCD_DE	Output	PIN_171	6	B6_N0	PIN_171
LCD_hsync	Output	PIN_168	6	B6_N0	PIN_168
LCD_vsync	Output	PIN_169	6	B6_N0	PIN_169
PFO	Input	PIN_164	6	B6_N0	PIN_164
RAMROM_ab[18]	Output	PIN_39	2	B2_N0	PIN_39
RAMROM_ab[17]	Output	PIN_37	2	B2_N0	PIN_37
RAMROM_ab[16]	Output	PIN_38	2	B2_N0	PIN_38
RAMROM_ab[15]	Output	PIN_44	2	B2_N0	PIN_44
RAMROM_ab[14]	Output	PIN_41	2	B2_N0	PIN_41
RAMROM_ab[13]	Output	PIN_49	2	B2_N0	PIN_49
RAMROM_ab[12]	Output	PIN_43	2	B2_N0	PIN_43
RAMROM_ab[11]	Output	PIN_52	2	B2_N0	PIN_52
RAMROM_ab[10]	Output	PIN_55	2	B2_N0	PIN_55
RAMROM_ab[9]	Output	PIN_51	2	B2_N0	PIN_51
RAMROM_ab[8]	Output	PIN_50	2	B2_N0	PIN_50
RAMROM_ab[7]	Output	PIN_45	2	B2_N0	PIN_45
RAMROM_ab[6]	Output	PIN_46	2	B2_N0	PIN_46
RAMROM_ab[5]	Output	PIN_6	1	B1_N0	PIN_6
RAMROM_ab[4]	Output	PIN_9	1	B1_N0	PIN_9
RAMROM_ab[3]	Output	PIN_13	1	B1_N0	PIN_13
RAMROM_ab[2]	Output	PIN_18	1	B1_N0	PIN_18
RAMROM_ab[1]	Output	PIN_21	1	B1_N0	PIN_21
RAMROM_ab[0]	Output	PIN_22	1	B1_N0	PIN_22
RAMROM_db[7]	Bidir	PIN_69	3	B3_N0	PIN_69
RAMROM_db[6]	Bidir	PIN_70	3	B3_N0	PIN_70
RAMROM_db[5]	Bidir	PIN_71	3	B3_N0	PIN_71
RAMROM_db[4]	Bidir	PIN_72	3	B3_N0	PIN_72
RAMROM_db[3]	Bidir	PIN_73	3	B3_N0	PIN_73

RAMROM_db[2]	Bidir	PIN_68	3	B3_N0	PIN_68
RAMROM_db[1]	Bidir	PIN_65	3	B3_N0	PIN_65
RAMROM_db[0]	Bidir	PIN_64	3	B3_N0	PIN_64
Reset	Input	PIN_187	7	B7_N0	PIN_187
ROM_CS	Output	PIN_5	1	B1_N0	PIN_5
ROM_OE	Output	PIN_4	1	B1_N0	PIN_4
romram_db_dir	Output	PIN_76	3	B3_N0	PIN_76
rotary_1_A	Input	PIN_181	7	B7_N0	PIN_181
rotary_1_B	Input	PIN_182	7	B7_N0	PIN_182
rotary_1_P	Input	PIN_183	7	B7_N0	PIN_183
rotary_2_A	Input	PIN_184	7	B7_N0	PIN_184
rotary_2_B	Input	PIN_185	7	B7_N0	PIN_185
rotary_2_P	Input	PIN_186	7	B7_N0	PIN_186
SRAM_CS	Output	PIN_63	3	B3_N0	PIN_63
SRAM_OE	Output	PIN_57	2	B2_N0	PIN_57
SRAM_WE	Output	PIN_56	2	B2_N0	PIN_56
VRAM_ab[8]	Output	PIN_132	5	B5_N0	PIN_132
VRAM_ab[7]	Output	PIN_126	5	B5_N0	PIN_126
VRAM_ab[6]	Output	PIN_131	5	B5_N0	PIN_131
VRAM_ab[5]	Output	PIN_128	5	B5_N0	PIN_128
VRAM_ab[4]	Output	PIN_127	5	B5_N0	PIN_127
VRAM_ab[3]	Output	PIN_120	4	B4_N0	PIN_120
VRAM_ab[2]	Output	PIN_119	4	B4_N0	PIN_119
VRAM_ab[1]	Output	PIN_118	4	B4_N0	PIN_118
VRAM_ab[0]	Output	PIN_133	5	B5_N0	PIN_133
VRAM_ab_dir	Output	PIN_117	4	B4_N0	PIN_117
VRAM_CAS	Output	PIN_134	5	B5_N0	PIN_134
VRAM_control_dir	Output	PIN_145	5	B5_N0	PIN_145
VRAM_db[7]	Bidir	PIN_80	3	B3_N0	PIN_80
VRAM_db[6]	Bidir	PIN_78	3	B3_N0	PIN_78
VRAM_db[5]	Bidir	PIN_82	3	B3_N0	PIN_82
VRAM_db[4]	Bidir	PIN_81	3	B3_N0	PIN_81
VRAM_db[3]	Bidir	PIN_84	3	B3_N0	PIN_84
VRAM_db[2]	Bidir	PIN_83	3	B3_N0	PIN_83
VRAM_db[1]	Bidir	PIN_88	3	B3_N0	PIN_88
VRAM_db[0]	Bidir	PIN_87	3	B3_N0	PIN_87
VRAM_db_dir	Output	PIN_93	4	B4_N0	PIN_93
VRAM_DSF	Output	PIN_173	6	B6_N0	PIN_173
VRAM_OE	Output	PIN_139	5	B5_N0	PIN_139
VRAM_RAS	Output	PIN_135	5	B5_N0	PIN_135
VRAM_SC	Output	PIN_142	5	B5_N0	PIN_142
VRAM_SE	Output	PIN_143	5	B5_N0	PIN_143
VRAM_WE	Output	PIN_137	5	B5_N0	PIN_137
WDI	Output	PIN_166	6	B6_N0	PIN_166
WDO	Input	PIN_161	6	B6_N0	PIN_161