

Sophia Noviel O. Silvestre

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PROFESSIONAL PROFILE

Aspiring Analyst Infrastructure Service provider with skills in network design, microchip validation, and data analysis. Proficient in network architecture, protocols, routing, and switching. Highly interested in infrastructure analysis and eager to develop expertise in network security. Committed to a long-term career in IT, I am motivated to learn, grow, and apply my knowledge to provide exceptional technical support, service delivery, and contribute to infrastructure projects.

CORE COMPETENCIES

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|-----------------------------------|------------------------------|----------------------------|
| • Python/C++/AWK | • Collaboration and Teamwork | • RF Analysis |
| • Network Infrastructure Planning | • Project Management | • Troubleshooting Software |
| • Data Analysis | • Microsoft 365 | • AWS |

RELEVANT EXPERIENCE

Test Engineer Intern, Microchip Technology Inc., Alabang, August 2023 – October 2023
Muntinlupa, Philippines.

- Improved manufacturability by ~15% by debugging, validating, and supporting ATE test program development for microchips.
- Developed 50+ randomized commands with AWK to enhance security and ensure robust testing scenarios.
- Created scripts for testing scenarios, enhancing tool efficiency for production databases
- Collaborated with cross-functional teams to resolve product issues during quality assurance
- Achieved 30% faster test cycle by enhancing automated testing scripts and monitoring systems.
- Managed 4 projects simultaneously.
- Identified and implemented debugging solutions to drive engineering improvements.

EDUCATION

Bachelor of Science in Electronics Engineering October 2020 – September 2024
Polytechnic University of the Philippines

PROJECTS

- **Proposed Local Area Network (LAN) Design.** Designed a robust LAN infrastructure for the IT Department of Municipality of Calamba City, Laguna, demonstrating knowledge in network architecture, efficient subnetting, and communication skills essential for collaborating with municipal stakeholders.
- **Design and Optimization of Yagi-Uda Antenna.** Demonstrated strong problem-solving and hands-on technical skills by designing and building a high-performance Yagi-Uda antenna. Utilized troubleshooting techniques and data analysis to optimize signal reception, achieving over 61 analog and digital channels. Temperature Sensing Fan with Motion Sensor.
- **Microwave Link Design.** Engineered a reliable 20-kilometer microwave link between Talisay, Batangas, and Calamba, Laguna, effectively supporting data transmission requirements and optimizing communication infrastructure in the region.
- **Line-of-Sight (LOS) Transmission Feasibility Study.** Conducted a comprehensive LOS feasibility analysis in Davao, planning and designing transmission towers to support long-distance communication with minimal signal obstruction, highlighting technical skills in radio frequency (RF) planning and strategic infrastructure development.

EDUCATION

Academic Distinction (Cum Laude Standing) GPA: 1.12 September 2024
Polytechnic University of the Philippines

Academic Achiever (Overall Rank 1 of BSECE Graduating Class) September 2024
Polytechnic University of the Philippines

Outstanding On-the-Job Trainee September 2024
Polytechnic University of the Philippines

Author and Presenter September 2024
International Conference on Informatics and Computational Sciences

DOST-SEI JLSS Merit Scholar October 2022 – September 2024
Outstanding Service Awardee October 2022
Polytechnic University of the Philippines