

# Sina Kashipazha

## PERSONAL DATA

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Address: Network Lab, School of Electrical and Computer Engineering, University College of Engineering, University of Tehran, North Kargar st., Tehran, Iran.

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## EDUCATION

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**B.Sc In Computer Engineering with Concentration in Software**  
University of Tehran

**Tehran, Iran**  
2012 - 2017

## RESEARCH INTERESTS

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- Flow distribution between multiple controller in SDN networks
- File placement in distributed file systems using online algorithms
- Task scheduling policies in Cluster Computing
- Load balance application layer traffic in Openstack

## RESEARCH EXPERIENCE

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- **University of Tehran**
  - **Computer Network Laboratory** Feb 2015 - Feb 2018  
Advisor: **Prof. Ahmad Khonsari**
  - **Thesis:**

## UNIVERSITY OF TEHRAN TEACHING EXPERIENCE

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- **Computer Network Lab (Fall)** 2018-2019
- **Computer Network Lab (Fall, Spring)** 2017-2018
- **Computer Network Lab (Spring)** 2016-2017

## UNIVERSITY OF TEHRAN TEACHING ASSISTANT EXPERIENCE

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- **Advanced Computer network (graduate) (Fall) Computer Network (Fall)** 2018-2019
- **Operating System (Fall, Spring), Operating System Lab (Fall, Spring), Computer Network (Fall, Spring)** 2017-2018
- **Advanced Computer network (graduate) (Fall) Operating System (Spring), Operating System Lab (Spring), Computer Network (Fall, Spring)** 2016-2017
- **Operating System (Fall, Spring), Operating System Lab (Fall, Spring), Design and Implementation of Compiler (Fall), Computer Network (Spring)** 2015-2016
- **Operating System (Spring), Operating System Lab (Spring), Formal Languages and Automata (Fall)** 2014-2015

## PROFESSIONAL EXPERIENCE

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- **University of Tehran**

- **Antenna Type Approval Laboratory**

Jun 2015 - Jul 2017

**Technical Staff,** RF, Microwave and MM-Wave Characterization

- Hands on Experience in using RF, Microwave and MM-Wave measurement equipments
- Familiar with general requirements of testing and calibration lab standard (ISO 17025)

- **Photonics Research Lab**

May-Oct 2016

**Research Staff,** RF and Microwave Circuits and System Design for Cellular Communications Systems

- Feasibility study of, design a LTE macro, micro stand alone Base Station RF Front-End and wrote a proposal worths approximately \$ 300K
- Designed, implemented and measured a high isolation (up to 44dB) power combiner for GSM BTS

- **Science and Technological Park, Ragan Parto Pars CO.**

Jun - Dec 2015

**RF System Engineer**

- Designed and implemented a high frequency, and Digital Printed circuits Board
- Designed and implemented a educational antenna kit for antenna lab course
- Designed and implemented high power waveguide filter

## TECHNICALS SKILLS

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- **Laboratory skills**

- Familiar with on wafer characterization using cascadeprobe station
- Free space optical alignment
- Opto-mechanical structure
- Experienced working in anechoic chamber and antenna pattern measurement

- **Biology**

- Familiar with Biological Cell Culture

- **Computational Electromagnetic Software**

- COMSOL Multiphysics, Ansoft HFSS, Keysight ADS

- **Scientific Programming, Hardware Discription**

- MATLAB, Microsoft Visual Studio C++, Fortran, VHDL, Verilog

- **RF and Digital Circuit Design**

- Altium Design, Eagle PCB