**Trasarea execuției programului de test pentru MIPS32**

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Pas** | **SW(7:5)** | "000" | "001" | "010" | "011" | "100" | "101" | "110" | "111" | **De completat numai pentru instrucțiuni de salt** | |
| **Instr** | **Instr** (*hexa*) | **PC+4** | **RD1** | **RD2** | **Ext\_Imm** | **ALURes** | **MemData** | **WD** | **BranchAddr** | **JumpAddr** |
| 0 | lui | X"3C011001" | X"00000004" | X"00000000" | X"00000000" | X"00001001" | X"00000000" | x | X"00000000" | x | x |
| 1 | lui | X"3C011001" | X"00000004" | X"00010000" | X"00000000" | X"00001001" | X"00000000" | x | X"00000000" | x | x |
| 2 | lw | X"8C290018" | X"00000008" | X"00000000" | X"00000000" | X"00000001" | X"00000000" | x | X"00000000" | x | x |
| 3 | sll | X"00095880" | X"0000000C" | X"00000000" | X"00000005" | X"00000002" | X"00000002" | x | X"00000002" | x | x |
| 4 | Addi | X"2129FFFF" | X"00000010" | X"00000005" | X"00000005" | X"FFFFFFFF" | X"FFFFFFF5" | x | X"FFFFFFF5" | x | x |
| 5 | Addiu | X"240D0001" | X"00000014" | X"00000000" | X"00000000" | X"00000000" | X"00000000" | x | X"00000000" | x | x |
| 6 | Beq | X"11200010" | X"00000018" | X"00000000" | X"00000000" | X"00000010" | X"00000010" | x | X"00000010" | X"00000058" | x |
| 7 | Addiu | X"240D0000" | X"0000001C" | X"00000001" | X"00000000" | X"00000000" | X"00000000" | x | X"00000000" | x | x |
| 8 | lui | X"3C011001" | X"0000020" | X"00010000" | X"00000000" | X"00001001" | X"00100001" | x | X"00100001" | x | x |
| 9 | Ori | X"342a0000" | X"00000024" | X"00010000" | X"00000000" | X"00000000" | X"00010000" | x | X"00010000" | x | x |
| 10 | Addiu | X"24090000" | X"00000028" | X"00010000" | X"00000004" | X"00000000" | X"00010000" | x | X"00010000" | x | x |
| 11 | lw | X"8D4C0000" | X"0000002C" | X"00000002" | X"00000000" | X"00000000" | X"00000002" | x | X"00000002" | x | x |
| 12 | lw | X"8D4D0004" | X"00000030" | X"00000007" | X"00000000" | X"00000004" | X"0000000B" | x | X"0000000B" | x | x |
| 13 | addi | X"214A0004" | X"00000034" | X"00000004" | X"00000000" | X"00000000" | X"00000004" | x | X"00000004" | x | x |
| 14 | beq | X"1140001A" | X"00000038" | X"00000004" | X"00000000" | X"00000000" | X"00000004" | x | X"00000004" | X"0000001A" | x |
| 15 | j | X"08100007" | X"0000003C" | x | x | x | x | x | x | x | X"00000007" |
| 16 | (salt realizat, pas 6) Beq | X"11200010" | X"00000018" | X"00000004" | X"00000000" | X"00000010" | X"00000014" | x | X"00000014" | x | X"00000064" |
| 17 | (branch, linia 25) lui | X"3C011001" | X"00000064" | X"00000004" | X"00000000" | X"00000001" | X"00000005" | x | X"00000005" | x | X"00000019" |
| 18 | Ori | X"342a0000" | X"00000068" | X"00010004" | X"00000000" | X"00000000" | X"00010004" | x | X"00010004" | x | x |
| 19 | Addiu | X"24090000" | X"0000006C" | X"00010000" | X"00000000" | X"00000001" | X"00010001" | x | X"00010001" | x | x |
| 20 | lw | X"8D440000" | X"00000070" | X"00000002" | X"00010000" | X"00000000" | X"00000002" | x | X"00000002" | x | x |
| 21 | beq | X"10800002" | X"00000074" | X"00000004" | X"00000000" | X"00000000" | X"00000004" | x | X"00000004" | X"00000002" | x |
| 22 | addi | X"214A0004" | X"00000078" | X"00000004" | X"00000000" | X"00000000" | X"00000004" | x | X"00000004" | x | x |
| 24 | j | X"0810001a" | X"0000007C" | x | x | x | x | x | x | x | X"0000001A" |
| 25 | (salt realizat, pas 19) addiu | X"24090000" | X"0000006C" | X"00010000" | X"00000000" | X"00000001" | X"00010001" | x | X"00010001" | x | x |
|  | Se repeta bucla si apoi se termina programul |  |  |  |  |  |  |  |  |  |  |

URL: <https://drive.google.com/file/d/1OgoST1-tEe1cbUdNk_VKr6NHq3zVfs83/view?usp=sharing>