



Settings SW1.1 - SW1.5: FDC IRQ Selection SW1.1: ON: IRQ3 (COM2, COM4) SW1.2: ON: IRQ4 (COM1, COM3) SW1.3: ON: IRQ5 SW1.4: ON: IRQ6 (Default FDC) SW1.5 - SW1.6: FDC Address Selection SW1.5: ON: 0x3F0 (Primary) SW1.6: ON: 0x370 (Secondary) SW1.7-SW1.8: DMA Selection SW1.7: ON: DMA2 SW1.8: ON: DMA3 SW2.1 - ON: Enable ROM SW2.1 - ON: Enable ROM SW2.2 - ON: Enable EEPROM write SW2.3 - ON: ROM A17 = 0; OFF: ROM A17 = 1 SW2.4 - ON: ROM A16 = 0; OFF: ROM A16 = 1 SW2.5 - ON: ROM A15 = 0; OFF: ROM A15 = 1 SW2.6 - ON: ROM A14 = 0; OFF: ROM A14 = 1 SW2.7 - ON: ROM A13 = 0; OFF: ROM A13 = 1 SW2.8 - NOT USED

File: isa_fdc.kicad_DB37-v3.1.2_pcb.sch Title: Sergey's XT (C) 2010 Sergey Kiselev, GNU Free Documentation License Size: User Date: 2020-08-03 KiCad E.D.A. eeschema (5.1.6-0) Rev: Additions/Changes/Upda

DRATEO INDEX ME2 DS3 DS2 ME3 DIR STEP WDATA WE TRKOO