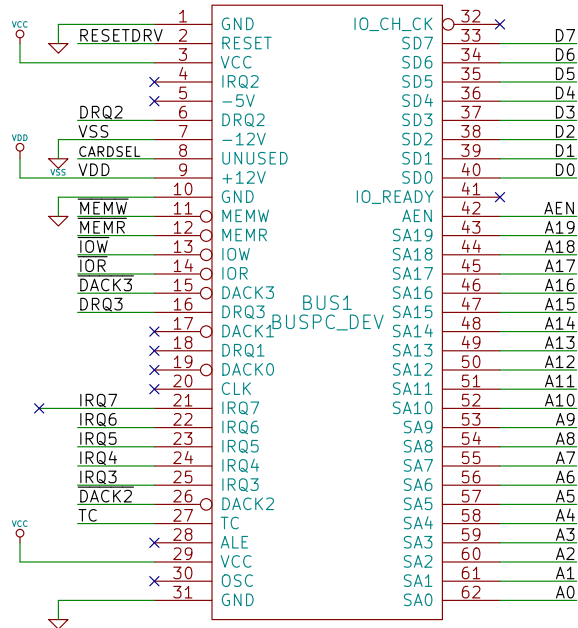
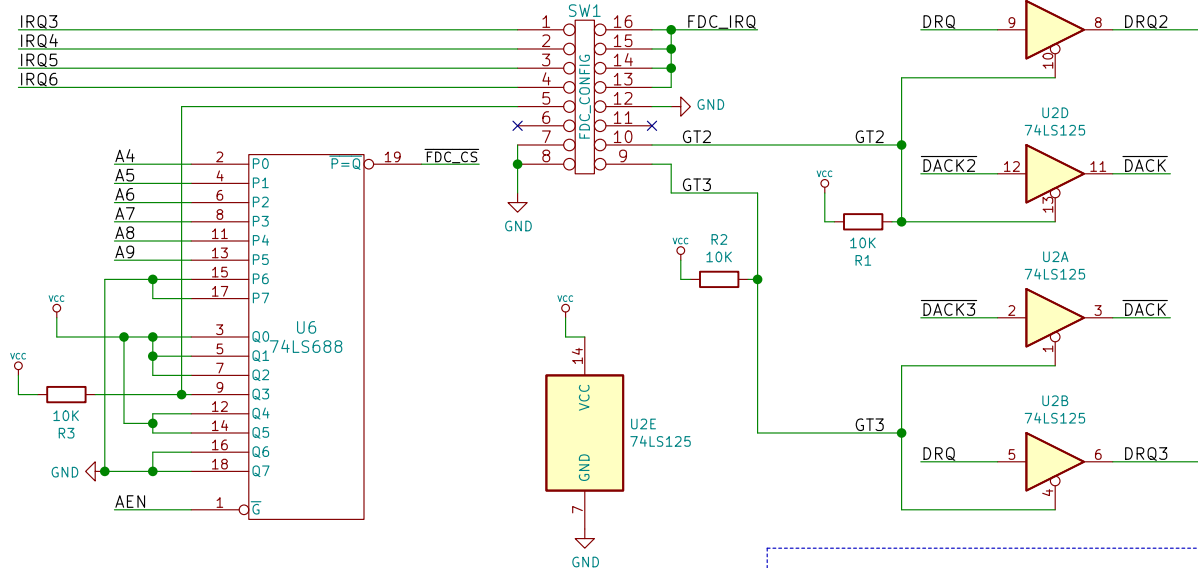


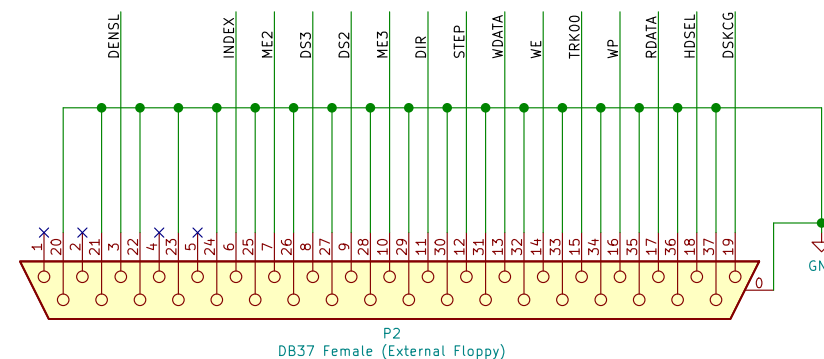
ISA Bus



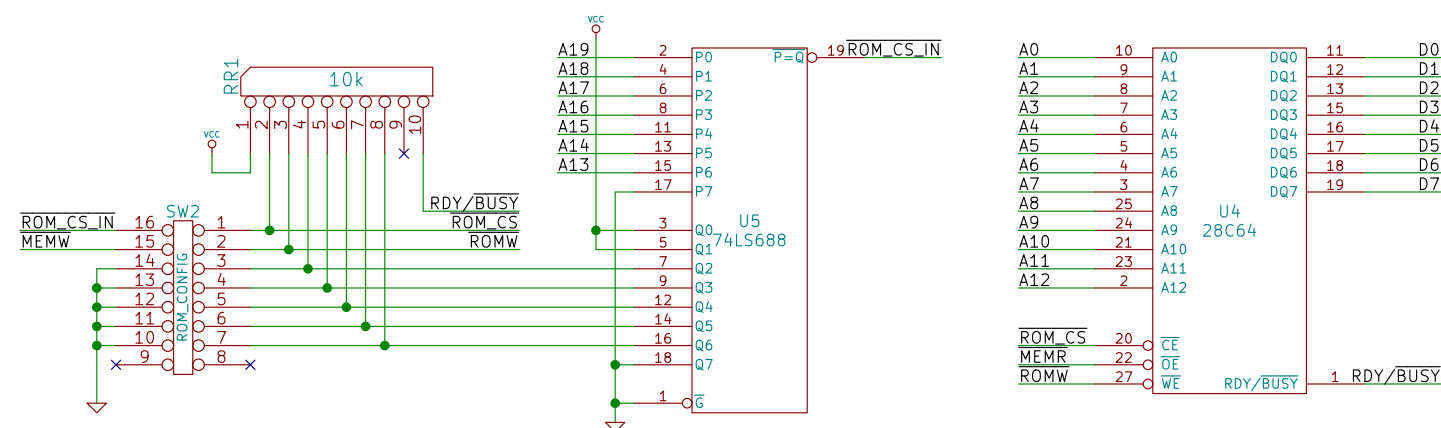
I/O Port/IRQ/DMA Chip Select Logic



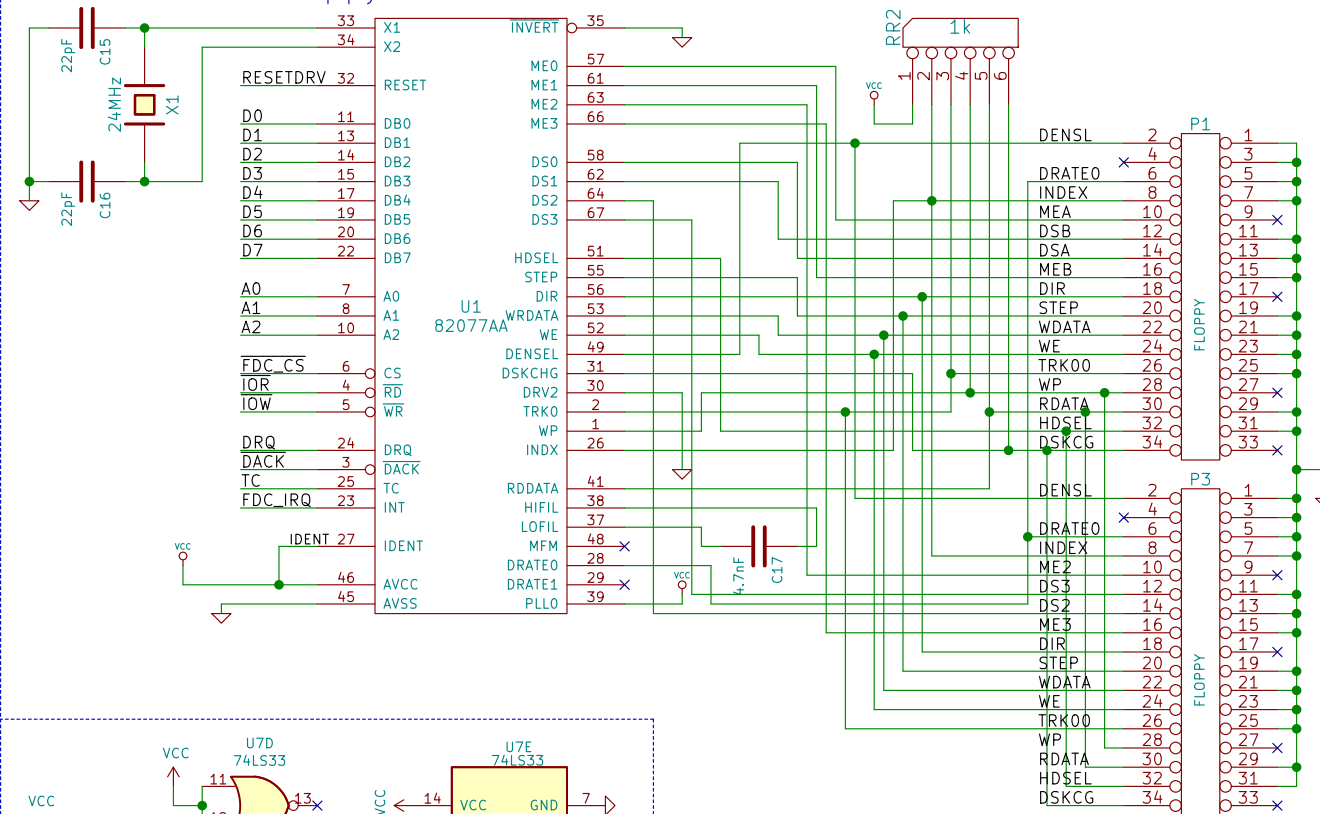
External Floppy Connector



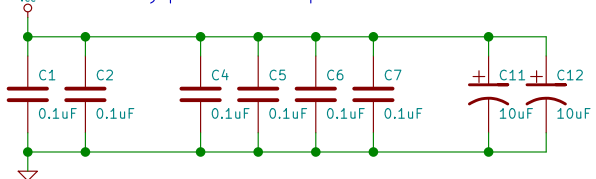
BIOS Extension ROM



Floppy Disk Controller



Bypass Capacitors



Settings

SW1.1 – SW1.5: FDC IRQ Selection
SW1.1: ON: IRQ3 (COM2, COM4)
SW1.2: ON: IRQ4 (COM1, COM3)
SW1.3: ON: IRQ5
SW1.4: ON: IRQ6 (Default FDC)

FDC Address Selection
SW1.5: OFF: 0x3F0 (Primary)
SW1.5: ON: 0x370 (Secondary)

SW1.6 – NOT USED

SW1.7–SW1.8: DMA Selection
SW1.7: ON: DMA2
SW1.8: ON: DMA3

SW2.1 – ON: Enable ROM
SW2.2 – ON: Enable EEPROM write
SW2.3 – ON: ROM A17 = 0; OFF: ROM A17 = 1
SW2.4 – ON: ROM A16 = 0; OFF: ROM A16 = 1
SW2.5 – ON: ROM A15 = 0; OFF: ROM A15 = 1
SW2.6 – ON: ROM A14 = 0; OFF: ROM A14 = 1
SW2.7 – ON: ROM A13 = 0; OFF: ROM A13 = 1
SW2.8 – NOT USED

Switch Mapping

11111 – 0xC0000	10001 – 0xDC000
11110 – 0xC2000	10000 – 0xDE000
11101 – 0xC4000	01111 – 0xE0000
11100 – 0xC6000	01110 – 0xE2000
11011 – 0xC8000	01101 – 0xE4000
11010 – 0xCA000	01100 – 0xE6000
11001 – 0xCC000	11011 – 0xE8000
11000 – 0xCE000	11010 – 0xEA000
10111 – 0xD0000	11001 – 0xEC000
10110 – 0xD2000	11000 – 0xEE000
10101 – 0xD4000	00111 – 0xF0000
10100 – 0xD6000	00110 – 0xF2000
10011 – 0xD8000	00101 – 0xF4000
10010 – 0xDA000	

