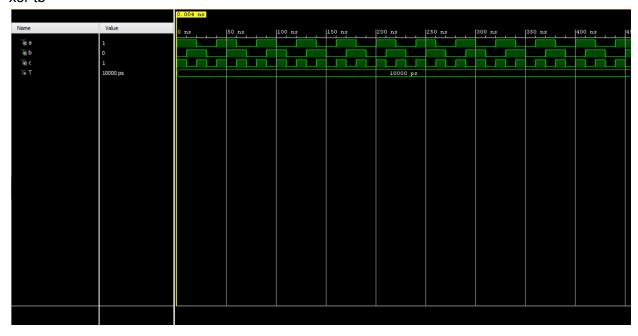
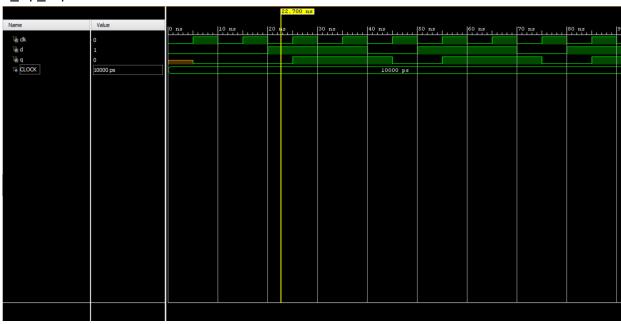
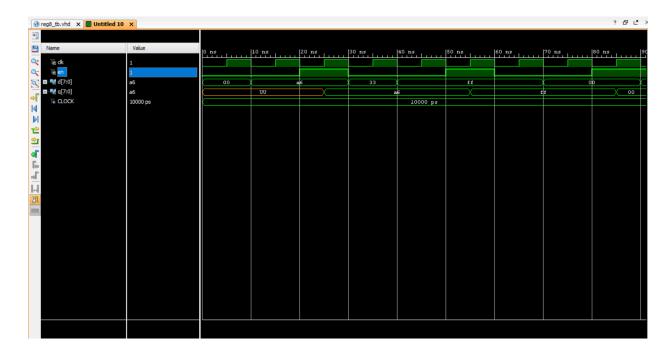
xor tb



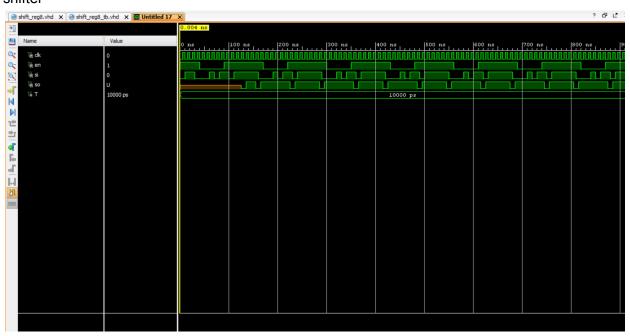
D_flip_flop



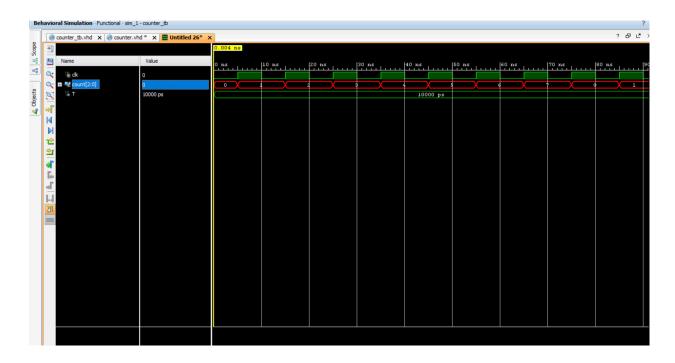
reg8



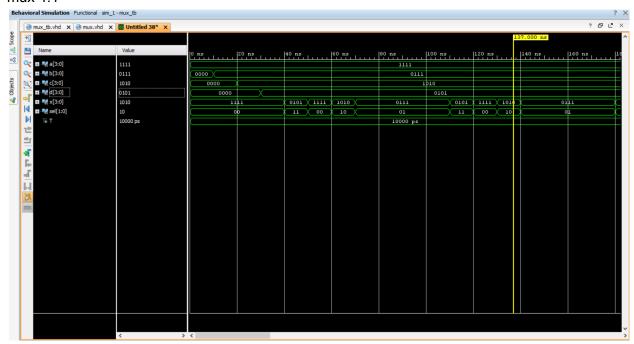
shifter



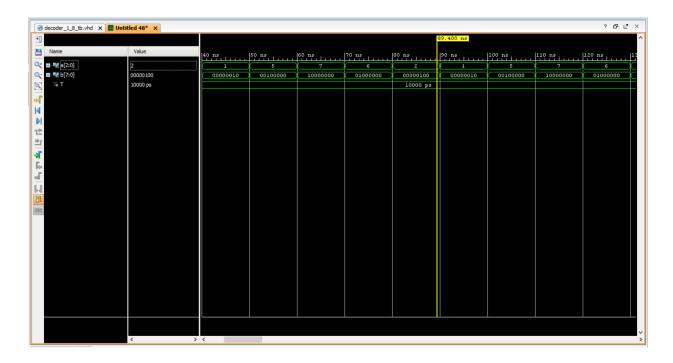
counter



mux 4:1



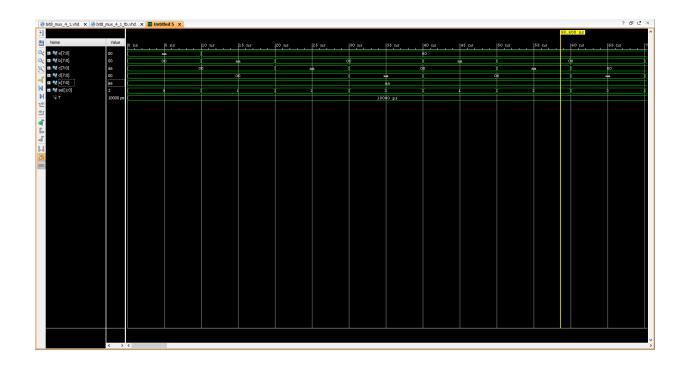
decoder



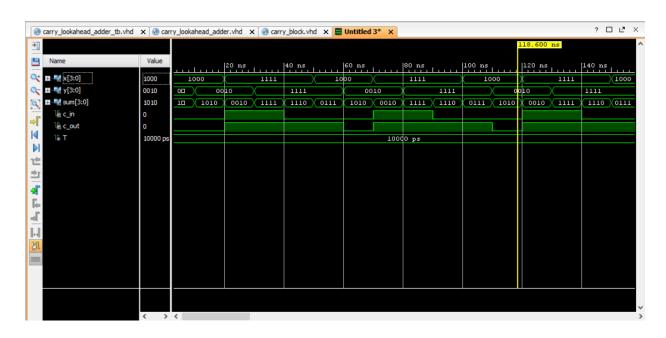
bcd decoder ssd



demux 8bit 4:1



carry lookahead adder



multiplier

