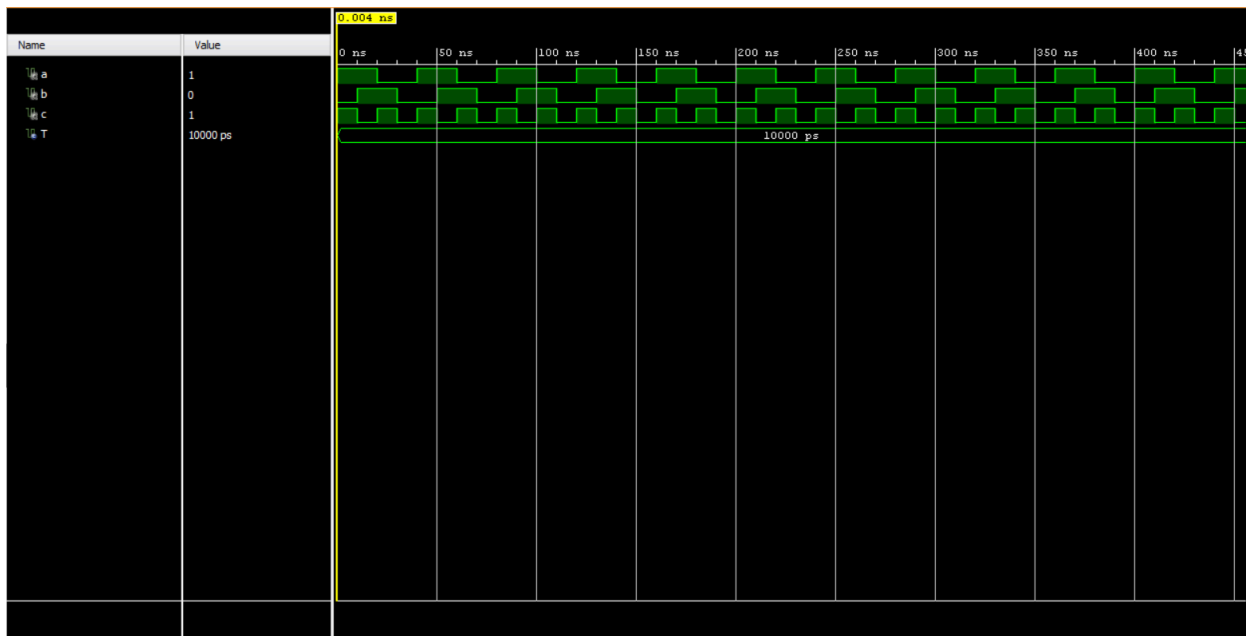
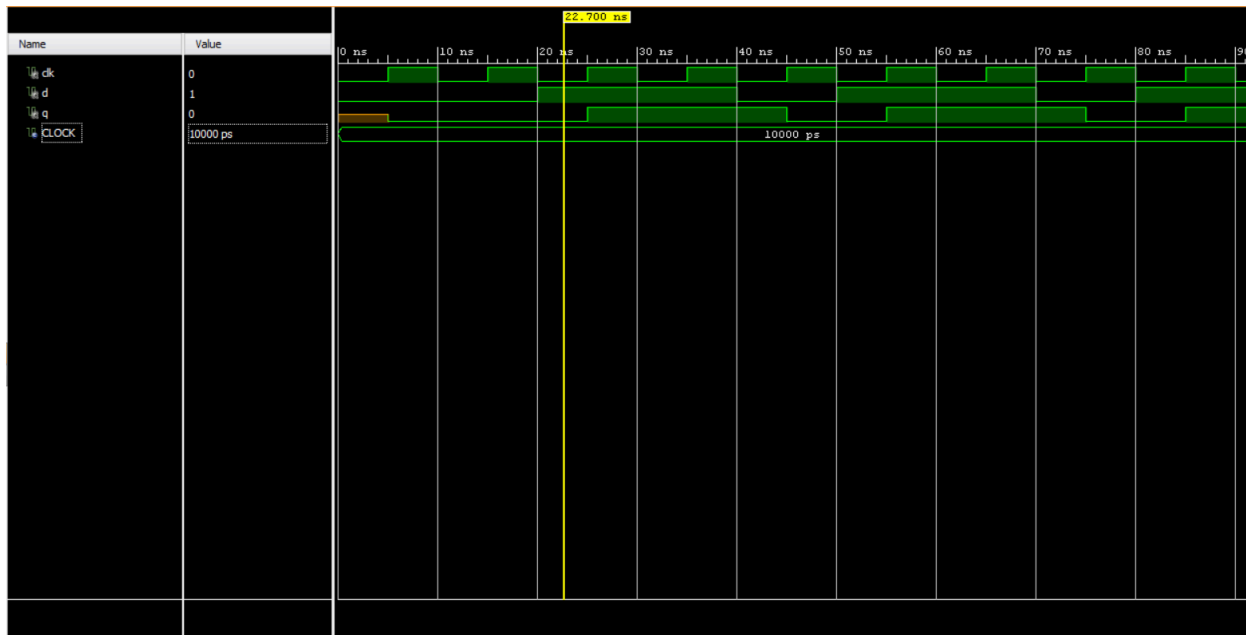


xor tb



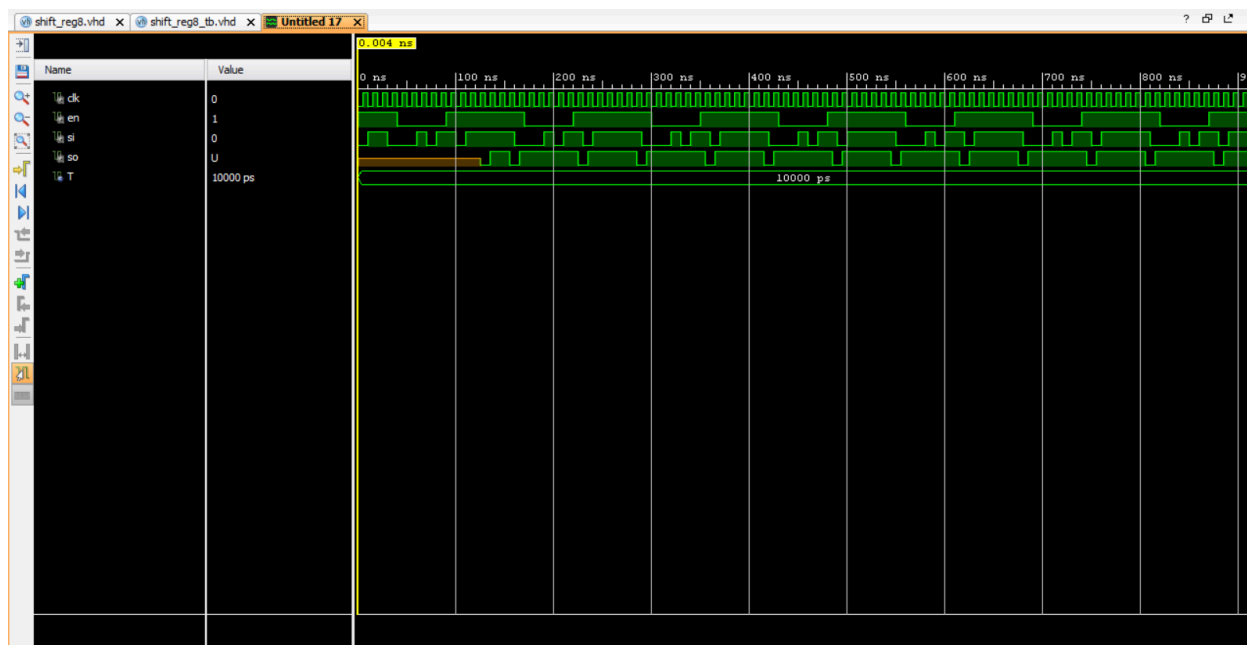
D_flip_flop



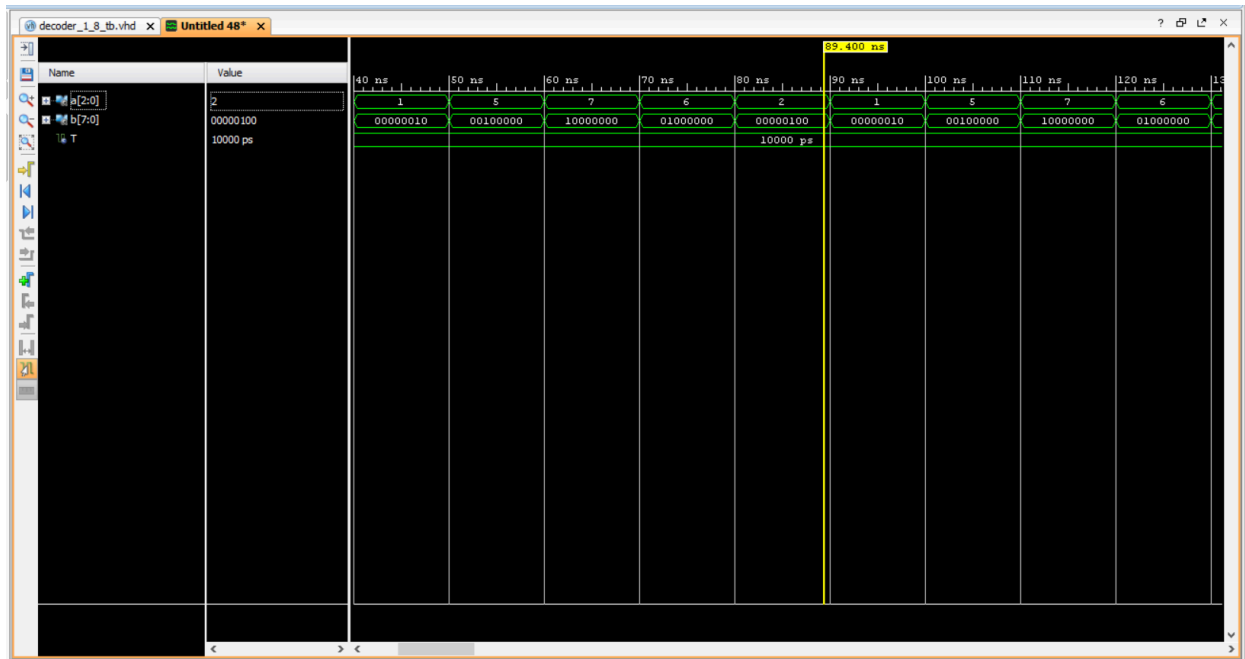
reg8



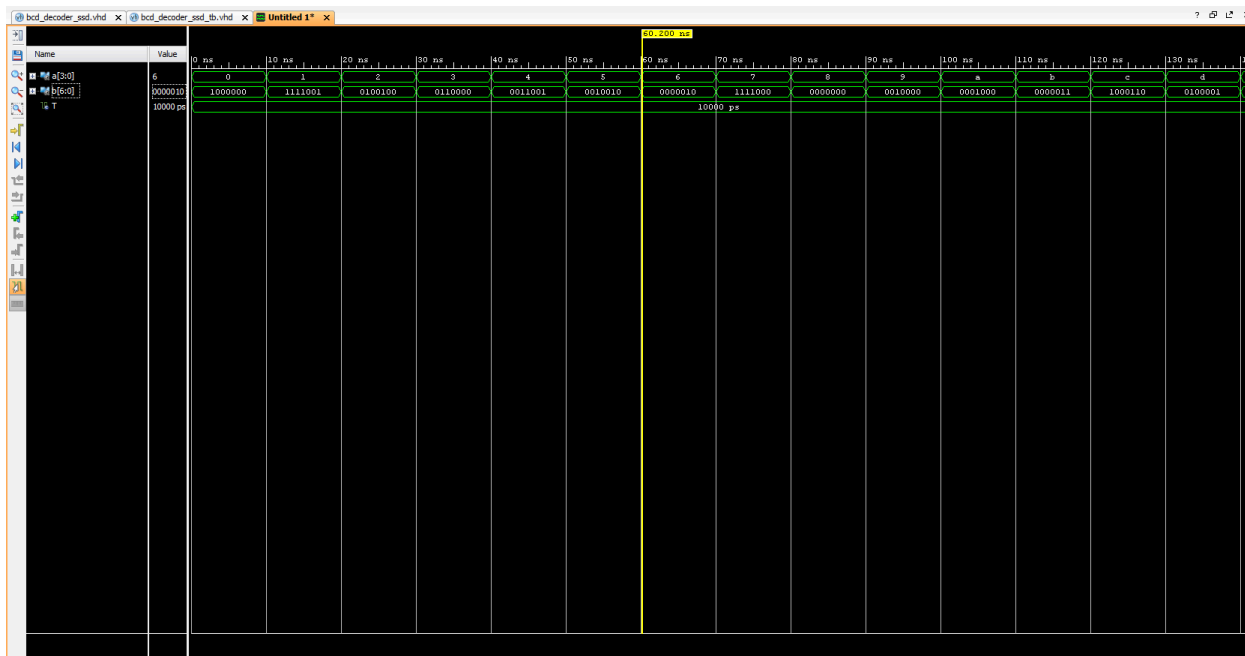
shifter



counter



bcd decoder ssd



demux 8bit 4:1

multiplier

