Low Level Programming
Lecture 1

Introductory information

Lecture outline

1. Short reminder of Intel/AMD 32 and 64-bit microprocessors architectures
2. Protected Mode (PM)
   - Memory management in PM.

3. Instruction set of Intel x86 microprocessors
   - Data transmission instructions,
   - Arithmetic operations,
   - Logical operations,
   - String handling instructions,
   - Special groups of instructions.

4. SIMD extensions in Intel microprocessors
   - Multimedia extension MMX.

5. Programming with Windows API.