

```

Xnand_2 Out N_3 Out1 Gnd Vdd nand $ $x=7100 $y=3300 $w=1800 $h=1000
Xnand_3 D En N_2 Gnd Vdd nand $ $x=3800 $y=5500 $w=1800 $h=1000
Xnand_4 En N_1 N_3 Gnd Vdd nand $ $x=3800 $y=3300 $w=1800 $h=1000
.ends

**** Top Level ****
XD_latch_1 N_1 En Out Out1 Gnd Vdd D_latch $ $x=5700 $y=4500 $w=1800 $h=1000
Xexor_1 Out T N_1 Gnd Vdd exor $ $x=2700 $y=4500 $w=1800 $h=1000
VV3 Vdd Gnd DC 5 $ $x=6700 $y=2600 $w=400 $h=600
*VV1 T Gnd PULSE(0 5 0 5n 5n 95n 200n) $ $x=1900 $y=2700 $w=400 $h=600
*VV2 En Gnd PULSE(0 5 0 5n 5n 95n 200n) ROUND=0 $ $x=3500 $y=2700 $w=400 $h=600

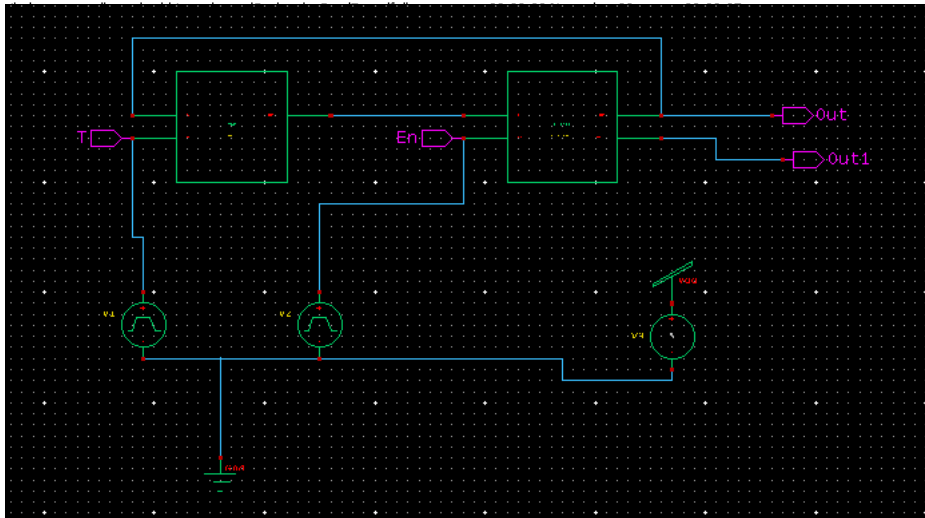
***** Simulation Settings - Analysis Section *****

.tran 10n 1000n start=0n
.lib "/home/WBPD07/SouhardyaDey/TannerEDA/TannerTools_v2021.2/Process/Generic_250nm/Models/Generic_250nm.lib" TT
.print tran v(T,Gnd) v(En,Gnd) v(Out,Gnd) v(Out1,Gnd)
VV1 T Gnd BIT ({1100} pw=5n lt=5n ht=5n on=2.5 off=0 rt=0.1n ft=0.1n)
VV2 En Gnd BIT ({1100} pw=5n lt=5n ht=5n on=2.5 off=0 rt=0.1n ft=0.1n)

***** Simulation Settings - Additional SPICE Commands *****

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Status	Input file	Start Time/Date	Elapsed Time



Parsing	0.26 seconds
Setup	0.19 seconds
DC operating point	0.46 seconds
Transient Analysis	32.28 seconds
Output	1.69 seconds
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Total	33.19 seconds

Simulation completed

