## E3 235-DAC 2021 Week-5 Lab-Assignment

- 1. **Get-it:** Circuits as given below. (figures-a & b)
- 2. **Put-it:** in LT-spice schematic with component and signal values. Mention circuit name along with your name in the schematic.
- 3. **Note-it:** LT-spice simulation readings including input and output  $(V_o, V_{out1}, V_{out2})$  signal-waveforms.
- 4. **Subm-it:** Simulated zip file before Monday-5:30PM and Lab- Experimental report with waveforms and corresponding tables before Tuesday-11:59PM.

**Note:** Assignments will be posted before Friday-5:30PM every week.

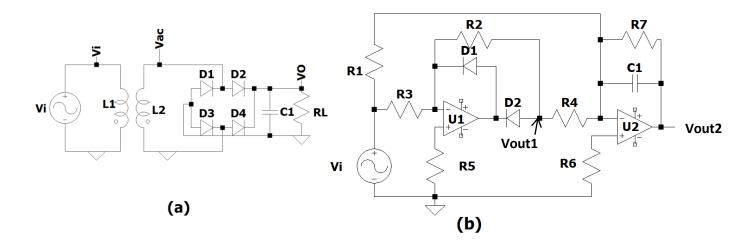


Fig-1: Rectifier (Classical) & Precession Rectifier (Op-Amp)

## Note:

- 1. You can use any op-amp of your choice.
- 2. Obtain your results and tabulate; include it in Lab-report.
- 3. Upload your report in the location: "E3 235-DAC2021/Assignments/Week-5"