Souparni Agnihoti. Group - J3 - Souparni Agnilustri Fahnida Jøgti Christopher Kelly. I am at a level of 7 confort with VHD REGISTER FILE. Rs-addr PSF. w_data D-00 (3D) · Decoder: - We would need a 5:32 devoder because we would be taking in a 5 bit unput and will get a 32 bit output. · 32:2 Multiplexer: I am using an array to store the 32 bit uput and will access each input for the corresponding select. I feel like that design would be easier to tuplement. Rs add 2 soudday · wodate gregister Yt oddy. 1 data I decoder. LA-CARA

MIPS PROCESSOR du SRC. ort-adda MIPS Add/sub w-addy file.