

# CS & IT ENGINEERING

Operating System

Process Synchronization

**Lecture No. 7**



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# TOPICS TO BE COVERED

Blocking Mechanisms :

Sleep-Wakeup

Semaphore \*



Q.



Fetch\_And\_Add (X, i) is an atomic Read-Modify-write instruction that reads the value of memory location X, increments it by the value i and returns the old value of X. It is used in the pseudocode shown below to implement a busy-wait lock. L is an unsigned integer shared variable initialized to 0. The value of 0 corresponds to lock being available, while any non-zero value corresponds to the lock being not available.

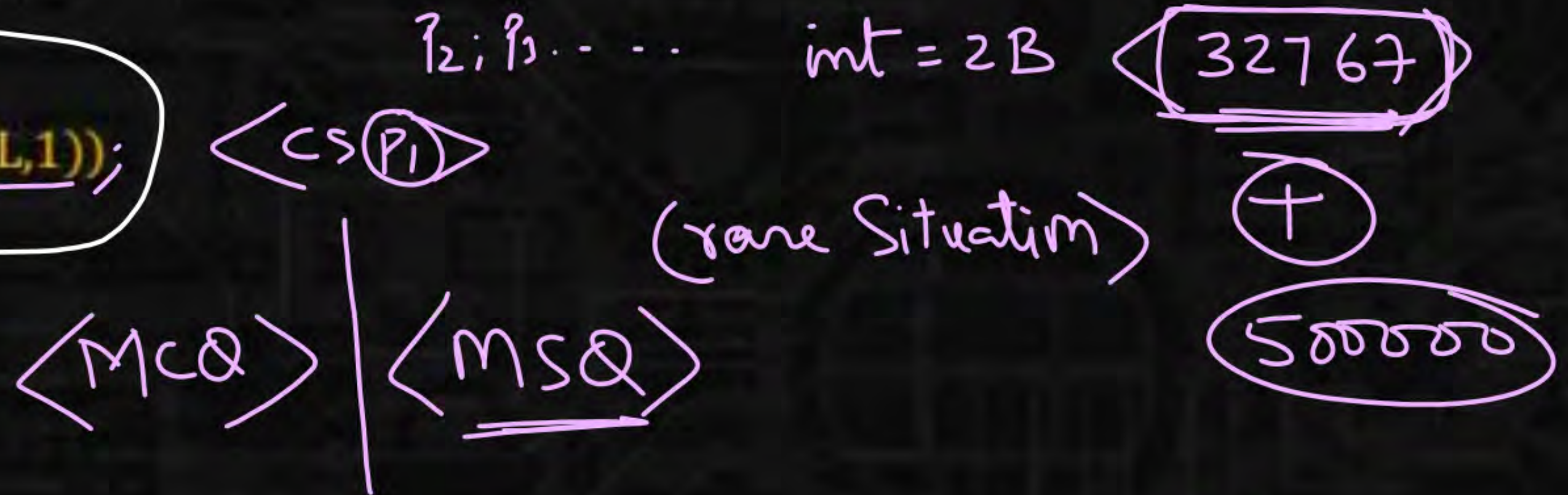
AcquireLock(L)

```
{
  While (Fetch_And_Add (L,1));
  L=1;
}
```

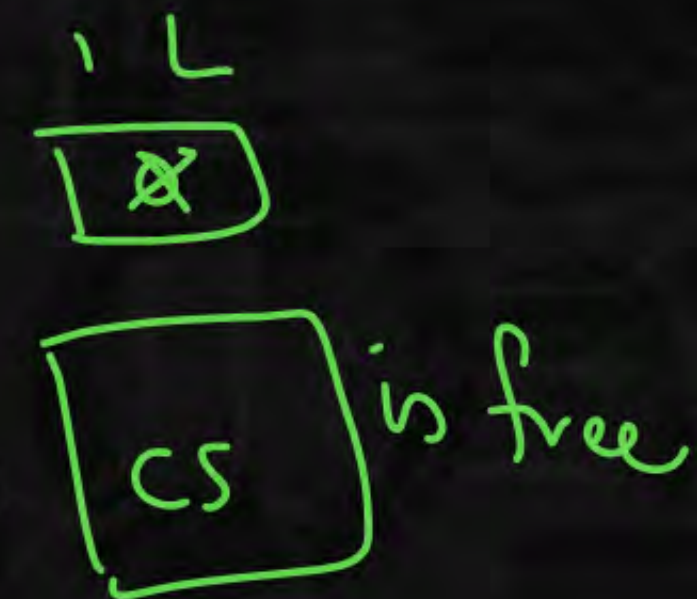
ReleaseLock(L)

```
{
  L=0;
}
```

This implementation



- ✓ A. fails as L can overflow
- ✓ B. fails as L can take on a non-zero value when the lock is actually available
- C. works correctly but may starve some processes
- D. works correctly without starvation





Atomic exec

```

int Fetch-And-Add(x,i)
{
    int rv;
    rv = x;
    x = x+i;
    return(rv);
}
  
```

int lock = 0;

Entry While (Fetch-And-Add(&Lock, 1))  
 Re → L = 1;

<cs>

Exit L = 0

$\overline{P_1 P_2 P_3}^{R0}$   $\overline{\cancel{X}}_2^{lock}$   
 $cs \ P_1$

I:

$t_1: (P_1): F-A-A: \rightarrow 0$

$t_2: (P_2): F-A-A: \rightarrow 1$

II:

$t_1: (P_1): F-A-A: \checkmark: \langle cs \rangle \dots Pre$

$t_2: (P_2): F-A-A: 1; (Pre): \langle L=1 \rangle$

$t_3: (P_1): \langle cs \rangle;$

$t_4: (P_2): L=1; FAA: 1$

int: 2B  
 <32767>

$\overline{\cancel{1} \cancel{X} \cancel{Z}}^{1} lock$



## II: Blocking Mechanisms / Non-Busy-waiting

< is to avoid wastage of CPU time  
in the form of loops;

: if - then - else

: Sleep-wakeup

: Semaphores

: Monitors

TQ = ~~9~~ ①  
while (-----);

Entry :  $P_i$

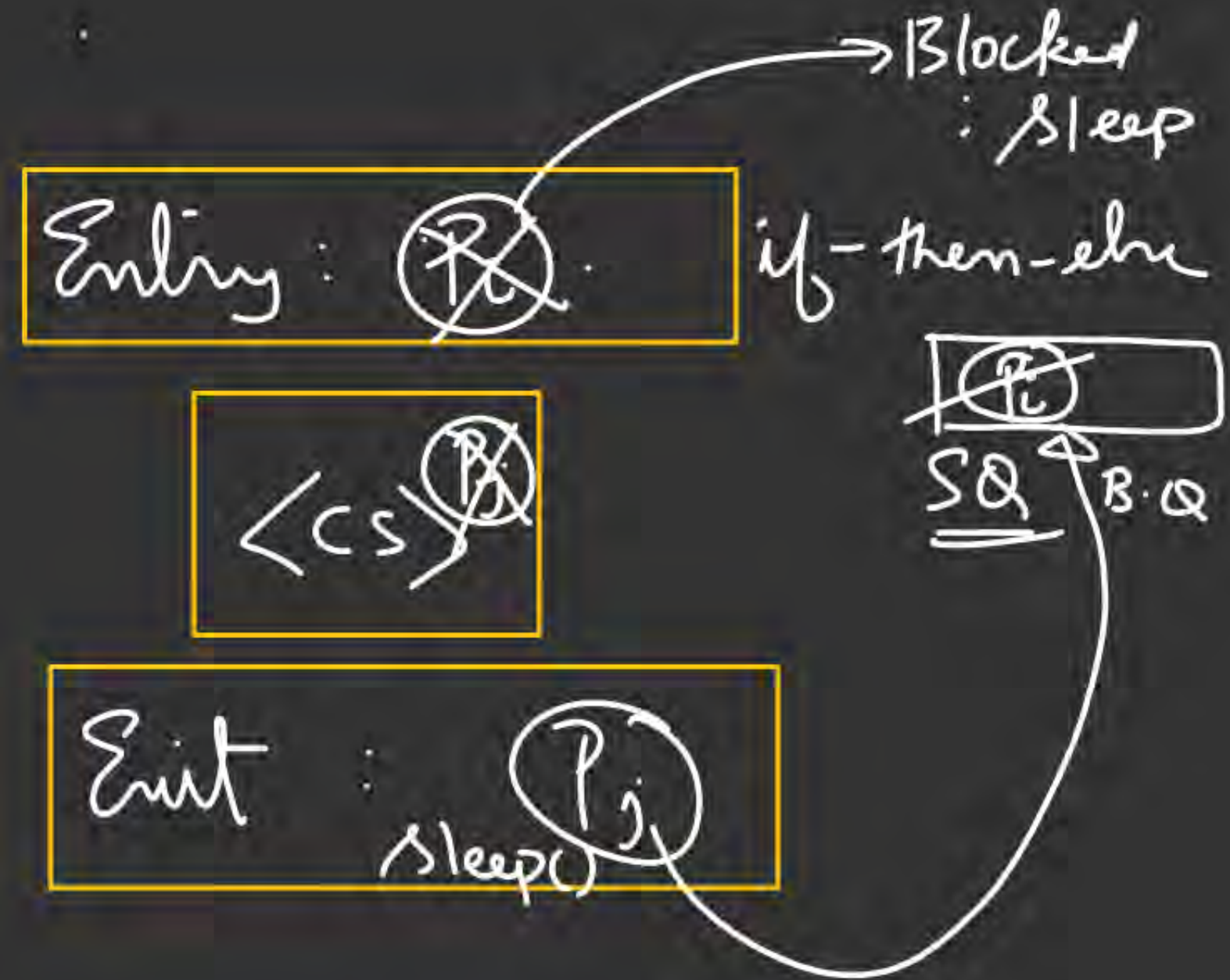
<cs>

Exit



# ① Sleep() & wakeup()

- Blocking Construct
- Multi-Process Synchronisation
- are OS primitives
- when process executes Sleep() : → gets Blocked till some other process wakes it up (wakeup())





# Producer-Consumer using Sleep() & Wakeup()

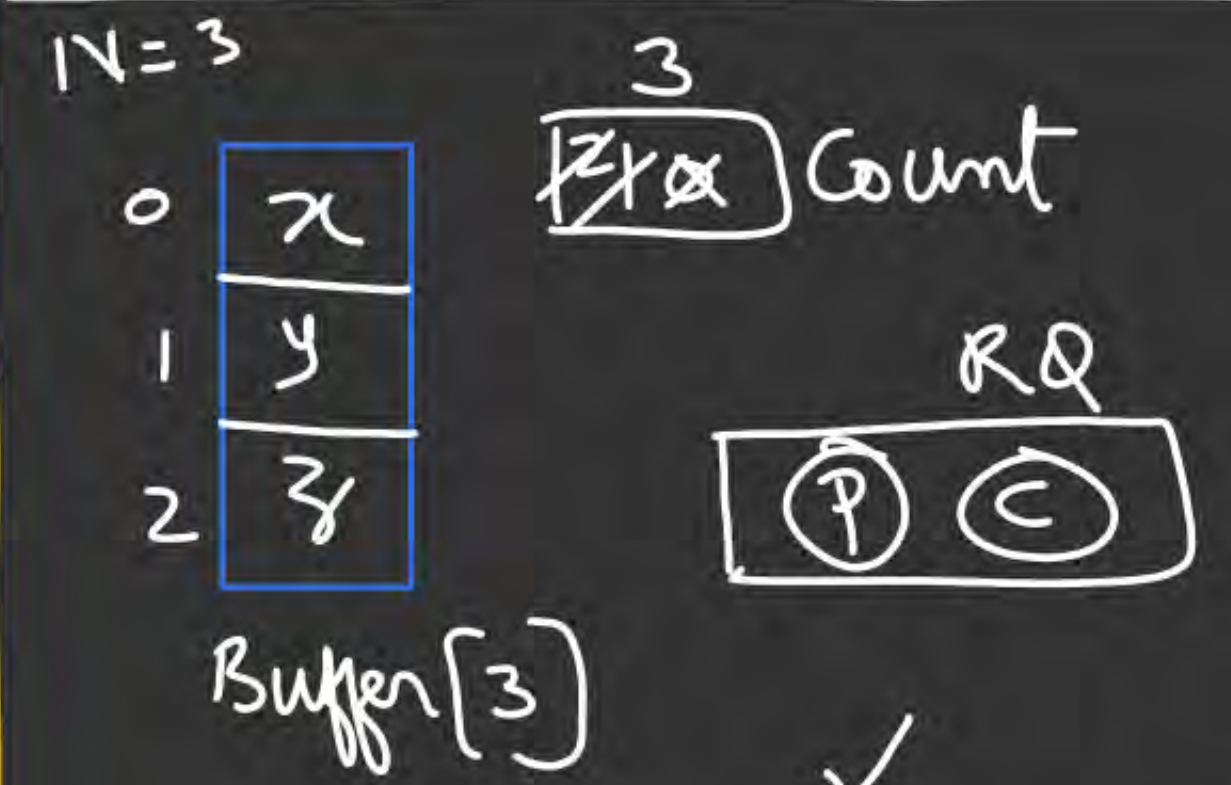
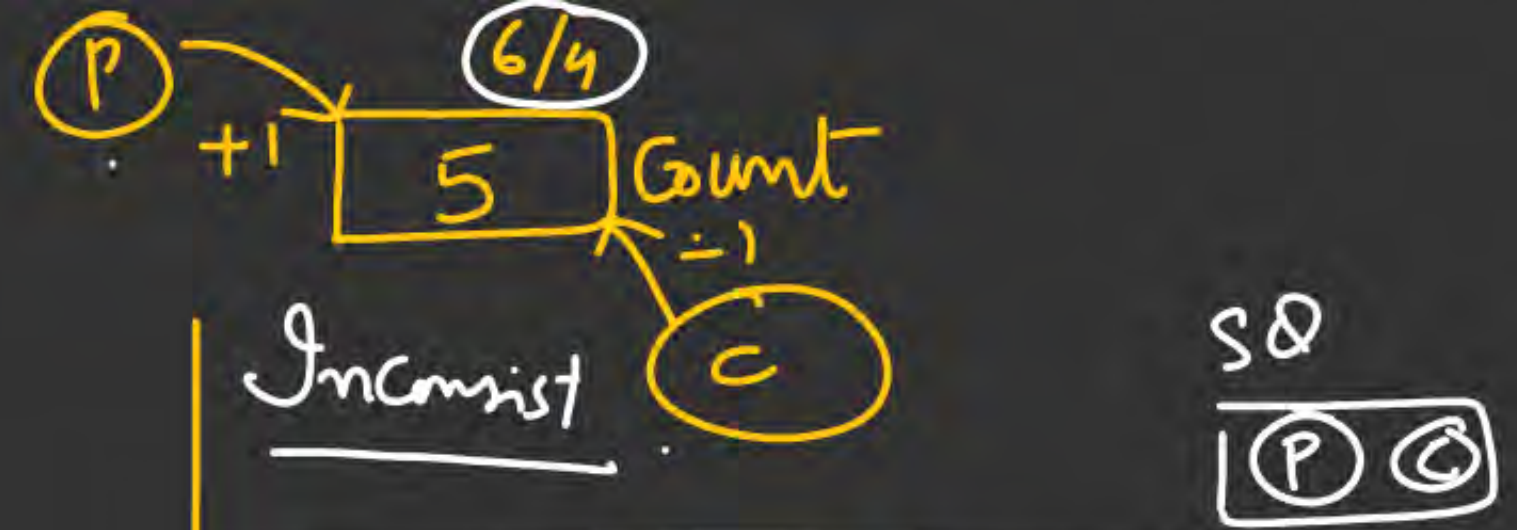
```
#define N 100
int Buffer[N];
int Count = 0
```

```
void Producer(void)
{
    int item, in = 0;
    while(1)
    {
        a) item = Produce-item();
        b) if (Count == N) Sleep();
        c) Buffer[in] = item;
        d) in = (in + 1) % N;
        e) Count = Count + 1;
        f) if (Count == 1) Wakeup(Consumer);
    }
}
```

```
void Consumer(void)
{
    int itemc, out = 0;
    while(1)
    {
        L; C; J; Sleep
        a) if (Count == 0) Sleep();
        b) itemc = Buffer[out];
        c) out = (out + 1) % N;
        d) Count = Count - 1;
        e) if (Count == (N-1)) Wakeup(Producer);
        f) Process-item(itemc);
    }
}
```

Inconsistency

Deadlock



$t_1: C: L; C; J; Re; \langle sleep \rangle$   
 $t_2: P: \dots Sleep(); \checkmark$   
 $t_3: C: Sleep();$



# SEMAPHORES:

→ Blocking Construct

→ OS based

<: is an OS resource  
(S/W)>

→ General purpose utility

{ — <CS>

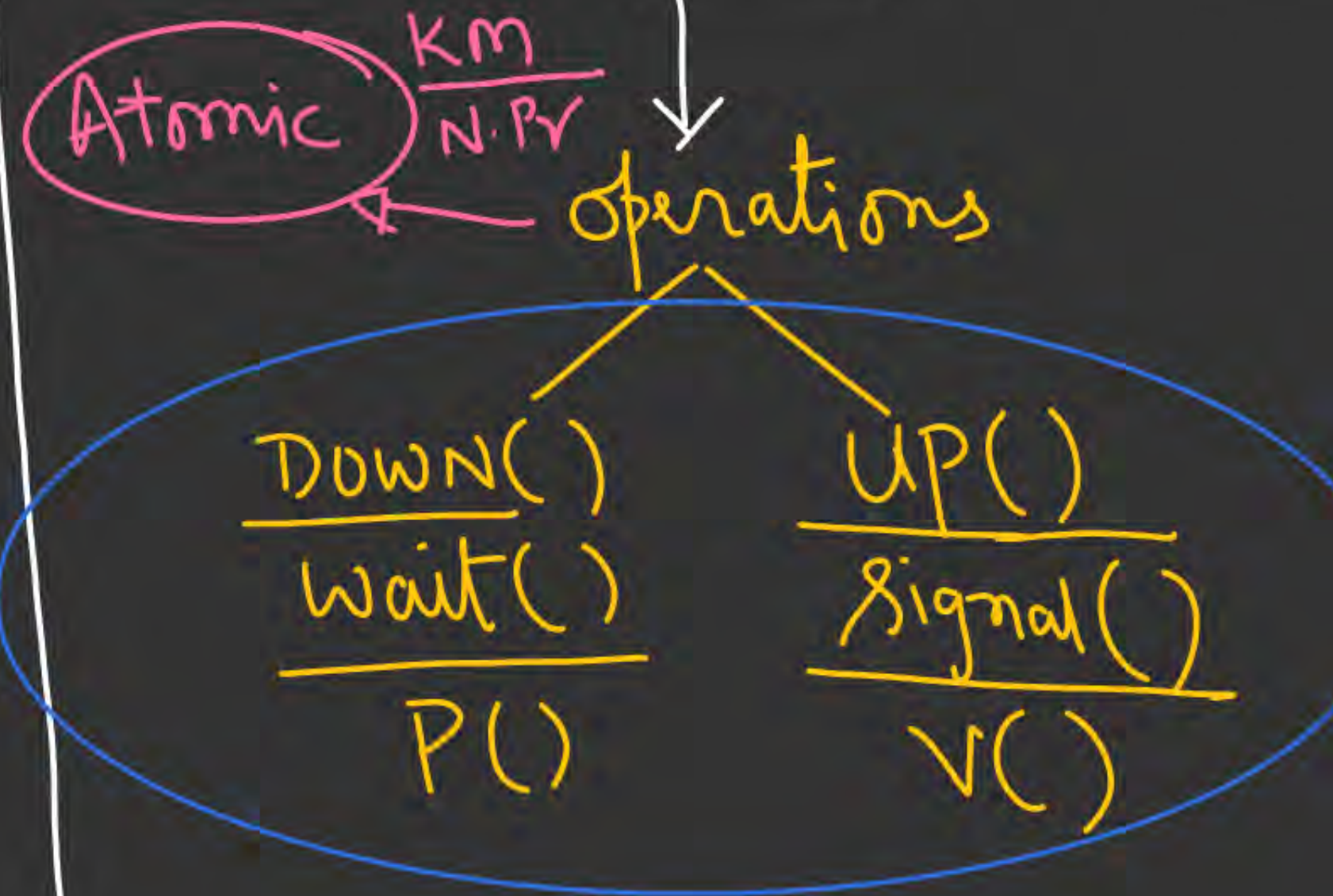
— Requirsements of classical  
IPC Problems

— Concurrency Mechanisms

+ Practically in use in OS

→ It was proposed &  
Impl. by E. Dijkstra

→ Semaphore is Implemented  
as an A-D-T <sup><switch></sup>





Defn: is a variable  $\langle \underline{ADT:SEM} \rangle$  that takes only integer values;

