TEST SUMMARY

ROTARY ENCODER USING PCNT DRIVER

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This is a summary of learnings I've made while testing the encoder on Pulse Counter module

Structs in C++ require ordered declaration
 For example,

```
pcnt_config_t pcnt_config = {
    .pulse_gpio_num = 15,
    .ctrl_gpio_num = 4,
    .lctrl_mode = PCNT_MODE_REVERSE,
    .hctrl_mode = PCNT_MODE_KEEP,
    .pos_mode = PCNT_COUNT_INC,
    .neg_mode = PCNT_COUNT_DIS,
    .counter_h_lim = INT16_MAX,
    .counter_l_lim = INT16_MIN,
    .unit = PCNT_UNIT_0,
    .channel = PCNT_CHANNEL_0
};
```

The above order is the correct initialization order of the struct pcnt_config_t, unlike the specified version in the pcnt example in ESP-IDF.

- If the encoder used is **2500PPR**, we can only achieve a resolution of **2500 counts per revolution** using a single PCNT unit, due to its configurability (Could be wrong!). To get **10000 counts per revolution**, we need to route these ports to 2 PCNT units with reversed pulse-control configuration and somehow combine the data from each of these two streams.
- The filter can be safely ignored in this case, due to stable pulses.
- The interrupts have been disabled using pcnt_intr_disable(PCNT_UNIT_X), which naturally cuts off events.