


# Pixel Online Software - Update

- ✓Several internal changes in PixelSupervisor, PixelFECSupervisor, PixelFEDSupervisor and PixelConfigDataFormats effected.
- ✓For coding consistency.
- ✓To accommodate multiple FECs and FEDs.
- ✓To weed out more hardcoding.
- ✓FED Supervisor has green light when last entry in a FED's TTS FIFO is Ready. Has red light otherwise. (Being tested.)

**Pixel Front End Driver Supervisor**Version: 3.0  
Date: Wed, 07 Feb 2007 03:11:43 GMTConfigured


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### Finite State Machine

Current State Configured				
<input type="button" value="Configure"/>	<input type="button" value="Halt"/>	<input type="button" value="Pause"/>	<input type="button" value="Resume"/>	<input type="button" value="Start"/>

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### Low Level Commands

FED with Base Address 0x1c000000: 

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## ✓FED Supervisor Low Level GUI

- ✓Sliders introduced for Channel Offset DAC and Optical Receiver Input Offset settings.
- ✓Slider values read from PixelFEDCard objects – Javascript for WebGUI dynamically created by C++.
- ✓Introduced a button for reading TTS FIFO of all FEDs in the crate.
- ✓Testing and developing existing tools. May add a few buttons to dump Error FIFO and Last DAC FIFO (DCS needs this capability soon)

The screenshot displays a web-based GUI for the Pixel Online Software. It features a vertical list of 20 sliders, each labeled 'FED Channel X Offset DAC' followed by a numerical value. The sliders are arranged in a column, with each slider having a blue circular handle. To the right of the sliders is a section labeled 'Optical Receiver Input Offset' with a single slider and a numerical value. Below this section is a button with a right-pointing arrow. The sliders are numbered 1 through 20, and their values are: 0, 100, 50, 171, 0, 0, 0, 87, 1, 2, 3, 4, 5, 6, 0, 0, 0, 0, 0, 0.

FED Channel	Offset DAC Value
1	0
2	100
3	50
4	171
5	0
6	0
7	0
8	87
9	1
10	2
11	3
12	4
13	5
14	6
15	0
16	0
17	0
18	0
19	0
20	0

Optical Receiver Input Offset: 9

Button: [Right Arrow]