

Pixel Front End Controller Supervisor

Version: 3.0 Date: Thu, 11 Jan 2007 14:07:41 GMT

Halted

Finite State Machine

Current State Halted	FEC Base Address: Global Key:			
Configure	Halt	Pause	Resume	Start

Low Level Commands

TBM Command				
mFEC: 1 • mFEC Channel: A • TBM Channel: A • Hub Address: 0 • Port Address: 0 • Offset: 0 • Data Byte: Direction: Up • TBMCommand				
Program DAC mFEC: 1 • mFEC Channel: A • Hub Address: 0 • Port Address: 0 ROC Id: 0 • DAC Address: Vdd • DAC Value: Prog_DAC	Program Pixel mFEC: 1 • mFEC Channel: A • Hub Address: 0 • Port Address: 0 ROC Id: 0 • Pixel Column: 0 • Pixel Row: 0 • Pixel: Enable • Trim (0-15): 0 •			
Calibrate Pixel mFEC: 1	Clear Calibration mFEC: 1			

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