



# Memory Expansion Daughterboard Documentation

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*Memory Expansion Daughterboard Documentation*  
*SpaceLab, Universidade Federal de Santa Catarina, Florianópolis - Brazil*



# Memory Expansion Daughterboard Documentation

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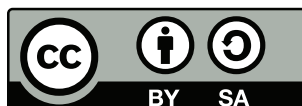
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# CHAPTER 1

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## Introduction

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The Memory Expansion Daughterboard (DB Memory in short) is a expansion module for the On-Board-Data-Handling (OBDH 2) of FloripaSat-2 2U CubeSat. The main purpose of DB Memory is to give more 1gb non-volatile flash memory though an integrated circuit (MT25QL01GBBB) and more if added a micro sd card. While the module is not intended to go for flight in GOLDS-UFSC mission, the project had the main purpose to design the first board using the stadar SpaceLab daughter and motherboard relation and to serve as a prototype and testing printed circuit boards (PCB) for new comers in SpaceLab laboratory.

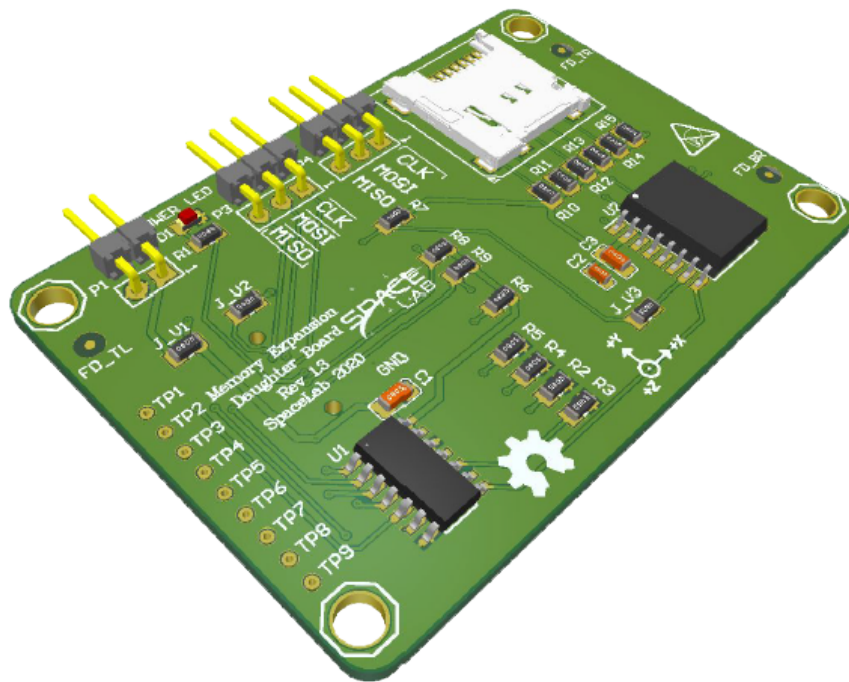


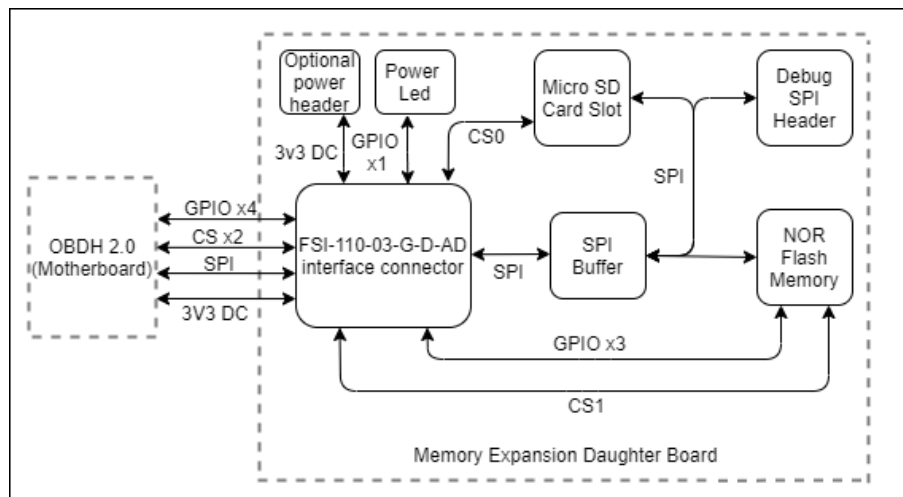
Figure 1.1: 3D view of the DB Memory PCB.



## CHAPTER 2

### System Overview

#### 2.1 Block Diagram





## CHAPTER 3

## Hardware

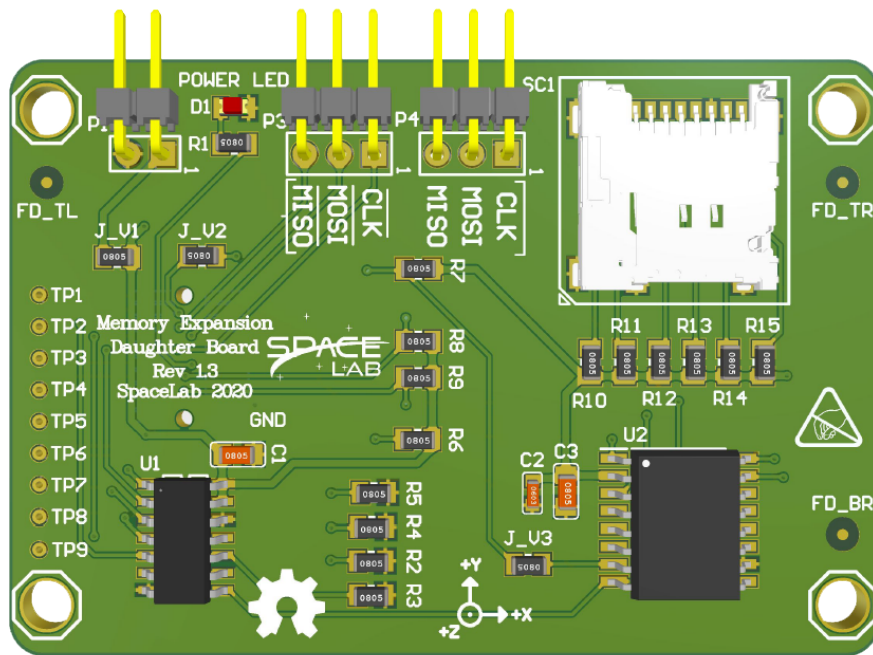


Figure 3.1: Top 3D view of the DB Memory PCB.

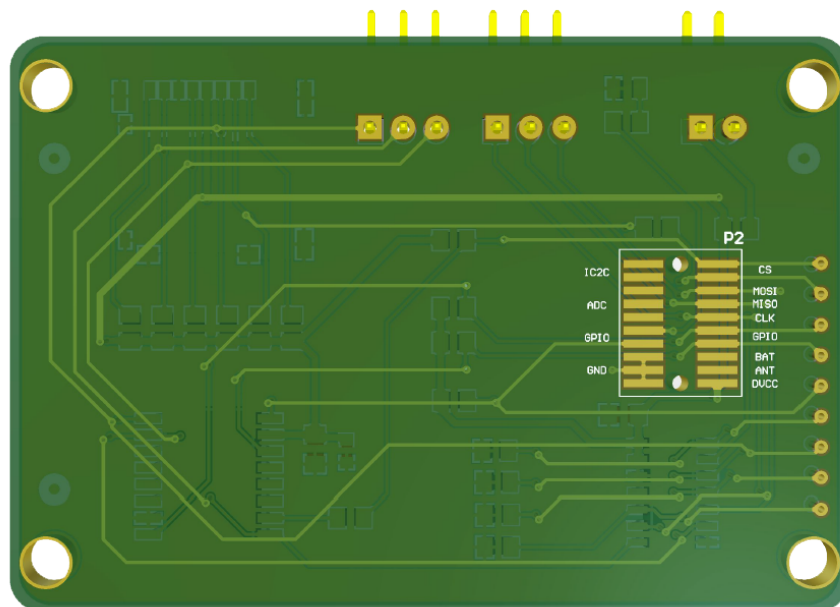


Figure 3.2: Bottom 3D view of the DB Memory PCB.



## CHAPTER 4

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### Firmware

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## CHAPTER 5

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### Usage Instructions

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