

A

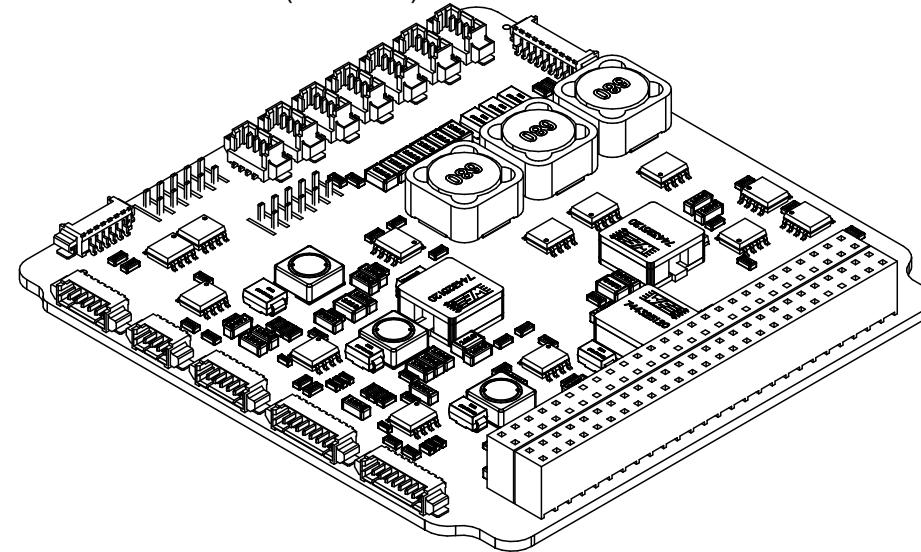
B

C

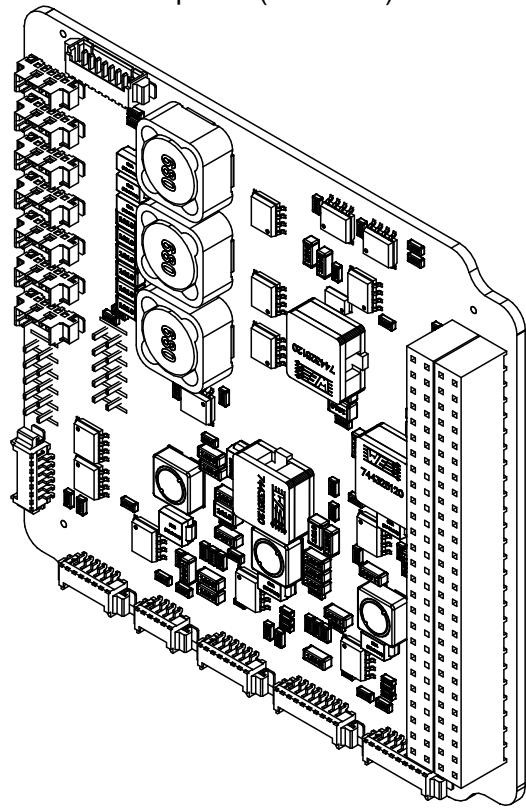
D

E

View from Front side (Scale 1:1)



View from Top side (Scale 1:1)



EPS 2.0 Hardware:

- Drawn by: André M. P. Mattos (updates from FloripaSat-I EPS)
- Based on FloripaSat-I OBDH designed by: Sara V. Martinez
- Reviewers: Kleber Gouveia and Yan C. Azeredo
- Support: Gabriel M. Marcelino

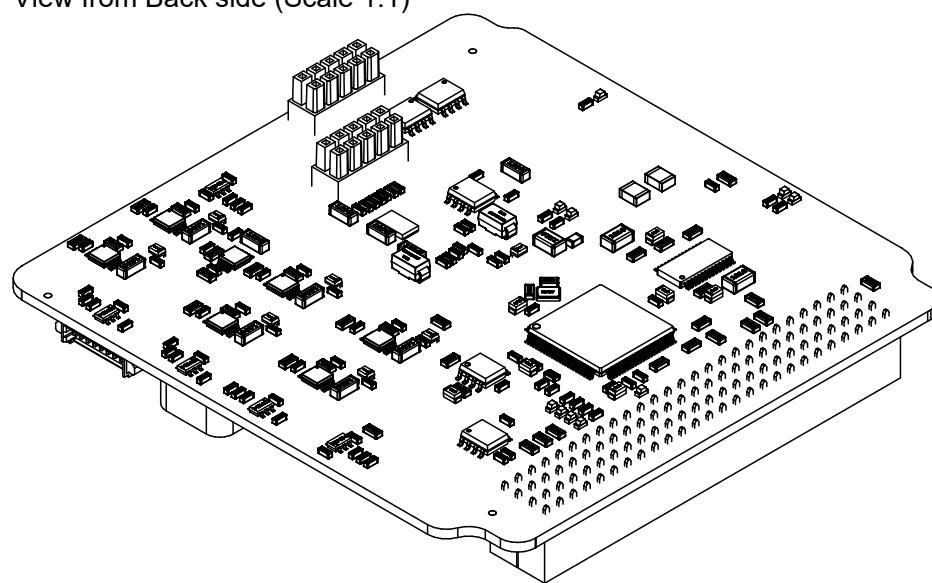
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Github repository: <https://github.com/spacelab-ufsc/eps2>

More info about SpaceLab: <https://spacelab.ufsc.br/>

View from Back side (Scale 1:1)



SpaceLab - Federal University of Santa Catarina	
Project: Electrical and Power System 2.0	
Title: Project info and board isometric views	
Designed by: André M. P. Mattos	

Date: 6/17/2021	Version: v0.2	Sheet 1 of 3
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A

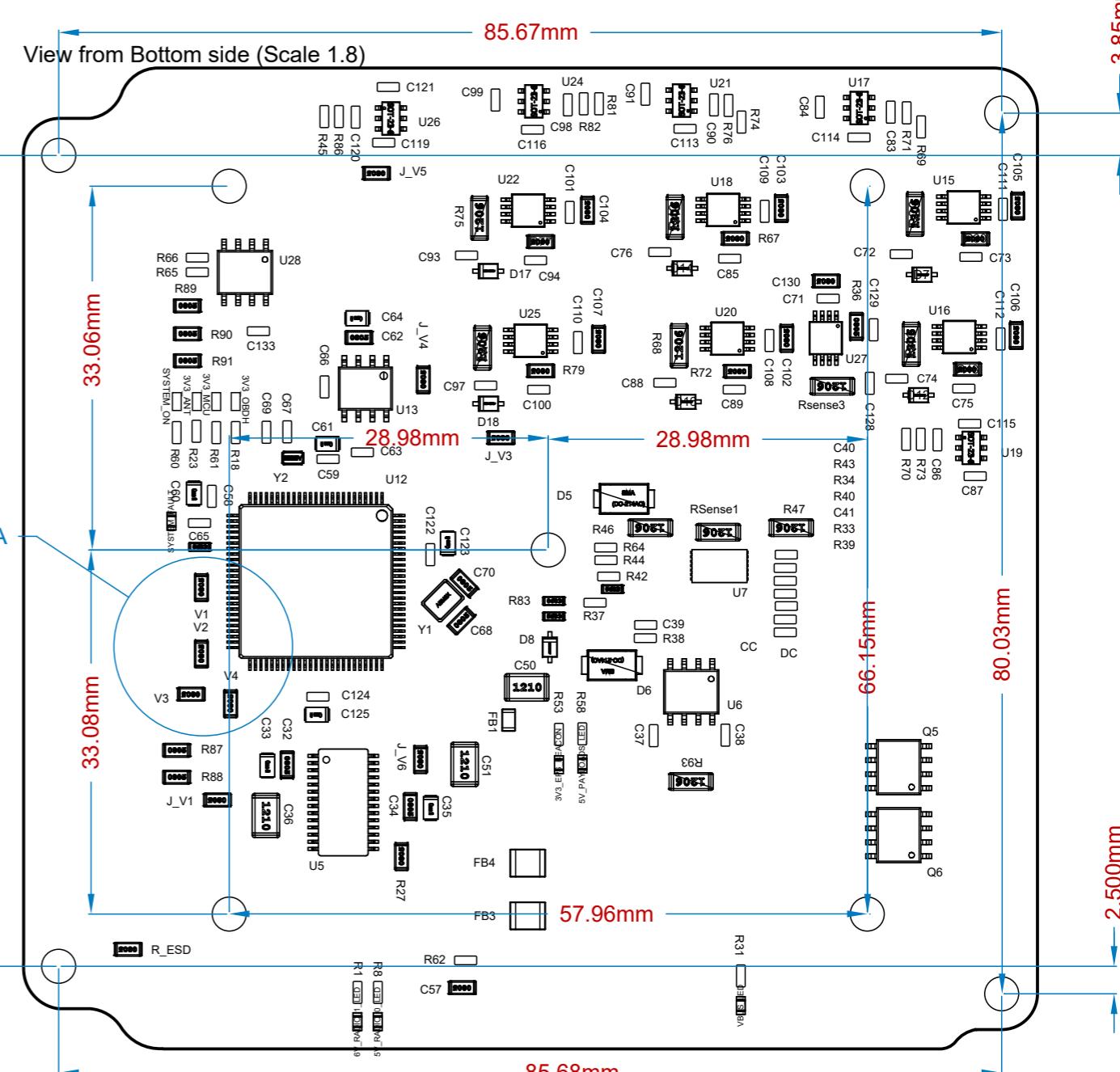
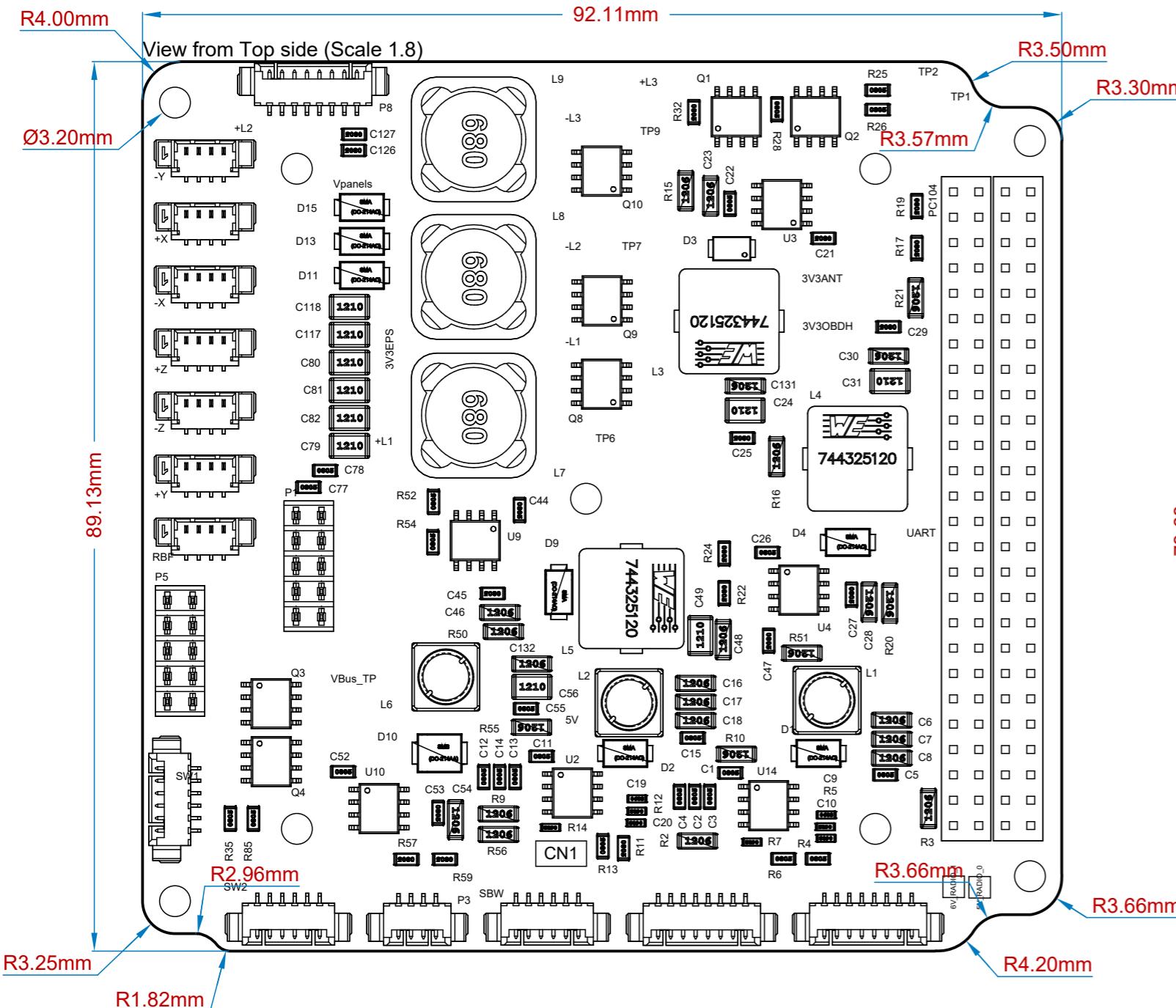
B

C

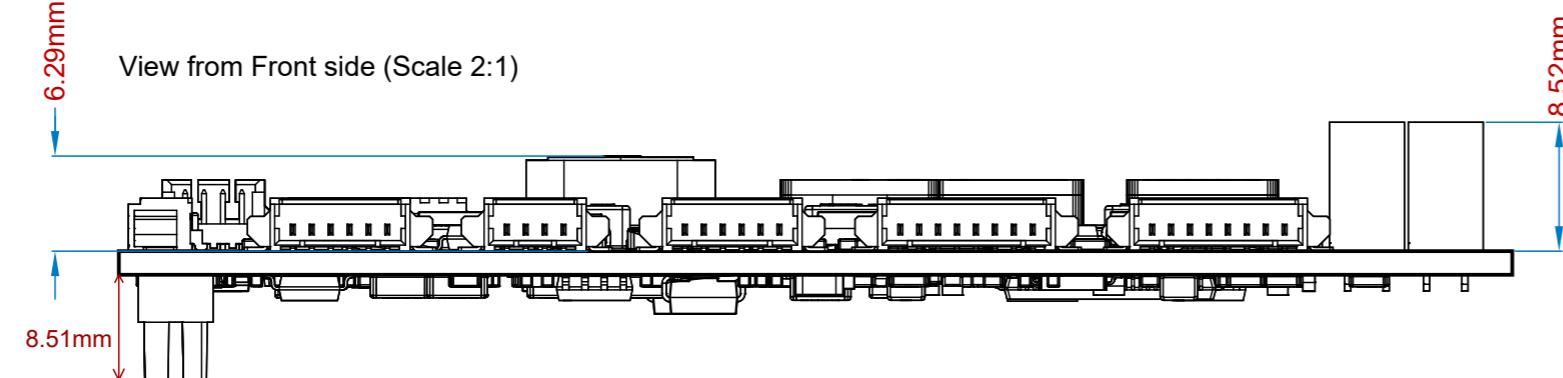
D

E

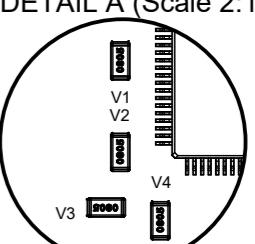
Placed components within a red mesh box  not supposed to be soldered in the flight model of the board.



View from Front side (Scale 2:1)



DETAIL A (Scale 2:1)
Hardware versioning
resistors



SpaceLab - Federal University of Santa Catarina

Project: Electrical and Power System 2.0

Title: Project info and board isometric views

Designed by: André M. P. Mattos

Date: 6/17/2021 Version: v0.2 Sheet 2 of 3



Project code: EPS2

Sheet size: A3

A

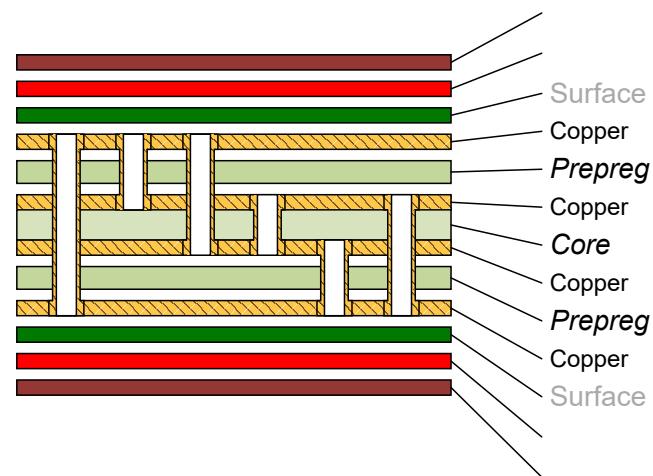
B

C

D

E

Layer Stack Legend



Material

Layer

Thickness

Dielectric Material

Type

Gerber

Top Paste

Paste Mask GTP

Top Overlay

Legend GTO

Surface Material

Top Solder

0.01mm

Solder Resist

Solder Mask GTS

Copper

Top Layer

0.04mm

Signal GTL

Prepreg

0.11mm

Dielectric

Copper

Signal Layer 1

0.04mm

Signal G1

Core

1.20mm

Dielectric

Copper

Signal Layer 2

0.04mm

Signal G2

Prepreg

0.11mm

Dielectric

Copper

Bottom Layer

0.04mm

Signal GBL

Surface Material

Bottom Solder

0.01mm

Solder Resist

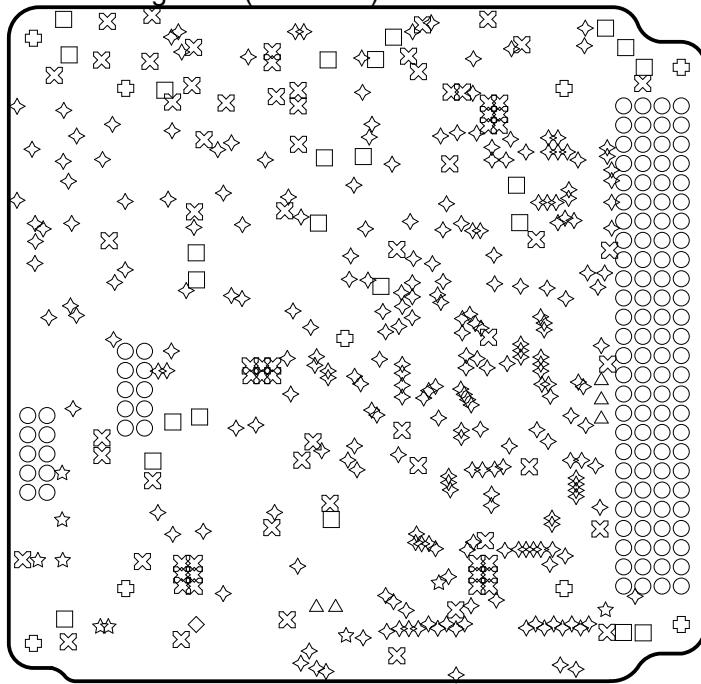
Solder Mask GBS

Bottom Overlay

Bottom Paste

Total thickness: 1.58mm

Drill Drawing View (Scale 1:1)



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
◇	253	0.30mm	Plated	None
☆	9	0.38mm	Plated	None
◊	1	0.60mm	Plated	None
✗	77	0.71mm	Plated	None
○	124	0.90mm	Plated	None
△	5	0.99mm	Plated	None
□	24	1.00mm	Plated	None
✚	9	3.20mm	Plated	None
502 Total				

SpaceLab - Federal University of Santa Catarina

Project: Electrical and Power System 2.0

Title: Layer stack and drill tables

Designed by: André M. P. Mattos



Project code: EPS2

Date: 6/17/2021 Version: v0.2 Sheet 3 of 3

Sheet size: A4

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B

C

D

E