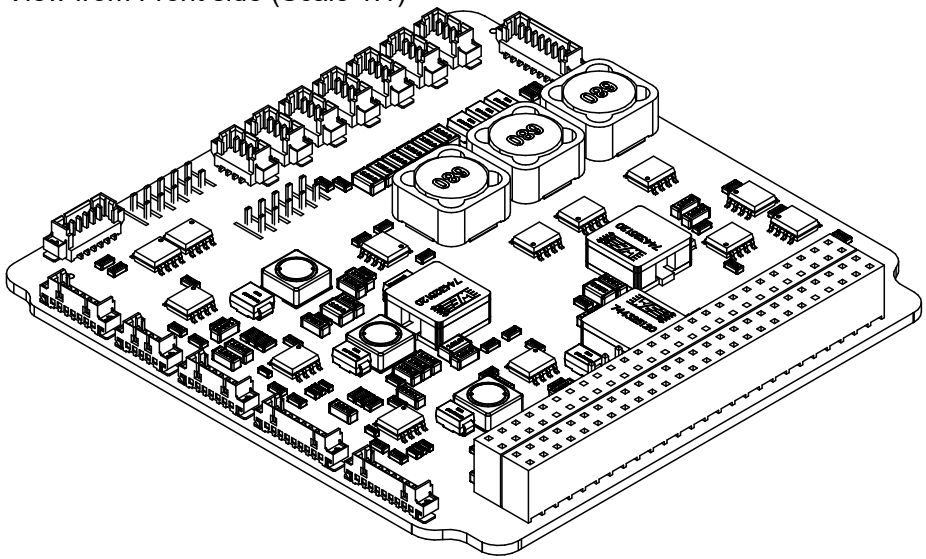
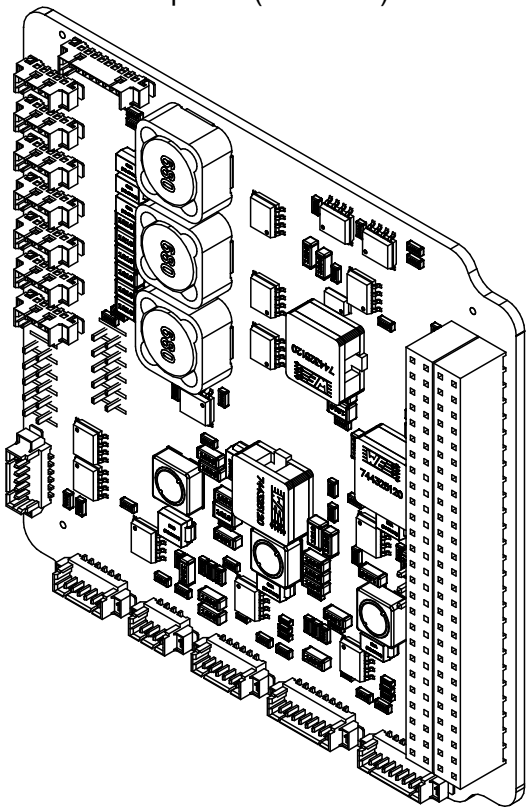
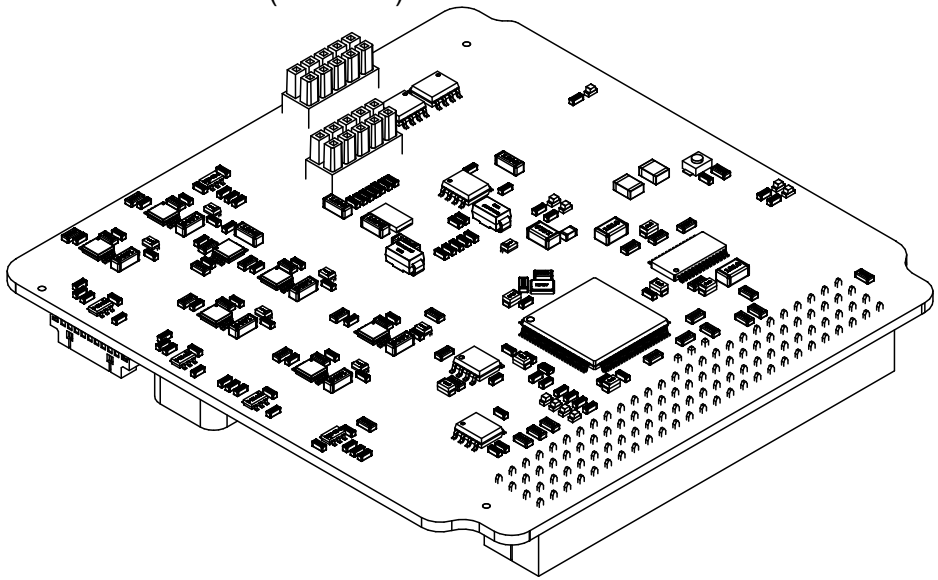
View from Front side (Scale 1:1)



View from Top side (Scale 1:1)



View from Back side (Scale 1:1)



EPS 2.0 Hardware:

- Drawn by: André M. P. Mattos (updates from FloripaSat-I EPS)
- Based on FloripaSat-I OBDH designed by: Sara V. Martinez
- Reviewers: Kleber Gouveia and Yan C. Azeredo
- Support: Gabriel M. Marcelino

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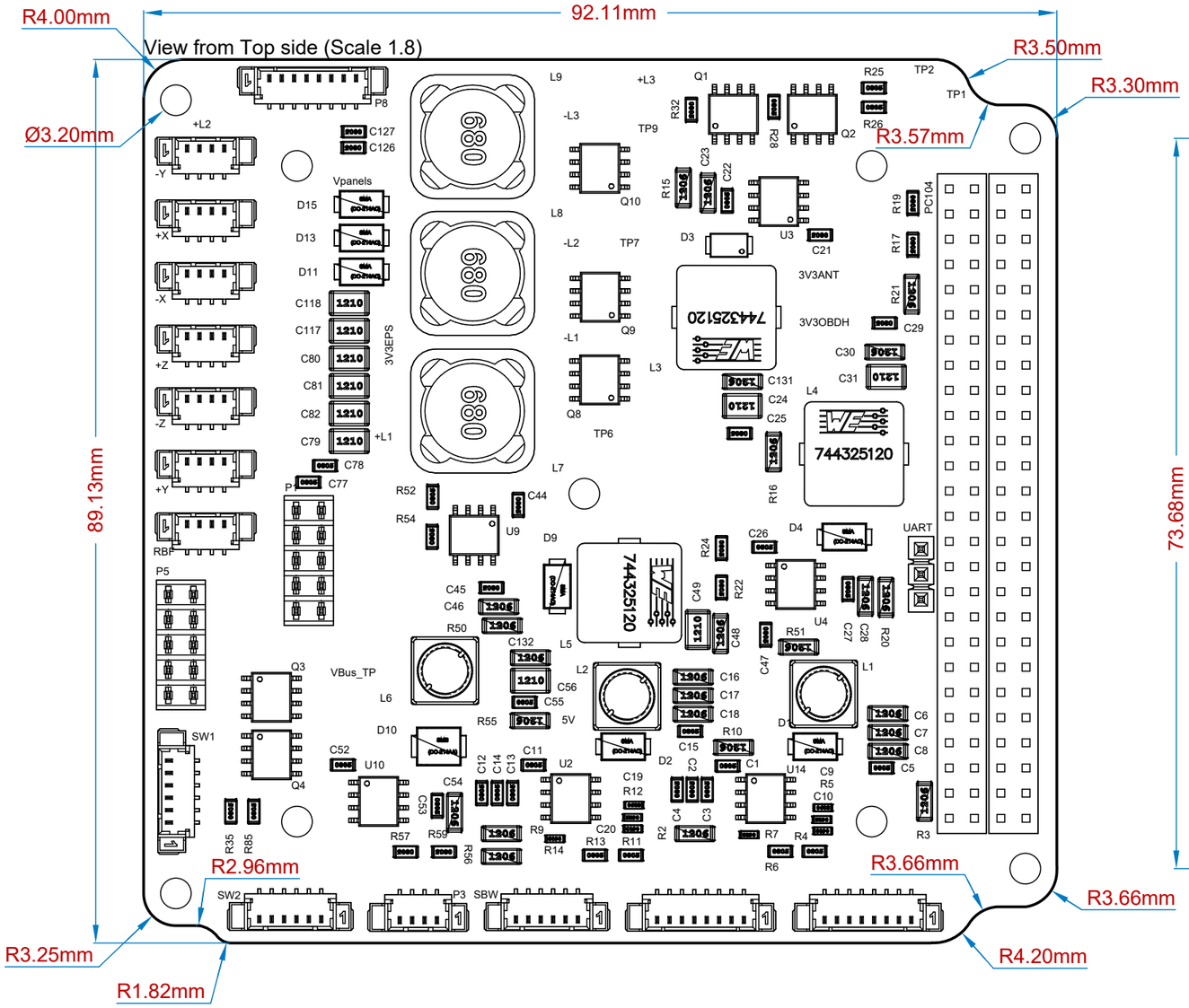
Github repository: https://github.com/spacelab-ufsc/eps2

More info about SpaceLab: https://spacelab.ufsc.br/

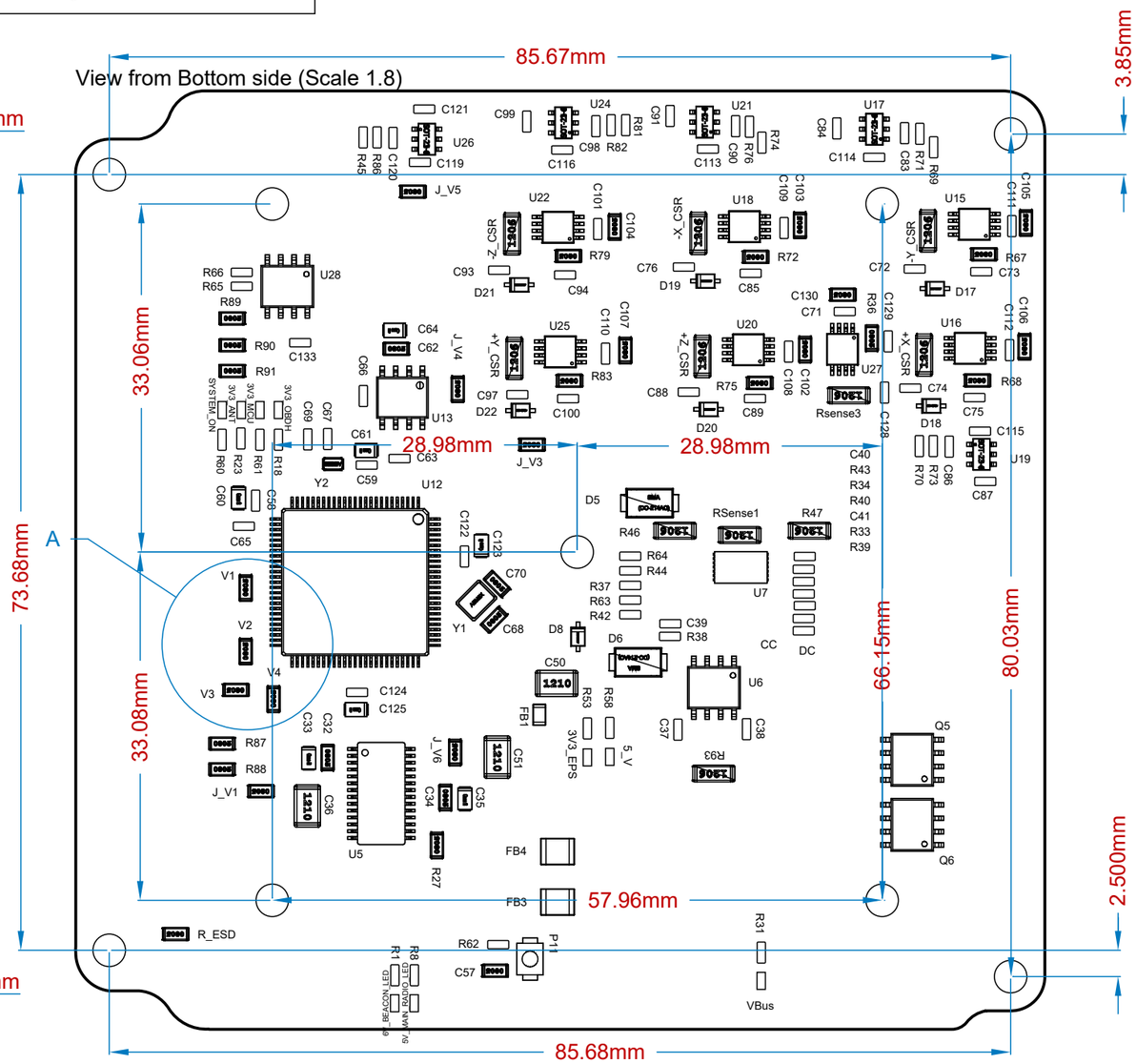
SpaceLab - Federal University of Santa Catarina			
Project: Electrical and Power System 2.0			
Title: Project info and board isometric views			
Designed by: André M. P. Mattos			Project code: EPS2
Date: 2/3/2021	Version: v0.1	Sheet 1 of 3	Sheet size: A4

played components within a red mesh box
not supposed to be soldered in the flight model of the board.

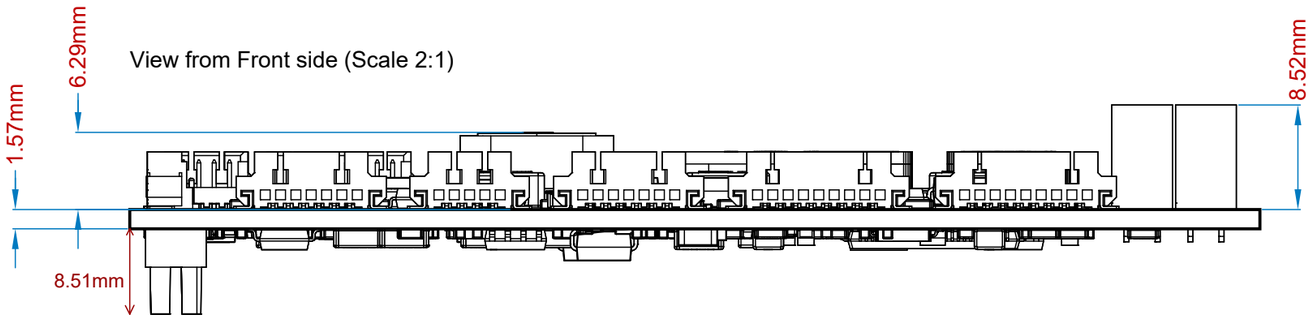
View from Top side (Scale 1.8)



View from Bottom side (Scale 1.8)

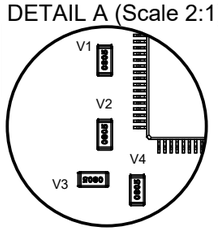


View from Front side (Scale 2:1)



Hardware versioning resistors

DETAIL A (Scale 2:1)



SpaceLab - Federal University of Santa Catarina			
Project: Electrical and Power System 2.0			
Title: Project info and board isometric views			
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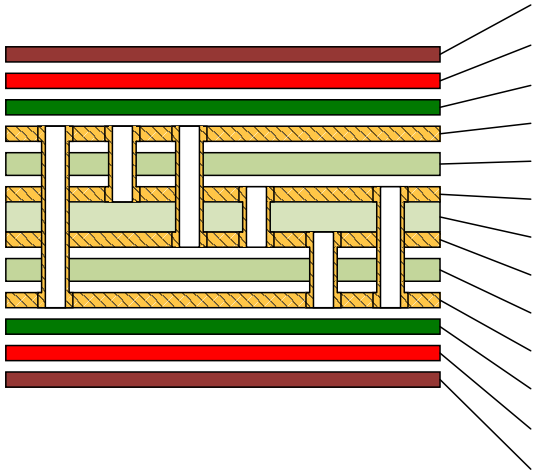
Date: 2/3/2021	Version: v0.1	Sheet 2 of 3	Sheet size: A3

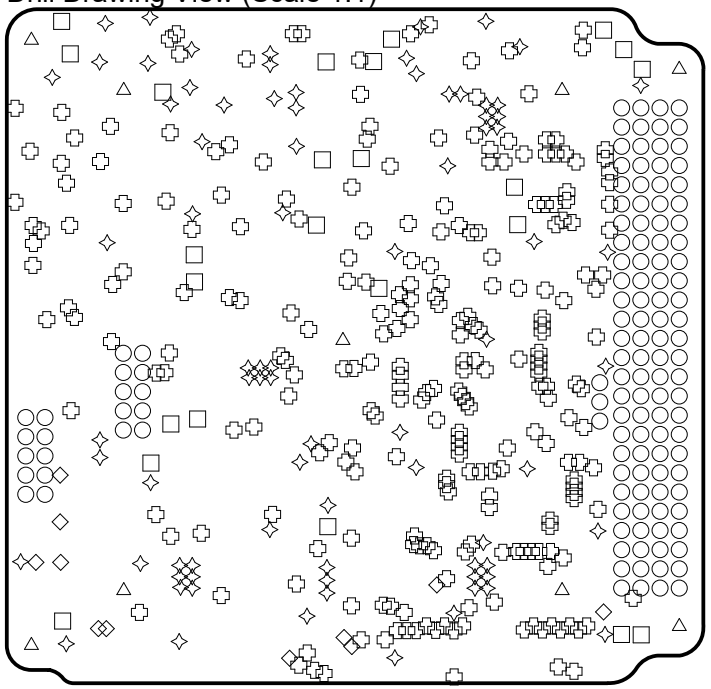
Layer Stack Legend



Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Paste			Paste Mask	GTP
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.01mm	Solder Resist	Solder Mask	GTS
Copper	Top Layer	0.04mm		Signal	GTL
Prepreg		*0.10mm*		*Dielectric*	
Copper	Signal Layer 1	0.02mm		Signal	G1
Core		*1.26mm*	*FR-4*	*Dielectric*	
Copper	Signal Layer 2	0.02mm		Signal	G2
Prepreg		*0.10mm*		*Dielectric*	
Copper	Bottom Layer	0.04mm		Signal	GBL
Surface Material	Bottom Solder	0.01mm	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO
	Bottom Paste			Paste Mask	GBP

Total thickness: 1.59mm

Drill Drawing View (Scale 1:1)



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
✚	255	0.25mm	Plated	None
◇	11	0.38mm	Plated	None
✧	80	0.71mm	Plated	None
○	127	0.90mm	Plated	None
□	24	1.00mm	Plated	None
△	9	3.20mm	Plated	None
	506 Total			

SpaceLab - Federal University of Santa Catarina			
Project: Electrical and Power System 2.0			
Title: Layer stack and drill tables			
Designed by: André M. P. Mattos			Project code: EPS2
Date: 2/3/2021	Version: v0.1	Sheet 3 of 3	Sheet size: A4