

Github repository: https://github.com/spacelab-ufsc/flatsat-platform/

More info about SpaceLab: https://spacelab.ufsc.br/

Project: flatsat\_platform.PrjPcb

Date: 1/4/2021 Version: 0.1

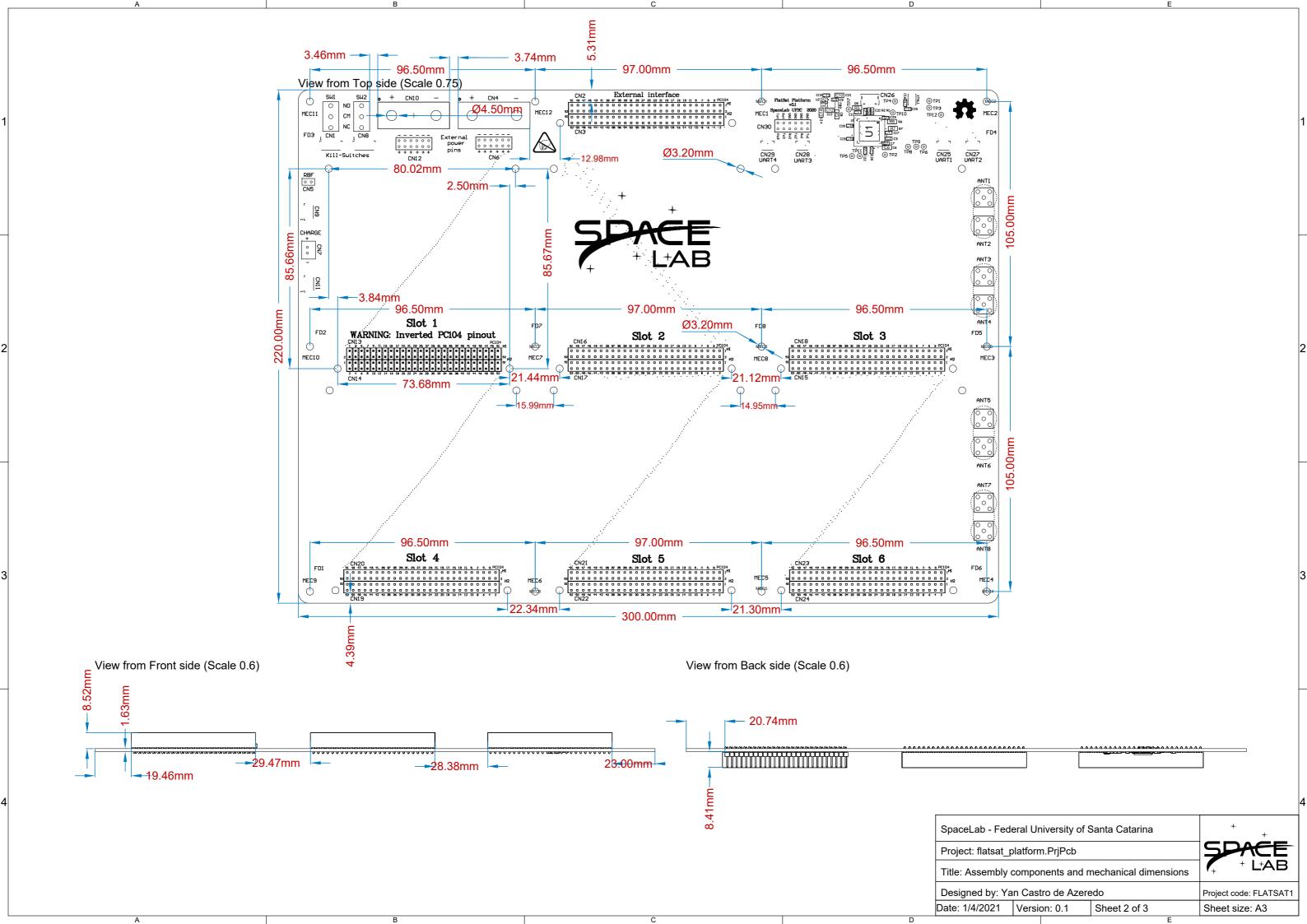
Title: Project info and board isometric views

Sheet 1 of 3

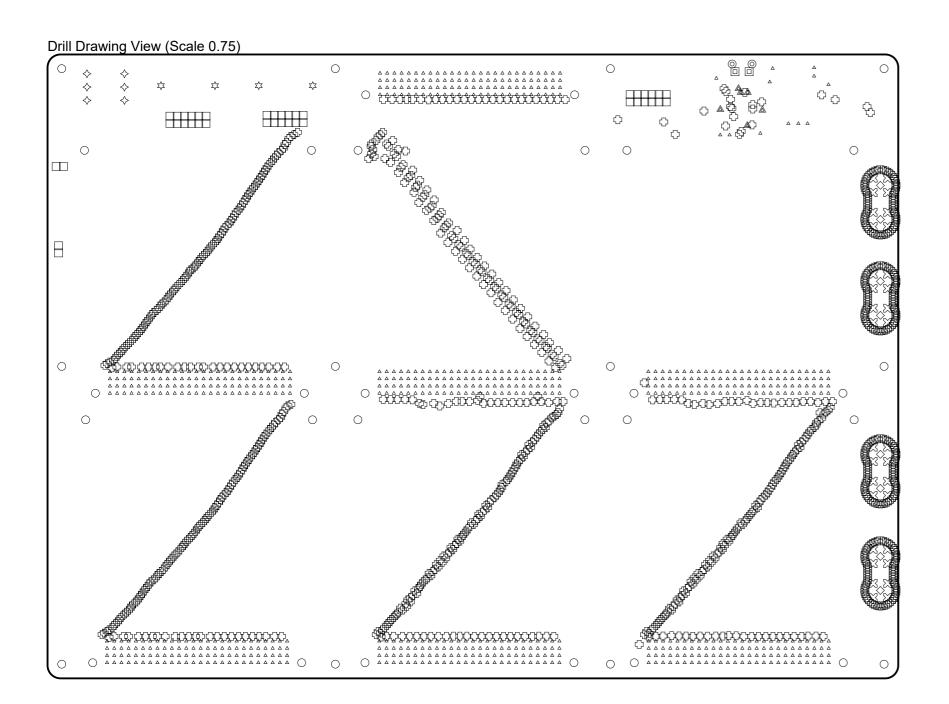
Project code: FLATSAT1

Sheet size: A3

Designed by: Yan Castro de Azeredo



Layer Stack Legend Material Thickness Dielectric Material Type Layer Gerber Top Overlay GTO Legend Surface Material Top Solder 0.01mm Solder Resist Solder Mask GTS GTL Copper Top Layer 0.04mm Signal Prepreg FR-4 Dielectric 0.13mm CF-004 Signal G1 Signal Layer 1 0.04mm FR-4 Dielectric Core 1.20mm 0.04mm Signal G2 Copper Signal Layer 2 0.13mm FR-4 Dielectric Prepreg 0.04mm Signal GBL Copper Bottom Layer Solder Mask GBS Surface Material Bottom Solder 0.01mm Solder Resist GBO Bottom Overlay Legend Total thickness: 1.63mm



## **Drill Table**

Symbol	Count	Hole Size	Plated	Hole Tolerance
¢	1002	0.30mm	Plated	
<b>A</b>	6	0.40mm	Plated	
0	2	0.50mm	Plated	
	2	0.55mm	Plated	
Δ	740	0.90mm	Plated	
	40	1.00mm	Plated	
$\Diamond$	8	1.70mm	Plated	
×	32	1.80mm	Plated	
<b>♦</b>	6	1.90mm	Plated	
0	38	3.20mm	Plated	
欢	4	4.50mm	Plated	
	1880 Total			

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Project: flatsat_	SPACE		
Title: Layer stac	+ LAB		
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