

A

B

C

D

FlatSat Hardware:

- Designed by: Yan C. de Azeredo
- Reviewers: Gabriel M. Marcelino and André M. P. Mattos

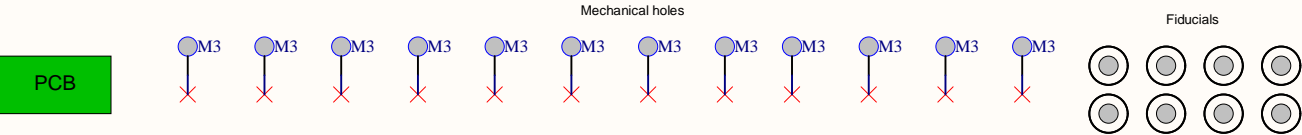
Project repository: <https://github.com/spacelab-ufsc/flatsat-platform>

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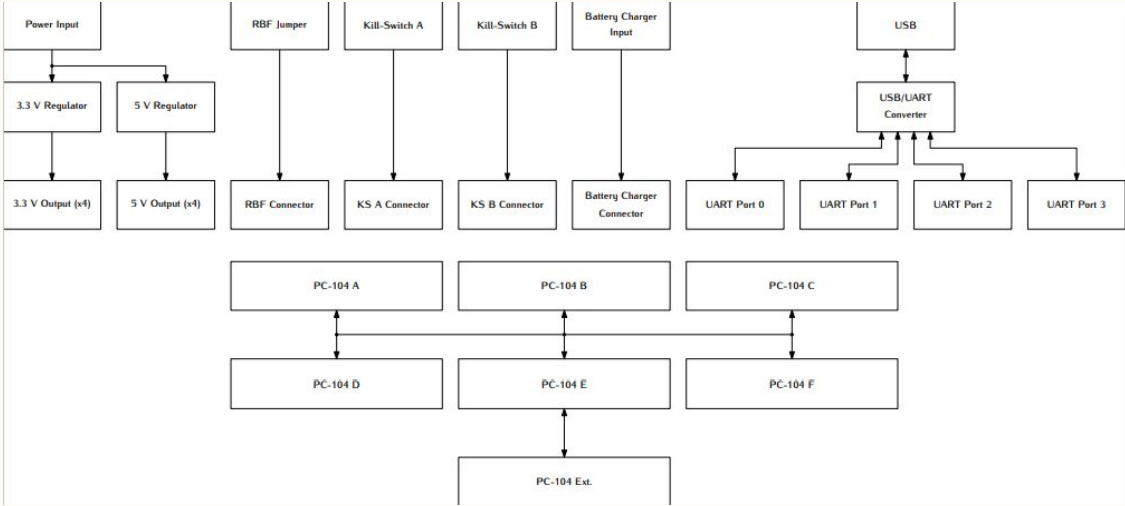
Project Contributions

Rev	Description	Date	Author
0.1	Initial release	11-10-2020	Yan C. de Azeredo

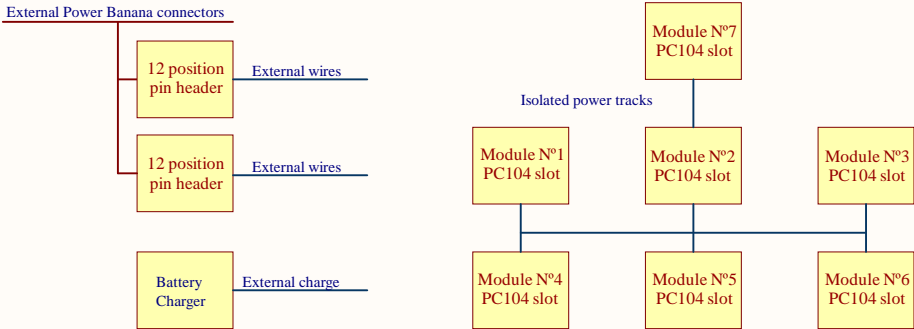
Revision History




PCB Elements



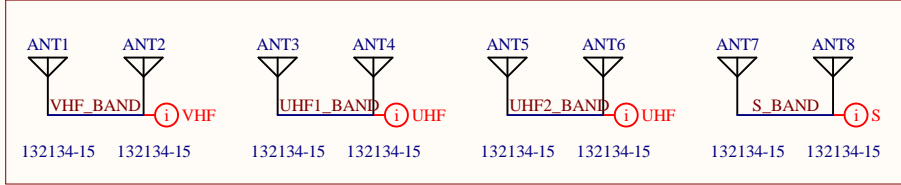
Block Diagram



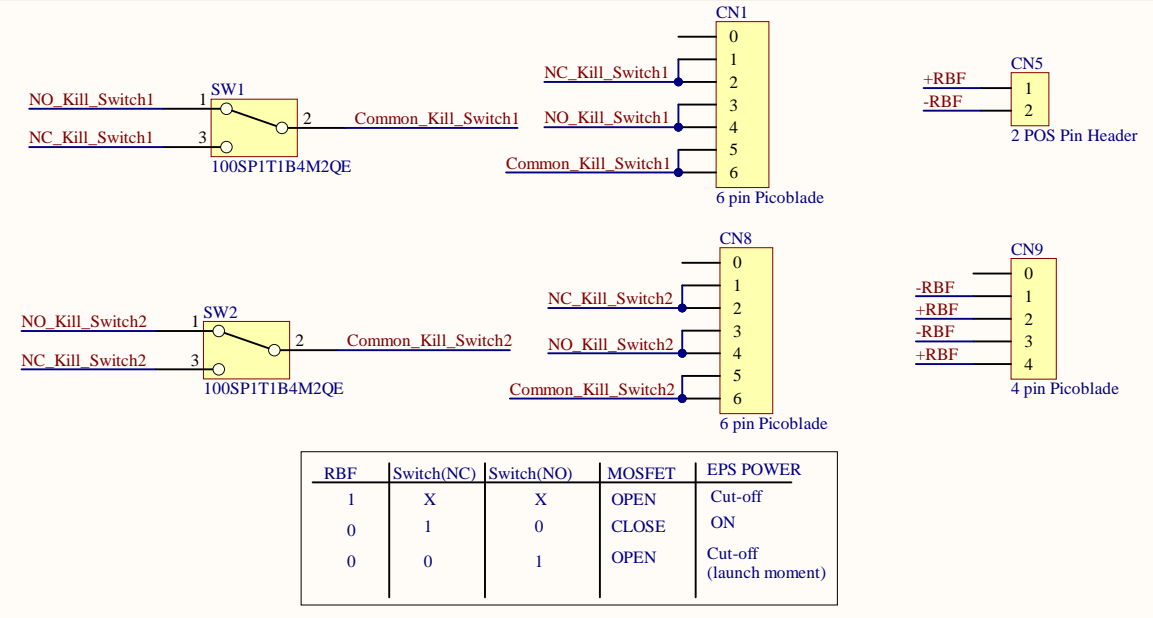
Power Architecture

SpaceLab - Federal University of Santa Catarina			
Project: <i>flatsat_platform.PrjPcb</i> / [No Variations]			
Title: <i>FlatSat Hardware Architecture</i>			
Designed by: <i>Yan Castro de Azeredo</i>			
Date: 11/5/2020	Revision: 1.0	Sheet 1 of 3	Project Code: <i>FLATSAT</i>
			Size: A4

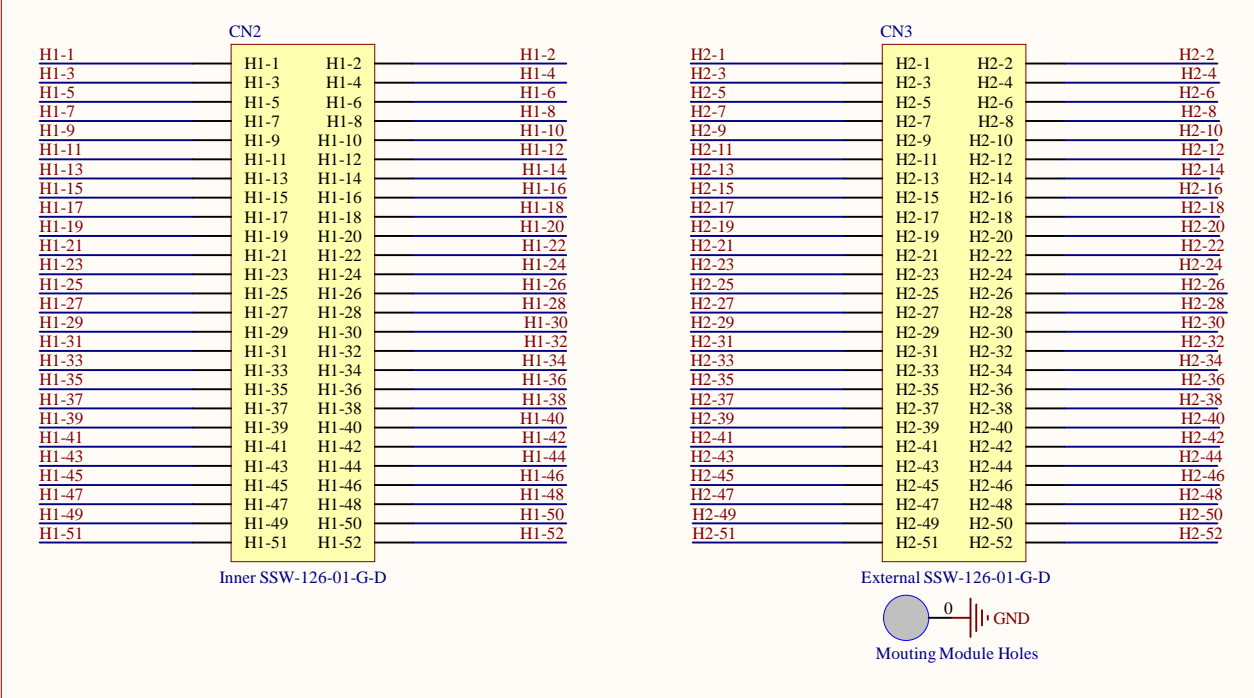
SMA Connectors



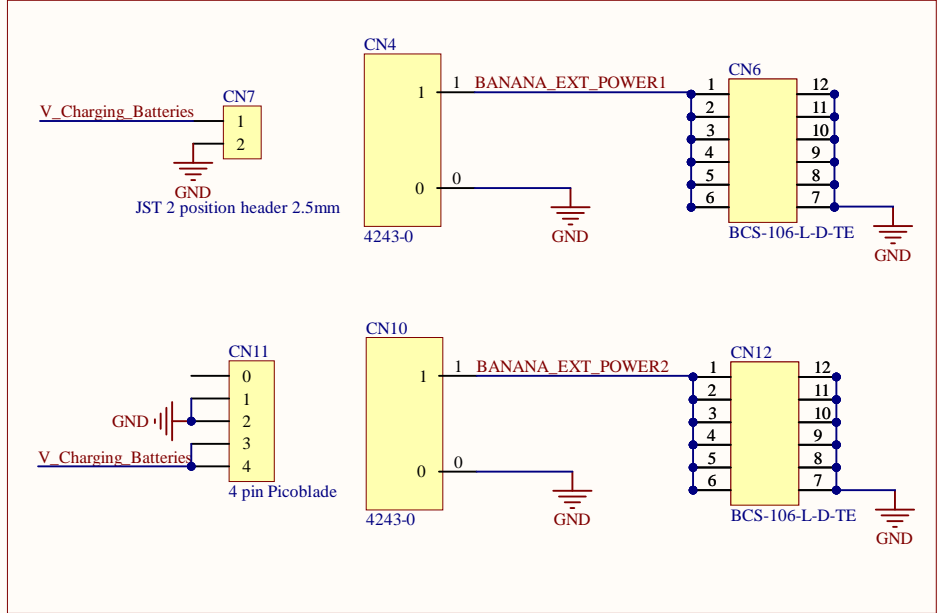
RBF and Kill-Switches



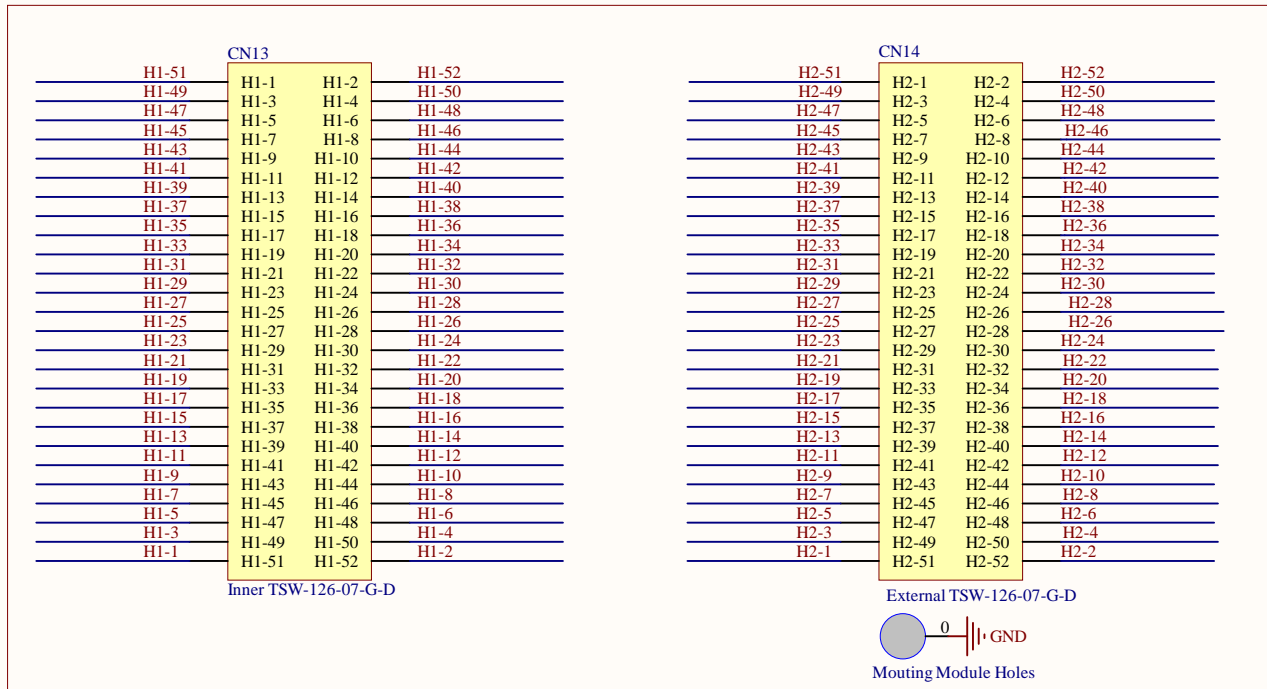
PC104 slot N°7



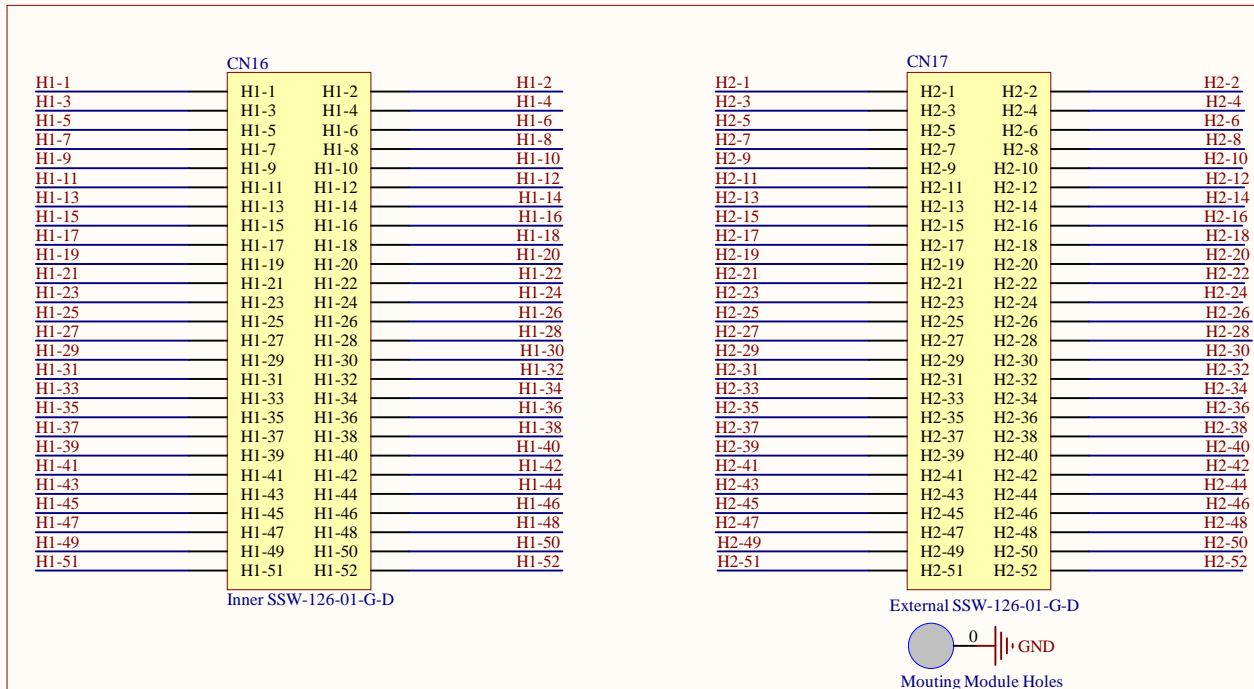
Charge and external power



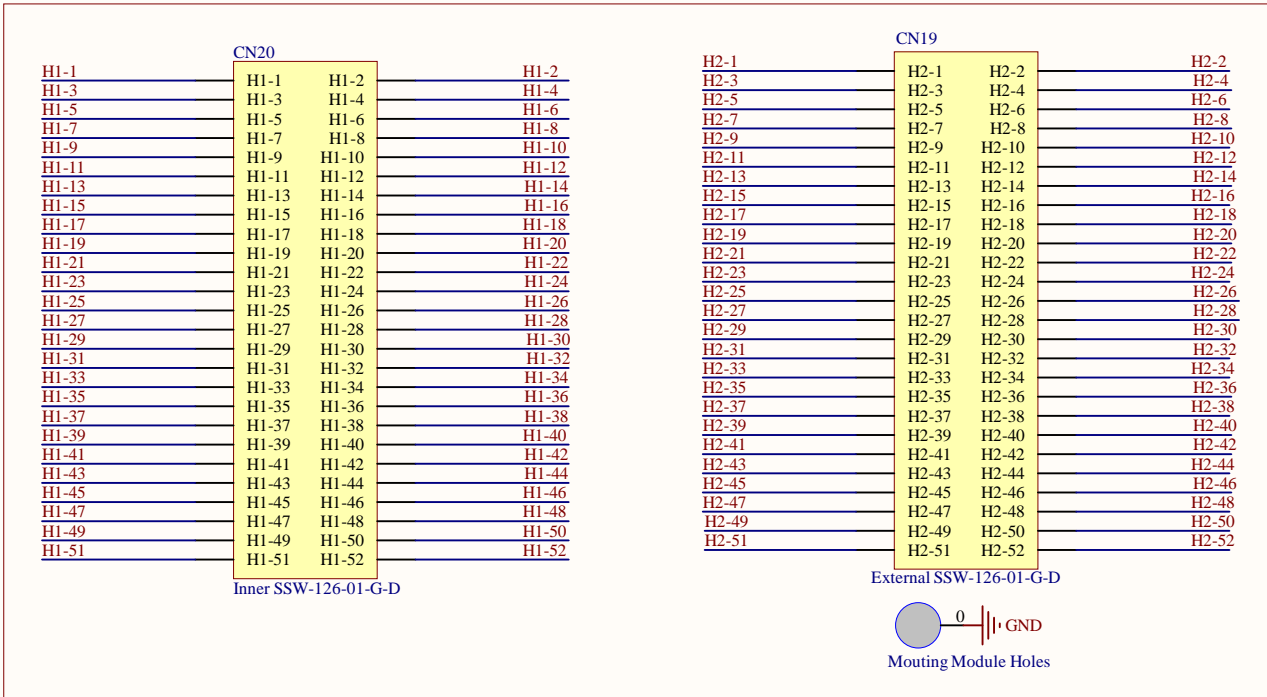
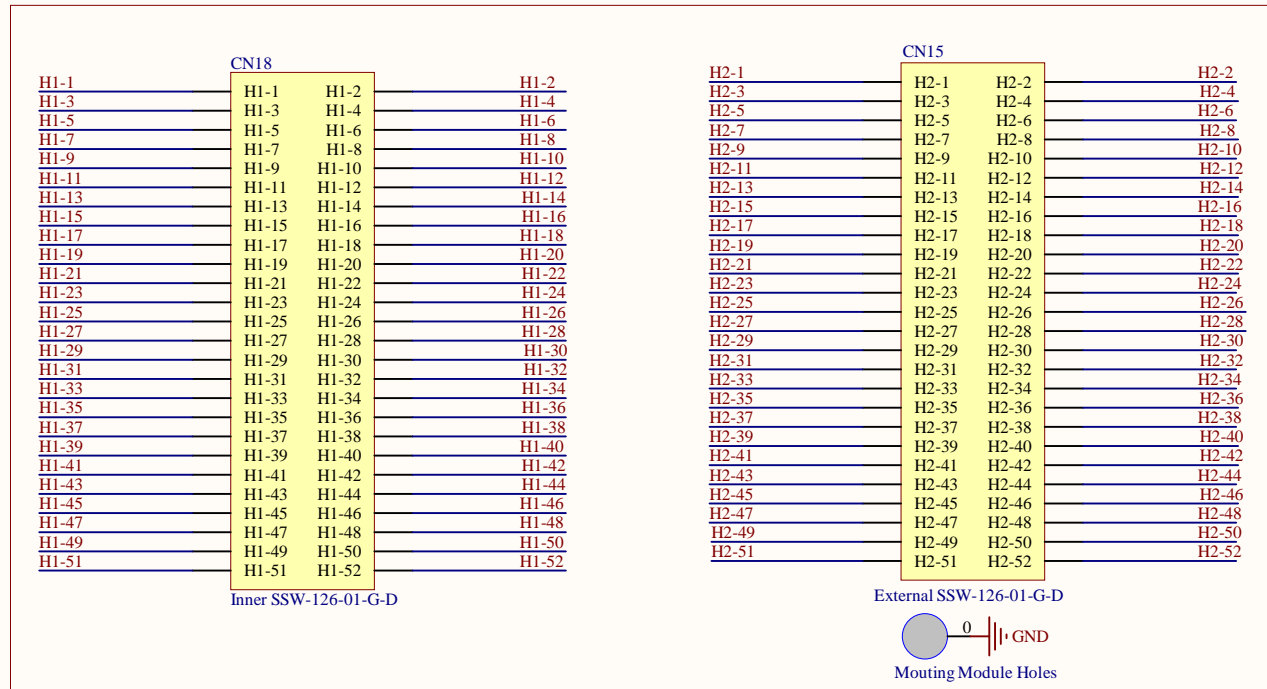
PC104 slot N°1 (inverted pinout)



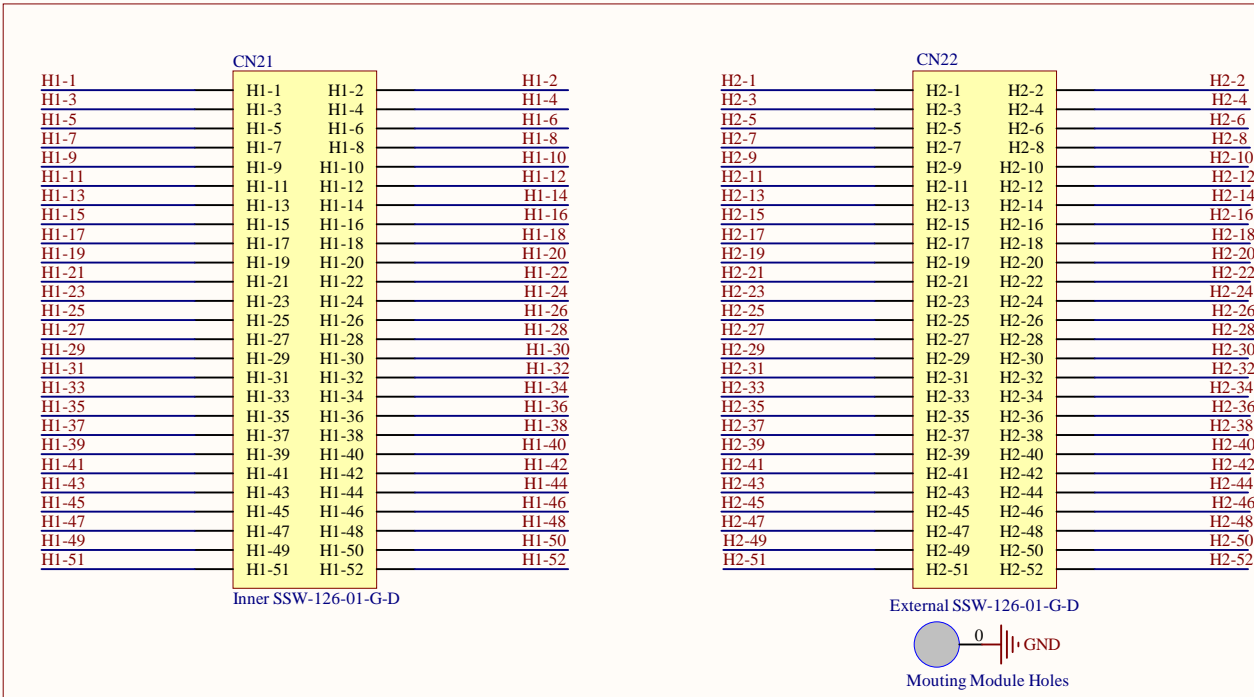
PC104 slot N°2



PC104 slot N°3



PC104 slot N°5



PC104 slot N°6

