

The Evolution of Distributed Systems for Graph Neural Networks and Their Origin in Graph Processing and Deep Learning: A Survey

JANA VATTER, Technical University of Munich, Germany RUBEN MAYER, University of Bayreuth, Germany HANS-ARNO JACOBSEN, University of Toronto, Canada

Graph neural networks (GNNs) are an emerging research field. This specialized deep neural network architecture is capable of processing graph structured data and bridges the gap between graph processing and deep learning. As graphs are everywhere, GNNs can be applied to various domains including recommendation systems, computer vision, natural language processing, biology, and chemistry. With the rapid growing size of real-world graphs, the need for efficient and scalable GNN training solutions has come. Consequently, many works proposing GNN systems have emerged throughout the past few years. However, there is an acute lack of overview, categorization, and comparison of such systems. We aim to fill this gap by summarizing and categorizing important methods and techniques for large-scale GNN solutions. Additionally, we establish connections between GNN systems, graph processing systems, and deep learning systems.

CCS Concepts: • General and reference \rightarrow Surveys and overviews; • Computing methodologies \rightarrow Machine learning; Distributed computing methodologies; • Mathematics of computing \rightarrow Graph algorithms; • Computer systems organization \rightarrow Neural networks;

Additional Key Words and Phrases: Graph neural networks, deep learning systems, graph processing systems

ACM Reference format:

Jana Vatter, Ruben Mayer, and Hans-Arno Jacobsen. 2023. The Evolution of Distributed Systems for Graph Neural Networks and Their Origin in Graph Processing and Deep Learning: A Survey. *ACM Comput. Surv.* 56, 1, Article 6 (August 2023), 37 pages.

https://doi.org/10.1145/3597428

1 INTRODUCTION

Deep Learning (DL) on graph structured data is a promising and rising field. As graphs are all around us [6, 64, 132], they can be used in numerous DL applications to model and analyze complex problems. Due to the differing properties of the input data, common DL architectures such as convolutional neural networks [104] or **Recurrent Neural Networks (RNNs)** [75, 150] cannot

Parts of the work were done while R. Mayer was at Technical University of Munich.

Authors' addresses: J. Vatter, Department of Computer Science, Technical University of Munich, Boltzmannstr. 3, Garching, 85748, Germany; email: jana.vatter@tum.de; R. Mayer, Faculty of Mathematics, Physics & Computer Science, University of Bayreuth, Nürnberger Str. 38, Bayreuth, 95448, Germany; email: ruben.mayer@uni-bayreuth.de; H.-A. Jacobsen, Department of Electrical and Computer Engineering, University of Toronto, 10 King's College Street, Toronto, ON M5S 3G4, Canada; email: jacobsen@eecg.toronto.edu.

Permission to make digital or hard copies of all or part of this work for personal or classroom use is granted without fee provided that copies are not made or distributed for profit or commercial advantage and that copies bear this notice and the full citation on the first page. Copyrights for components of this work owned by others than the author(s) must be honored. Abstracting with credit is permitted. To copy otherwise, or republish, to post on servers or to redistribute to lists, requires prior specific permission and/or a fee. Request permissions from permissions@acm.org.

 $\ \, \odot$ 2023 Copyright held by the owner/author(s). Publication rights licensed to ACM.

0360-0300/2023/08-ART6 \$15.00

https://doi.org/10.1145/3597428

6:2 J. Vatter et al.

easily be used for DL on graphs. Therefore, a new type of **Deep Neural Network (DNN)** architecture was developed in the late 2000s, the so-called **Graph Neural Network (GNN)** [60, 155]. This architecture bridges the gap between graph processing and DL by combining message passing with neural network operations. The field of applications of GNNs ranges from recommendation systems [37, 200], computer vision [48, 154], and natural language processing [103, 199] to biology and medicine [49, 52].

With the ever-growing size of real-world graphs [76] and deeper GNN models [105, 106, 112], the need for efficient GNN training solutions has emerged that aim to process large-scale graphs in a fast and efficient manner. With large datasets containing millions of nodes and billions of edges [76], a high level of parallelization is demanded along with the opportunity to run the computations on distributed architectures. As a result, current research increasingly deals with developing large-scale GNN systems [87, 183, 213]. Multiple issues arise when designing such a system. First, systems originally designed for DL or for graph processing cannot be directly used for GNN training, as the former do not support graph processing operations and the latter do not support DL operations. A vertex in a graph usually only is connected to few other vertices leading to many zero values in the graph adjacency matrix. In contrast, DL operations incorporate high-dimensional features leading to dense matrices. Consequently, both sparse and dense matrix operations need to be supported. Thus, specialized frameworks are being developed incorporating and optimizing both types of tasks. Second, redundant computations and repeated data access might occur during a training iteration. For instance, if nodes share the same neighbor, the neighbors' activation will be computed multiple times in most cases since the computation of the nodes is regarded independently of one another [18, 88]. Thus, the same activation is calculated redundantly. Therefore, the need for appropriate memory management and inter-process communication is enforced. Third, the interdependence of training samples is challenging and leads to increased communication between the machines. One has to decide how to partition and distribute the graph among the machines and which strategy to choose for synchronizing intermediate results.

Although there are many works proposing GNN systems that aim to solve the preceding issues and propose optimization methods, there is an acute lack of overview, categorization, and comparison. There are numerous surveys that classify either scalable graph processing or DL systems but rarely any articles coping with systems for GNNs. Hence, we aim to fill this gap by giving an overview and categorization of large-scale GNN training techniques.

1.1 Related Surveys

On the one hand, there are surveys on graph processing systems. For instance, Batarfi et al. [7] provide an overview of large-scale graph processing systems and present five popular platforms in detail. Additionally, they investigate the performance using selected algorithms and graph datasets. Heidari et al. [70] and Coimbra et al. [27] discuss and categorize graph processing systems in regard to concepts such as partitioning, communication, and dynamism. The preceding papers give a general overview of systems irrespective of the hardware set up; however, Shi et al. [163] focus on graph processing on GPUs. They distinguish between graph processing systems on a single GPU and others with a multi-GPU setting. Another specialized work is done by Kalavri et al. [90], where programming abstractions for large-scale graph processing systems are investigated and evaluated. Gui et al. [62] concentrate on preprocessing, parallel graph computing, and runtime scheduling from the hardware side. Xu et al. [194] further explore hardware acceleration for graph processing and mainly investigate GPUs.

On the other hand, an overview of systems for DNN training is provided in numerous works. Chahal et al. [16] give an overview of distributed training methods and algorithms. The authors also dive into frameworks for distributed DNN training. In their survey, Ben-Nun and Hoefler [8]

give insights into parallelization strategies for DL. They approach the problem from a theoretical angle, model different types of concurrency, and explore distributed system architectures. The survey by Zhang et al. [211] introduces distributed acceleration techniques from the algorithm, distributed system, and applications side. Additionally, the price and cost of acceleration as well as challenges and limitations are presented. Mayer and Jacobsen [124] investigate challenges, techniques, and tools for distributed DL. In their paper, a selection of 11 open source DL frameworks are analyzed and compared. The key aspects of Ouyang et al. [134] and Tang et al. [170] are communication optimization strategies when performing distributed DL. The former highlight algorithm optimizations as well as network-level optimizations for large-scale DNN training, and the latter present communication synchronization methods, compression techniques, systems architectures, and different types of parallelization. Other research focusing on communication optimizations for large-scale distributed DL is done by Shi et al. [162]. Instead of examining the scaling problem from a qualitative perspective, the authors pursue a quantitative approach. Therefore, they conduct experiments with selected communication optimization techniques on a GPU cluster.

To our knowledge, there are many surveys on general aspects of GNNs, like methods, architectures, and applications [17, 67, 189, 209, 215], but only a few study optimizations for GNN training [1, 9, 159]. Abadal et al. [1] provide an overview of algorithms and accelerators for GNN training. They focus on methods from the software side as well as hardware-specific optimizations but do not draw parallels to methods introduced by graph processing systems, which is one of our core contributions. Serafini and Guan [159] also explore scalable GNN training. However, they specialize on the comparison of whole-graph and sample-based training and do not investigate other techniques for large-scale GNN training. Besta and Hoefler [9] provide an in-depth analysis of accelerator techniques for GNN training. Their main contribution lies in the field of parallelism techniques, whereas we give insight into the overall GNN training pipeline, corresponding optimizations, and, most importantly, their origin. In contrast to the all of the preceding works, our focus lies on large-scale systems for GNNs and the corresponding optimization techniques within each step of the training process. Additionally, we provide an overview of the two background technologies—systems for graph processing and systems for DNN training—and draw the connection to GNN systems.

1.2 Our Contributions

This survey examines distributed systems for scalable GNN training. We differentiate ourselves from other surveys not only by presenting methods for GNN systems but also giving insights into the two crucial background topics: systems for graph processing and systems for DNN training. It is important to know about the basic ideas behind those topics to get a better understanding of how and why these techniques are used in large-scale GNN training. We establish connections between the fields and show that most techniques used for large-scale GNN training are inspired by methods either from graph processing or DNN training. This way, we bring together the graph processing, DL, and systems community that are all contributing to distributed GNN training. In addition, we categorize and compare the methods regarding questions of partitioning, sampling, inter-process communication, level of parallelism, and scheduling. This should make it easier for researchers and practitioners to assess the usability of the presented systems and methods to their own application scenario.

1.3 Structure of the Survey

We structure this survey as follows. In Section 2, we provide fundamentals of distributed systems for graph processing and for DL. We discuss the main aspects for achieving parallelism, scalability, and efficiency. In Section 3, we relate the insights from graph processing systems and DL systems

6:4 J. Vatter et al.

to facilitate the comprehension of methods used by systems for GNN training. Finally, open challenges, limitations, and research trends are presented in Section 4.

2 FOUNDATIONS

This section describes relevant topics building the basis of distributed GNN training. We start by giving a short introduction to general graph processing followed by presenting selected graph processing systems. Furthermore, we explore the basic steps of neural network training and how the training process can be realized in a distributed fashion.

2.1 Graph Processing

A graph G is a non-linear data structure, meaning that the data elements are not sequentially or linearly ordered. It can be formally described as G = (V, E), where V denotes the set of vertices and E the set of edges. A vertex v, also known as the node, represents an object, and the edge e describes a relation between two vertices. For example, a vertex can represent a person and an edge models the relationship between two persons. There are many different types of graphs (e.g., the graph can contain cycles (cyclic graph) or no cycles (acyclic graph)), the edges can be directed or undirected, and a weight can be assigned to each edge signifying its importance (weighted graph). Depending on the density of edges within the graph, it can be sparse, dense, or fully connected.

Graphs occur in numerous domains, including social networks [35, 64, 169], route optimization and transportation [28, 165], natural language processing [120, 132], recommendation systems [63, 82, 164], and the natural sciences [6, 122, 142]. For instance, a social network graph models the relation between users; graphs in natural language processing represent the relations between words or sentences; and in the natural sciences, graphs help to model the constitution of molecules.

Graph processing algorithms explore properties of the vertices, relations between them, or characteristics of a subgraph or the whole graph. Depending on the use case, different types of graph problems need to be solved by those such as traversals, component identification, community detection, centrality calculation, pattern matching, and graph anonymization [27, 33]. Algorithms like depth-first search [171], breadth-first search [14], Dijkstra [31], the Girvan-Newman algorithm [57], and PageRank [137] are among the most famous to tackle the presented problems.

2.2 Distributed Graph Processing

To process large graphs and to accelerate computation, graph processing is parallelized and distributed across machines. One of the most important methods to process data in parallel is the MapReduce computing model [30]. MapReduce incorporates two user-defined primitive functions: map and reduce. The map function takes a key-value pair as input and processes it in regard to the specified functionality. All intermediate results are grouped and passed to the reduce function, which merges them to obtain a smaller set of values. A master node is responsible for assigning the map and reduce functions to the worker nodes. Each map worker takes a shard of the input pairs, applies the given function, and stores the intermediate results locally. The reduce workers load the intermediate files, apply the reduce function, and write the results to output files. With the help of the MapReduce computing model, large amounts of data can be handled; however, it reaches its limits when employing it for graph processing. A reason for MapReduce being unsuitable for graph processing is that the vertices within a graph are not independent. When performing a computing step of a graph algorithm, knowledge about multiple vertices is needed, leading to time- and resource-consuming data accesses. Graph processing algorithms may perform multiple iterations; however, the MapReduce model is optimized for sequential algorithms [129].

Therefore, the Pregel system [119] was developed, which specializes on parallel graph processing. It supports iterative computations more efficiently than MapReduce and keeps the dataset in

			Partit	ioning	Executi	on Mode	Message Propagation		
Year	System		Edge-cut	Vertex-cut	Synchronous	Asynchronous	Push	Pull	
2010	Pregel	[119]	√		✓		√		
2012	Apache Giraph	[44]	√		✓		√		
2012	GraphLab	[114, 116]	√			✓		√	
2012	Distributed GraphLab	[115]	√			✓		√	
2012	PowerGraph	[58]		√	✓	✓		✓	
2013	GPS	[152]	✓		✓		√		
2013	GRACE	[177]	√			✓	√		
2015	PowerLyra	[21]	✓	√	✓	✓		✓	

Table 1. Categorization of Different Distributed Graph Processing Systems

memory to facilitate repeated accesses. Further, a vertex-centric programming model is provided, allowing the user to express graph algorithms more easily. Thus, the user thinks of how the algorithm is modeled from the perspective of a vertex rather than dataflows and transformation operators. During the process, messages are passed between the vertices in a **Bulk Synchronous Parallel (BSP)** [175] manner. Hence, the messages are transmitted synchronously after all machines have finished their computation step. The iterative model works as follows: in each iteration, called *superstep S*, a user-defined function is executed in parallel for each vertex v. Aside from computing the vertex value, the function can pass messages to other vertices along outgoing edges during an iteration. In superstep S, it can send a message that will be received in superstep S+1 while messages sent to v in superstep S-1 can be read. Similar to MapReduce, Pregel uses a reduction mechanism called *aggregator* to combine resulting values and make them available in the next superstep S+1. The introduced synchronous superstep model can be used for various graph algorithms and is the basis for subsequent systems [13, 94, 152].

The GAS (Gather-Apply-Scatter) model used in PowerGraph [58] follows the four steps of gather, sum, apply and scatter. The gather and sum operations resemble the map and reduce scheme aiming at assembling information about the vertices' neighborhood. This phase is invoked on the edges adjacent to the vertex and executed locally on each machine. The results are sent to the *master*, which runs the apply function with the aggregated information. It sends the resulting updates to all machines that execute the scatter phase.

Following the two programming models, a general distributed iterative vertex-centric graph processing scheme can be described. Before starting the iterative computation, the graph is split into partitions that are distributed across the machines. After that, the iterative process starts. Each vertex aggregates messages and applies a given function to compute a new state. Subsequently, the vertex state is updated. Then, the vertex passes messages to adjacent vertices containing the updated information and the aggregation phase starts over.

The vertex-centric paradigm lets the user intuitively define computations on graphs, but it reaches its limitations when processing real-world graphs with skewed degree distributions, large diameters, and high densities [196]. Therefore, block-centric models have been developed [38, 173, 196]. Instead of sending messages from a vertex to its neighbors, blocks composed of multiple vertices send messages to other blocks. Inside the blocks, information moves freely [173], resulting in reduced communication cost and improved performance.

Based on this general iterative model, we present and categorize selected distributed graph processing systems. We start by discussing partitioning strategies and how to store the resulting subgraphs. We distinguish between the synchronization method used in the message propagation phase and show how the messages can be transmitted. Table 1 summarizes the most important properties of the selected systems and categories.

2.2.1 Sampling. Graph sampling is a preprocessing step aiming to make the graph sparser by removing vertices or edges to reduce processing time and memory consumption. A key challenge

6:6 J. Vatter et al.

here is to ensure that important graph structures are preserved. Iyer et al. [84] calculate a probability signifying whether an edge between vertex a and vertex b should be kept in the graph or not. The probability value depends on the average degree of the graph, the out-degree of vertex a, the in-degree of vertex b, and the level of sparsification s, which can be chosen by the user. In this manner, less memory is needed while preserving the most important structures within the graph. The fast, approximate ASAP engine [83] for graph pattern mining consists of two phases, namely the sampling and closing phase. In the sampling phase, the graph is treated as a stream of edges. The edges are either randomly selected or depending on the previously streamed ones. Then, the closing phase awaits certain edges to complete patterns. This technique helps to preserve certain structures within the graph while excluding certain edges. An application-aware approach that drops certain messages is proposed by Schramm et al. [157]. In every superstep, a given percentage of messages is identified as least important and dropped on-the-fly. The calculation of the importance value is dependent on the desired application and the deployed graph algorithm. Therefore, this method can be used in a plethora of applications.

2.2.2 Partitioning. There exist numerous approaches on graph partitioning algorithms that the systems adopt and refine. Those algorithms either follow the edge-cut [92, 166, 174], vertex-cut [68, 123, 125, 127, 140, 156], or hybrid-cut model [21, 39]. Further, we distinguish between offline and online partitioners. Offline partitioners [71, 91] determine the partitions according to the whole graph; however, online partitioners [123, 140, 174] stream vertices or edges and assign the vertex or edge to a partition on-the-fly. An interesting approach is HEP [125], where offline and online components are combined into a hybrid scheme.

To assess the quality of the partitions, metrics like replication factor, communication cost, and workload balancing are used. The *replication factor* indicates the ratio between the number of replicas and the total number of vertices. Based on that, the *communication cost* can be determined. Whenever an edge is cut, communication between the partitions is needed. When using an edge-cut method for example, the communication increases proportional to the number of edges that are cut. The so-called *workload balancing* aims to partition the graph in a way such that during the computation phase of the graph processing algorithm, the load is balanced among the workers.

There exist various cost functions that help to form partitions. Depending on the goal and the application, the cost function needs to be defined differently. In the LDG (Linear Deterministic Greedy) [166] method, a vertex is allocated to the partition where it has most edges. This is combined with a penalty function indicating the capacity of a partition. Consequently, communication costs are minimized and balanced workloads across partitions is ensured. FENNEL [174] unifies two heuristics: a vertex is either assigned to the cluster that shares the largest number of neighbors or the one with the smallest number of non-neighbors. This results in a minimal number of edges that are cut and consequently minimal communication costs. The HDRF (High Degree (are) Replicated First) [140] method handles graphs with skewed distribution where the degree of the nodes highly varies. The goal is to balance the load evenly by cutting and replicating high-degree vertices. 2PS [126] gathers clustering information in a preprocessing phase, which is then used in the scoring mechanism. Instead of defining one particular cost function, Fan et al. [39] use an application-driven approach. A cost model based on a given application algorithm is minimized to partition the graph. This results in an adaptive partitioning strategy suitable for numerous use cases.

Pregel [119] uses one of the simplest methods to initially partition the graph. Here, the partitioning is based on the vertex ID using a hash function $hash(ID) \ mod \ N$, where N is the number of partitions. The partitions are then evenly distributed across all workers. The underlying partitioning principle is called edge-cut as the edges are cut and replicated while the vertices are assigned

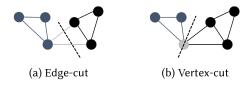


Fig. 1. Edge-cut and vertex-cut partitioning.

to the different machines (Figure 1(a)). Other systems relying on edge-cut partitioning are Apache Giraph [44], GraphLab [114, 116], Distributed GraphLab [115], **Graph Processing System (GPS)** [152], and GRACE [177]. Compared to other partitioning strategies, the presented edge-cut methods induce less computation cost and fewer overheads. Conversely, the following methods aim to reduce the cut size at the cost of higher runtimes and memory overheads. However, the obtained partitions are of improved quality.

With edge-cut partitioning, the number of vertices is balanced across partitions; however, the number of edges per partition might highly vary. To obtain more equal partitions and to improve distributed processing of natural graphs, Gonzalez et al. [58] propose balanced vertex-cut partitioning (Figure 1(b)) in their PowerGraph model. Natural graphs typically are imbalanced and thus difficult to partition. Some vertices have a high degree of outgoing edges, whereas other vertices are of low degree. Hence, the computation costs per partition vary a lot. For that reason, the system employs balanced vertex-cut partitioning. The novel partitioning strategy improves the processing of natural graphs by equally distributing the edges between the machines, whereas adjacent vertices are mirrored on the machines. One vertex among all replicas is randomly assigned as the *master* to compute and update the vertex state. All *mirrors* keep local copies of the vertex state.

A dynamic repartitioning strategy to reduce communication is introduced in GPS [152]. First, the system partitions the input graph with a standard partitioning technique. During the execution of the graph processing algorithm, communication is observed to determine which vertices to reassign to another worker and when to do so. Messages that are passed over the network are significantly reduced because repeated vertex accesses are performed by workers that already loaded the vertex in a previous iteration. Therefore, processing time is sped up, the workload is balanced, and scaling to larger graphs is improved.

PowerLyra [21] further optimizes the processing of natural graphs by introducing a hybrid-cut partitioning algorithm that combines edge- and vertex-cut. Furthermore, it handles high-degree vertices and low-degree vertices separately to minimize the replication of edges and vertices. The proposed balanced p-way hybrid-cut algorithm exploits a form of edge-cut if a vertex has a low number of outgoing edges and vertex-cut if a vertex is of high degree.

GridGraph [216] introduces a grid representation for graphs to speed up partitioning. First, the vertices are partitioned into P chunks where each chunk contains connected vertices. The edges are sorted into the resulting $P \times P$ grid according to their source and destination vertices. In this method, the list of edges does not need to be ordered, leading to small preprocessing times. Further, the grid representation can facilitate the execution of the following graph processing steps.

As there are numerous partitioning strategies with different characteristics and objectives, it is challenging to choose the optimal one for a given application. Therefore, some experimental studies investigate the performance and resource usage of different strategies [3, 55, 136], whereas the EASE system [131] provides a quantitative prediction and automatic selection.

2.2.3 Inter-Process Communication. Once the graph has been partitioned, it has to be decided how the worker processes synchronize their data (e.g., by sending messages or by accessing shared memory). In the former case, the systems store the subgraphs locally on the assigned machines

6:8 J. Vatter et al.

and exchange synchronization messages. GraphLab [114, 116] supports the latter case and uses a shared-memory abstraction. A data graph accessible for all workers stores the program state as well as the corresponding data structures. Oriented on GraphLab, PowerGraph [58] also performs computation following a shared-memory view.

2.2.4 State Synchronization and Scheduling. When iteratively executing graph processing algorithms, the computation steps can be performed following a synchronous, asynchronous, or hybrid scheme. The underlying basic principle Pregel follows is the BSP model. Every node goes through an iteration by aggregating and combining the desired features and subsequently updating the state. When the node has finished the computation step, it waits for all other nodes to finish before continuing with the next iteration. This assures that every node shares the same parameters.

Although the synchronous mode works well in a lot of cases, it can be inefficient in other cases. An example is the label propagation algorithm for community detection [141]. Here, each vertex is assigned an initial label. In each iteration, the vertex adopts the label the maximum of neighbors has. After some propagation rounds through the network, dense communities consent to the same label. If the synchronous mode is chosen and the graph is bipartite, meaning that each vertex of one subgraph connects to each vertex in another subgraph, the labels might oscillate and change after each iteration. This makes it impossible for the algorithm to terminate, as the labels therefore need to be stable. To solve this issue, asynchronous execution is used. Asynchronous processing means that vertices can already read state updates of the current iteration in addition to the updates of the previous iterations. In GNN training, the asynchronous mode has the ability to prioritize specific state updates, which results in faster convergence of the overall computation. Another important benefit is the avoidance of long idle times through stragglers.

GraphLab [114, 116] uses an asynchronous execution method. Three steps are performed independently by each worker: fold, merge, and apply. In the fold step, the data across all vertices is gathered. If provided, a merge operation is performed. Otherwise, the apply function directly completes the computation and stores the data in shared memory. This process is done without regarding the phase of computation the other workers are currently passing. GRACE [177] allows for high-performance execution by adapting the BSP model to permit asynchronous processing. Hence, idle times are minimized, but it is possible that the workers perform their iteration with stale data. As both synchronization methods have their benefits and drawbacks, PowerGraph [58] allows the user to either choose a synchronous or asynchronous mode. The synchronous mode is performed analogously to the synchronous execution in Pregel, and the asynchronous mode resembles the GraphLab computation model.

Usually, choosing a mode manually requires the user to deeply understand the graph processing system and often does not lead to the optimal performance. Hsync [190] removes this issue by automatically switching between the synchronous and asynchronous mode according to a set of heuristics. The heuristics aim to predict the performance of the current mode and determine the step at which a switch to the other mode can be beneficial. In the AAP (Adaptive Asynchronous Parallel) model [36], each worker decides on its own when to start the next computation step depending on two parameters. The first parameter is the relative progress of a worker compared to the other ones. The second parameter indicates data staleness. Consequently, stragglers are avoided while also reducing stale computations.

PowerLyra [21] goes a step further and not only provides both synchronization modes but also distinguishes between high- and low-degree vertices to determine how they are processed. The former are processed based on the GAS model (i.e., gather, apply, scatter) [58]. The master vertex activates the mirrors to execute the gather function, and the results are sent back to the

master. After having received all messages, the master runs the apply function. A combined message with the updated data and the activation for the scatter function is sent from master to mirror. In contrast to the original GAS model, PowerLyra combines the apply and scatter messages from master to mirror vertices to minimize communication. The system handles the low-degree vertices similar to GraphLab. The master vertex performs the gather and apply phase locally. Hereafter, activation and update messages are combined and sent to the mirrored vertices. Each mirror then performs the scatter phase. Because of the adapted scheme, replication of edges is eliminated, and in each iteration, only one message needs to be sent by a mirror.

Opposed to the synchronous mode, using an asynchronous execution implies the need for a scheduling scheme that can influence the convergence of the overall computation. Additionally, a high level of concurrent execution can be achieved as scheduling helps to determine the order of the tasks and assigns the tasks to the machines. GraphLab [114, 116] provides the so-called *set scheduler*. Based on the dependencies between the tasks, an *execution plan* is established and an overall speedup of the computations can be observed. In their GRACE system, Wang et al. [177] incorporate a customizable scheduling policy. The system loosens the restrictions of the BSP model and lets the user prioritize specific vertices for faster convergence. One can choose an individual set of vertices and also the desired processing order of those. Then, the system calculates a *scheduling priority* for each vertex to determine the overall execution order.

2.2.5 Message Propagation. Regardless of the execution mode and scheduling technique, messages need to be transmitted between the vertices containing the updated states. There are two main methods to transfer messages: push and pull. Pregel-based systems[44, 119, 152, 177] use the push-operation, whereas systems supporting the GAS model [21, 58, 114–116] rely on pulling the messages. After each iteration, Pregel-based systems synchronously propagate the update messages. All vertices simultaneously push their messages, meaning that each vertex directly sends a message to all of its adjacent vertices containing the updated state. GraphLab [114, 116], however, stores the data graph with associated features in shared memory. Like this, all workers are able to access the data any time. At the beginning of an iteration, the worker pulls the current data graph via the gather operation to perform calculations on the most recent features. At the end of an iteration, the worker updates the data graph with the newly computed state.

2.3 Distributed Neural Network Training

A neural network consists of a number of connected nodes, called *neurons*, organized in one or multiple layers. Each neuron takes an input and processes it in regard to given weights and an update function. When having computed the new value, an activation function [143, 160] decides how important the output value is. The values are passed through the network until the last set of neurons is reached, called *forward pass*. After that, a loss function is computed in regard to the calculated and expected output. To minimize the loss, the weights need to be adapted. The backpropagation [149] algorithm is used to adjust weights in a backward pass through the network. A widely used technique to do so is stochastic gradient descent (SGD) [145, 148]. It computes the partial gradients by considering the calculated loss. With the help of those gradients, the weights are adjusted. This is called *backward pass*. The whole process, forward and backward pass, is iteratively repeated until convergence or a maximum number of iterations is reached. The final neural network weights can be used to make predictions on unseen data [59, 102, 186].

With the growing amount of training data and the increase of model size, distributed neural network training has become necessary. In the following, we discuss different techniques to scale neural network training with regard to parallelism, execution mode, and coordination. Table 2 gives an overview of the described systems and techniques.

6:10 J. Vatter et al.

		Parallelism		Synchroni	zation Mode	Coordination		
Year	System		Data	Model	Synchronous	Asynchronous	Centralized	Decentralized
2012	DistBelief	[29]	✓			✓	✓	
2014	Project Adam	[25]		√		✓	✓	
2015	MALT	[107]	✓			✓		✓
2016	TensorFlow	[2]	✓	✓	✓	✓	✓	
2016	Ako	[184]	✓			✓		✓
2019	CROSSBOW	[99]	✓		✓			✓
2019	PyTorch	[138]	✓	√	✓	√	✓	✓

Table 2. Categorization of Systems for Distributed Neural Network Training

Parallelism. Data parallelism allows for parallel training on multiple processors. Therefore, the data is divided into a fixed number of subsets. Each worker is assigned a subset that it processes on a local replica of the model. After that, the resulting model parameters are exchanged with the other workers. The next iteration is performed with the updated parameter configuration. This process is repeated until convergence. Systems using data parallelism are, for instance, MALT [107] and Ako [184]. If the model itself is too big to fit on a single machine [12, 100], it is split up. In model parallelism, the computation nodes process the whole dataset on their partition of the model. After having finished the computation, the intermediate output of the forward pass is passed to the machines responsible for computing the subsequent layer. Here, scheduling is important to efficiently coordinate the training process. The most intuitive way to partition the model is layerwise, meaning that each layer is assigned to one node. However, this sometimes does not benefit parallelism, as the worker controlling the current layer needs to wait for the worker handling the prior layer to finish before being able to run their computation. A system exploiting model parallelism is Project Adam [25], where each machine is assigned a certain part of the model. Another possibility to distribute the model more intelligently is according to its specific architecture. Suitable could be architectures like the two-fold Siamese network [69] where some of the components can be easily run in parallel.

The two techniques are combined (e.g., by PipeDream [133] and GPipe [81]) into the so-called hybrid or pipeline parallelism. Here, the data as well as the model are shared among the workers. In contrast to PipeDream, the GPipe algorithm further splits the input mini-batches into chunks to maximize the number of concurrent calculations within an iteration. In general, the use of hybrid parallelism can significantly improve training speed in comparison to data and model parallelism.

2.3.2 Synchronization Mode. When training neural networks in a distributed fashion, it is important that the machines regularly exchange parameter updates or intermediate results to ensure convergence. This can either follow a synchronous, asynchronous, or hybrid mode [20, 29, 89]. When using a synchronous mode, the updates are sent simultaneously to the other nodes as soon as all machines have finished their computation. Then, the nodes continue with the computation of the next iteration. In this manner, every machine is always aware of the current parameters. A downside of this approach is that a single straggler decreases the speed of the whole training process [26]. A system following the synchronous execution mode is CROSSBOW [99].

Models using asynchronous execution [25, 29, 107, 184] eliminate the problem of stragglers by not waiting for all workers to finish. Instead, the updates are sent as soon as they are available. Thus, the training speed is increased and the resources are efficiently used. A drawback of this method is that the worker nodes might not always be up to date, therefore computing their updates on stale parameters. The computation of gradients on outdated parameters can lead to a slower or no convergence at all, which is called the *stale gradients problem* [34]. An attempt to merge the synchronous and asynchronous execution scheme was made by Ho et al. [72]. Their model is

based on synchronous execution, but it incorporates a staleness threshold determining how many timesteps two workers may be apart until the faster worker needs to wait for the slower worker to finish its current computation. In contrast to synchronous execution, this decreases the impact of stragglers while also limiting the staleness of the parameters to ensure up-to-date computation. Popular frameworks like TensorFlow [2] and PyTorch [138] leave the choice of synchronization level to the user. Here, both methods are supported.

2.3.3 Coordination. Besides determining the synchronization mode, it needs to be decided how the parameters are stored and the updates are coordinated. Common ways to do so are in a centralized or decentralized manner. The centralized method operates with a global parameter server that stores, aggregates, and distributes the relevant updates. Consequently, all machines share the same set of parameters. However, the use of a parameter server introduces a single point of failure, as it is part of all update requests. DistBelief [29] and Project Adam [25] are systems operating in a centralized fashion. Decentralized systems [99, 107, 184] eliminate the need for a parameter server by passing the update messages directly from machine to machine by using a collective communication primitive such as an all-reduce operation [153]. Here, each machine exchanges update information with its peers and combines the received parameters with its own. As a result, each machine holds the latest set of parameters. An advantage of this approach is that the computation of updates is balanced among all machines [99]. As both strategies have their benefits and drawbacks, PyTorch [138] leaves the choice of coordination to the user.

To conclude, there are several ways to perform distributed DNN training. Depending on the architecture and the data, one might choose between data, model or hybrid parallelism, synchronous or asynchronous updates, and a centralized or decentralized system. In all methods, parameters need to be exchanged between the machines. Therefore, communication is a bottleneck that needs to be addressed when training neural networks in a multi-machine setting. In addition, it is important that the resources are fully utilized without long idle times [208].

3 SYSTEMS FOR GNNS

3.1 GNN Basics

The term graph neural network initially emerged in a work by Gori et al. [60] and was further investigated by Scarselli et al. [155]. It refers to neural network architectures that do not take multiple independent or sequenced data samples as input, like convolutional neural networks [104] or RNNs [161], but rather graphs. In contrast to images or text, graphs do not follow a specific structure and are not sequentially ordered. A graph G can be formally denoted as G = (V, E). It consists of a set of vertices V and a set of edges E. A vertex v represents an object and is also known as node. An edge e describes the relation between two vertices. As graphs are unstructured, it is necessary to employ a new type of neural network: the GNN. They combine DNN operations like matrix multiplication and convolution with methods known from graph processing like iterative message propagation [87, 178]. Due to uniting DNN operations and message passing, GNNs are sometimes also denoted as message passing neural networks [56, 65, 144, 205].

To begin with, each vertex of an input graph is initially represented by a feature vector, called *activation*. This initial activation only incorporates information about the vertex itself but not about the context within the graph. At each layer, a set of DNN operations and message passing steps are performed vertex-wise to update the activation values. In the first step of an iteration, each vertex aggregates the activations of its adjacent vertices by exchanging messages according to an *aggregation* function

$$a_v^{(k)} = AGGREGATE^{(k)}(\{h_u^{(k-1)} | u \in N(v)\}), \tag{1}$$

6:12 J. Vatter et al.



Fig. 2. Schematic of the GNN training process.

where $a_v^{(k)}$ denotes the aggregated values of vertex v at the kth layer. The term $h_u^{(k-1)}|u \in N(v)$ describes the activations of the neighboring vertices at the previous layer with N(v) denoting the neighbors of v in the given graph. Next, the gathered information is combined and the current value of the vertex is updated with an *update* function. The update function $h_v(k)$ can include standard DNN operations like matrix multiplication and is defined by

$$h_v^{(k)} = UPDATE^{(k)}(a_v^{(k)}, h_v^{(k-1)}),$$
(2)

where $h_v^{(k)}$ is the new activation of vertex v at the kth layer. To obtain the updated activation, the aggregated activations $a_v^{(k)}$ are combined with the vertices' activation of the previous layer $h_v^{(k-1)}$. A special case occurs if k=1, then the initial activations $h_v(0)$ or $h_u(0)|u\in N(V)$ are needed, respectively [65, 87]. The new values now serve as starting point for the next layer where the activations are aggregated and combined again. This process is repeated iteratively. Consequently, more and more vertices are explored. After k layers, the k-hop neighborhood of a vertex is captured. When the final layer has been passed, the final representation of a vertex includes information about the vertex itself as well as about the vertices within the graph context.

In Figure 2, an overview of a complete forward pass is given. It consists of the preceding described steps: (1) fetch the initial weights, (2) pass and aggregate messages from neighboring nodes, (3) perform DNN operations, and (4) update the weights according to a given function. Steps (2) through (4) are repeatedly executed. After n iterations, a final model configuration (5) is obtained.

Analogous to the general neural network training described in Section 2.3, a loss function is computed relative to the output of the forward pass. Backpropagation is applied during the backward pass to adapt the weights of the network [192]. After multiple forward and backward passes, the model can make vertex- and edge-level predictions. To make assertions about the whole graph, a pooling layer needs to be added that aggregates and combines all states and labels contained in the output graph based on a specified pooling operation [215].

There exist several types of GNNs [189], and among the most famous ones are Gated Graph Neural Networks (GG-NNs) [110], Graph Convolution Networks (GCNs) [97], GraphSAGE [67], Graph Attention Networks (GATs) [176], and graph auto-encoders [98]. Architectural differences involve the message propagation process, sampling method, and pooling operation, as well as the composition of the layers. For instance, the GraphSAGE model uses a max-pooling strategy, whereas GCNs use mean-pooling instead. GATs include masked self-attention in the pooling process and GG-NNs capture spatial and temporal changes throughout time by using gated recurrent units as the update module. In general, if the relations between objects are essential to make predictions on the data, GNNs can be used and are often preferable over common DNN architectures [75, 104]. In contrast to DNNs, which read in the data object by object or in an ordered sequence, GNNs naturally capture the relations within a graph and are also able to predict the relations between data points. This cannot be easily done with other DNN models. Thus, GNNs benefit the processing of and prediction on graph data. Sometimes it can be advantageous to combine GNNs with other DNN models, for instance, when handling temporal graphs where nodes and edges are updated sequentially [101, 146, 207]. Here, a GNN is combined with an RNN.

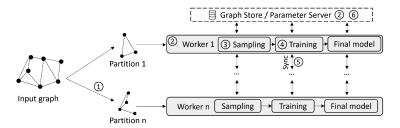


Fig. 3. General setup of a GNN system.

However, if there are no significant connections between the individual data points, there usually is no need to choose a GNN over another DNN model to perform DL.

3.2 Categorization of Methods for Distributed GNN Training

Training GNNs on large graphs is a challenging task that requires high communication, large memory capacity, and high bandwidth [130]. In contrast to general distributed DNN training, all data points within a graph are connected and not independent of each other. Due to this dependency, one cannot simply split the data to process the batches in parallel. Furthermore, memory-intensive edge-centric operations in addition to arithmetic-intensive vertex-centric operations need to be regarded when optimizing GNNs [96]. Thus, large-scale graph processing methods or efficient DNN training operations cannot directly be used for GNN training. More specialized techniques adjusted to GNN characteristics are needed to overcome the described challenges and to speed up training and inference.

A general setup of GNN systems is shown in Figure 3. Similar to the first step in distributed graph processing, the graph can initially be partitioned and distributed across the workers (①: Section 3.2.1). Other types of parallelism (Section 3.2.5) are also possible; however, data parallelism is the most common choice. After that, questions of how to store the subgraphs and whether to cache any data need to be addressed (2): Section 3.2.4). This can either be done locally on the machines or globally on a dedicated graph store. Depending on the training mode, a sampling step is performed (3): Section 3.2.2). Here, only a selection of vertices instead of the whole partition is used to train the model. The main training begins with a random initial representation of the vertices. Then, messages are pulled to or pushed from adjacent vertices (4): Section 3.2.6), DNN operations are applied, and the updated vertex states are shared with the other vertices before the next iteration starts. The whole process can be executed in a synchronous or asynchronous fashion (5): Section 3.2.7), and various scheduling techniques may be applied (Section 3.2.8). Instead of a decentral all-reduce operation to share the parameters, a centralized parameter server might be used (6): Section 3.2.9). After having finished all iterations, predictions can be made based on the final set of parameters. Most systems additionally provide a programming abstraction adapted to the individual optimizations (Section 3.2.3). In the following, we present and categorize systems for GNNs based on the described steps for setting up such a system.

3.2.1 Partitioning. Graph processing systems rely on partitioning the input graph and distributing it across machines before starting the main computation. GNN systems adopt the idea of partitioning because the input graphs are unlikely to fit in a single machine's memory. Some systems use traditional edge-cut or vertex-cut methods [130, 213], whereas others combine those with features like a cost model [87], feasibility score [111], or dataflow partitioning [95]. Table 3 summarizes the different partitioning methods.

In DistDGL [213], the input graph is partitioned using the METIS [91, 92] edge-cut algorithm. Additionally, optimizations like multi-constraint partitioning [93] and a refinement phase are used to

6:14 J. Vatter et al.

				Cut type	e	Static v	vs. Dynamic Offline vs. Online		vs. Online	
Year	System		Edge	Vertex	Hybrid	Static	Dynamic	Offline	Online	Balancing Objective
2019	NeuGraph	[117]			√	√		√		2D partitioning with equally sized
	_									disjoint vertex chunks
2019	GReTA	[95]			√	√		√		2D dataflow partitioning
2020	ROC	[87]	√				✓	√		Runtime of a partition
2020	AGL	[204]	√			√		√		Neighborhood size
2020	PaGraph	[111]	√				√		√	Feasibility score, node degree,
										computation expense
2020	DistDGL	[213]	√			√		√		Minimum edge-cut
2021	P^3	[47]	√			√		√		Random hash
2021	GNNAdvisor	[183]	√			√		√		2D workload partitioning
2021	DistGNN	[130]		√		√			√	Edges per partition
2021	DeepGalois	[73]	√						✓	User-defined policy
2021	ZIPPER	[210]		√		√		√		2D partitioning with equally sized
										disjoint vertex chunks

Table 3. Categorization of Partitioning Strategies

improve load balancing. DistGNN [130] generates partitions with a minimum vertex-cut algorithm and the tool Libra [191]. Here, edges belong to one specific partition while vertices can correspond to multiple partitions, hence they need to be replicated. The so-called *replication factor* measures the number of replicas. A lower replication factor induces less communication across partitions. Moreover, Libra generates balanced partitions by equally distributing edges among subgraphs.

NeuGraph [117] first preprocesses the input graph with the METIS edge-cut algorithm and then applies a grid-based partitioning scheme that combines edge- and vertex-cut. It is similar to the method used in GridGraph [216] and assigns each vertex and its corresponding features to one of P vertex chunks. Then, the adjacency matrix is tiled into $P \times P$ chunks containing the corresponding edges. This partitioning strategy benefits the edge-wise processing, because only the source and destination vertex data need to be loaded. Unlike NeuGraph, GReTA [95] does not partition the graph itself but the dataflow into blocks. The dataflow, also called nodeflow, is a graph structure representing the propagation of feature vectors throughout the forward pass of the GNN model. The vertices each represent a mathematical unit of computation, whereas the edges represent the inputs and outputs of the units. At first, GReTA partitions the vertices of the dataflow graph into nand m-sized chunks. Then, blocks of size $n \times m$ are formed out of the adjacency matrix containing the relevant edges. Hence, only a part of the grid representation instead of the whole graph needs to be loaded for performing a computation step. Additionally, an entire column can be processed in the aggregation phase. Inspired by NeuGraph and GReTA, ZIPPER [210] also uses a grid-based partitioning technique where the graph adjacency matrix is tiled into multiple rectangular blocks. It is distinguished between source and destination vertices throughout the partitioning process to ensure that each block is only associated with one source and one destination partition. Thus, each edge is uniquely identified. As described by the former systems, this partitioning strategy is applied to reduce the memory footprint and communication.

Usually, the graph is partitioned at the beginning of the computation, and these partitions are used until the end of the overall process. ROC [87], however, repartitions the graph before each iteration using an online regression model. A cost model predicts the execution time of various operations on a given graph partition based on parameters like the number of vertices and edges and the number of memory accesses. The cost model is updated and minimized at the end of each iteration with the actual runtimes needed for the subgraph. Then, the graph is repartitioned based on the updated costs.

Zhang et al. [204] provide AGL (the Ant Graph ML system) [204] to employ large GNNs for industrial use cases. The GraphFlat module is responsible for dividing the input graph into k-hop neighborhoods. It uses a pipeline inspired by message passing to produce the desired neighborhoods. In a MapReduce style, self-information about a vertex is generated, propagated

along outgoing edges, and aggregated. This is done until k iterations are reached. Now, the nodes contain information determining the partitions regarding their k-hop neighbors.

PaGraph [111] designs a GNN-aware partitioning algorithm that distributes the vertices among partitions depending on a score. In each iteration of the algorithm, a vertex is scanned and a vector is calculated where the ith position determines the feasibility for assigning the vertex to partition i. The score incorporates features of the vertices so far assigned to partition i, the in-neighbor set of the vertex, and the expected number of vertices in the partition. The current vertex is allocated to the partition with the highest feasibility score, and it is proceeded with the subsequent vertex. Balanced partitions and an evenly distributed computing effort are ensured. GNNAdvisor [183] relies on neighbor partitioning where only the adjacent vertices of a target vertex belong to the given partition. A reason to choose neighbor partitioning over edge- or vertex-cut partitioning is the mitigation of highly varying partition sizes. Further, the probability for tiny partitions is lower, which reduces the managing costs. In contrast to the systems presented previously, Deep-Galois [73] allows for customized partitioning by incorporating the CuSP (Customizable Streaming Partitioner) [74] framework. A simple API lets the user determine the specific partitioning policy, supported are edge-cut, vertex-cut, and hybrid-cut. Hence, the user can tailor the partitioning strategy to the specific GNN architecture. Unlike systems exploiting a compute-intensive customized partitioning strategy [87, 95, 111], P³ [47] relies on a simple random hash partitioner. This ensures simple, fast, and efficient partitioning with only minimal overhead. Here, the initial graph partitioning helps to balance the workload, but the main optimizations to scale to large graphs are done in the consecutive steps of the system, namely sampling (Section 3.2.2), inter-process communication (Section 3.2.4), choice of parallelism (Section 3.2.5), synchronization mode (Section 3.2.7), scheduling (Section 3.2.8), and coordination (Section 3.2.9).

3.2.2 Sampling. The underlying idea of sampling originates in graph processing. However, the idea of sampling slightly differs in connection with GNN training. Different to DNN training, the samples within a graph are not independent. When performing mini-batch training, one can not randomly pick out a number of vertices without regarding the relation to other ones. Thus, training samples of a mini-batch need to include the k-hop neighborhood of a vertex. However, without sampling, these neighborhoods are likely to "explode" as the neighborhood size quickly grows with each hop. For that reason, numerous strategies such as vertex-wise [66], layer-wise, [218] or subgraph-based [24] sampling are introduced. The underlying idea of all of these methods is to restrict the number of k-hop neighbors to be explored to prevent the described neighborhood explosion issue [213]. Although sampling is an established technique working well for many tasks, the choice of the specific strategy depends on the desired downstream ML task, the graph structure, and the objective of the sampling method [147].

An early algorithm for efficient sampling in GNN training is GraphSAGE (SAmple and aggreGatE) [66]. The model trains mini-batches and restricts the neighborhood size per layer. The number of vertices to be sampled is fixed, and the vertices are randomly chosen. GraphSAGE is able to work on larger graphs compared to the general GCN architecture. However, by picking the vertices at random, neighborhood information might be lost, leading to a decrease in accuracy. Therefore, PinSage [200] adopts importance-based neighborhood sampling. The new technique incorporates random walks to compute a score for each vertex and select a fixed number of neighbors accordingly. Hence, memory usage can be controlled and adjusted, if needed, while yielding higher accuracy than random sampling.

FastGCN [19] further explores the idea of sampling based on a calculated score. The authors introduce an importance based layer-wise sampling mechanism where an importance score and a fixed neighborhood size determine which vertices to select. The score mainly depends on the

6:16 J. Vatter et al.

degree of each vertex and is calculated for each layer to restrict the corresponding number of vertices. Thus, the neighborhood explosion problem can be avoided and large graphs can be handled. However, as the vertex-wise importance is calculated independently per layer, the selected neighborhoods for two subsequent layers may differ, which might lead to slow convergence. This issue is faced by LADIES [218], which exploits importance sampling in a layer-dependent way. Depending on the sampled vertices in the previous layer, the neighboring vertices in the current layer are selected and a bipartite graph between the two layers is constructed. After that, the sampling probability is calculated and a fixed number of vertices is sampled. This procedure is repeated for each layer to sample the needed vertices.

ClusterGCN [24] allows for subgraph-based sampling. Contrary to general mini-batch GCN training where the vertices are randomly chosen, a graph clustering algorithm is used to form the mini-batches. The clustering strategy aims at minimizing the number of links between vertices in the batch or between multiple batches. As a consequence, ClusterGCN is faster and uses less memory compared to previous methods. Zeng et al. [202] propose GraphSAINT, which also supports subgraph-based sampling. In contrast to former sampling-based systems first building a GCN and then sampling the input graph, GraphSAINT starts with sampling subgraphs and builds a GCN for each subgraph after that. By building a complete GCN for each sample, extensions like skip connections as proposed by JK-Net [193] are applicable without needing to adapt the sampling process. JK-Net requires the samples of the current layer to be a subset of the previous layers' samples, which is naturally met by using GraphSAINT. Further, GraphSAINT ensures a minimized neighborhood size while maintaining a high accuracy.

AliGraph [197] incorporates sampling by providing three steps: traverse, neighborhood, and negative. Traverse draws a set of vertices and edges from the subgraphs, and neighborhood is responsible for building a vertex context that may be one- or multi-hop neighbor vertices. The last step, negative, speeds up convergence of the training by setting up negative samples. Here, negative sampling refers to including exemplary vertices in the training process that are not part of the given sample. For instance, given a graph G where vertex A is connected to vertex B, but there is no edge between vertex A and vertex C. Negative sampling would mean to not only incorporate edge (A, B) in the training process as positive example but also edge (A, C) with an explicit negative tag indicating there is no connection between those vertices.

Inspired by ClusterGCN [24], GraphTheta [108] uses a clustering algorithm to form the graph samples. Within one subgraph, the algorithm detects and builds communities maximizing intracommunity and minimizing inter-community edges. Due to forming communities before the main sampling step, the sampled vertices tend to overlap not as much as with (random) neighbor sampling, leading to fewer repeated vertex accesses. Despite the advantage of less redundant calculations, graphs with weak community structures are not supported and the batch size can be imbalanced due to varying community sizes.

The AGL system [204] provides a variety of sampling methods that can be chosen from, such as uniform sampling and weighted sampling. This ensures that the user can select the best strategy for each application. Wang et al. [179] also pursue the idea of providing several sampling techniques in DGL. A set of methods is provided, like the well-known neighbor sampling [66] and cluster sampling [24]. The DistDGL system [213] is based on DGL and supports several sampling techniques but implements the sampling step in a distributed way. The sampling request originates from the trainer process and is sent to the machine responsible for the target set of vertices. After having received the request, the sampling worker calls the sampling operators of DGL and performs sampling on the local partition. It sends the results back to the trainer process, which generates a mini-batch by assembling all acquired results. Like AGL, DGL, and DistDGL, P³ [47] does not provide a particular sampling strategy. However, it adopts the sampling method

			Method		Coordi	nation		
Year	System		Community	User Defined	Full Batch	Centralized	Distributed	Main Sampling Concepts
2019	DGL	[179]		√		✓		Choose method based on application
2019	NeuGraph	[117]			√	✓		No sampling
2019	AliGraph	[197]		√		✓		Three steps: traverse, neighborhood,
								negative
2020	ROC	[87]			√	√		No sampling
2020	AGL	[204]		√		√		Choose method based on application
2020	DistDGL	[213]		√			√	Sampling worker responsible for local
								partition
2021	GraphTheta	[108]	√			✓		Sample from clusters, minimize
								overlapping vertices
2021	P ³	[47]		✓		√		Adapt to given GNN architecture and
								application

Table 4. Categorization of Sampling Strategies

given by the GNN architecture. If no specific method is included, P³ proceeds without a sampling phase. This ensures that a variety of GNN architectures are supported by the system.

Although sampling-based methods may decrease training time of GNNs, there remain issues like lack of consistency [77] and limited applicability to GNN architectures with many-hop or global context layers. For that reason, NeuGraph [117] and ROC [87] omit the sampling phase and rely on full-batch training. A short overview of the different sampling strategies can be found in Table 4.

3.2.3 Programming Abstractions. To facilitate the implementation of the desired GNN architecture and to support custom optimizations, the proposed systems come with programming abstractions including user-defined functions. There are programming models based on message passing [42, 78, 179], whereas other abstractions are using a dataflow paradigm [95, 108, 117].

A PyTorch [138] extension tailored to GNN training is PyG (PyTorch Geometric) [42]. The library comes with a message passing base class where the user only needs to define the construction of messages, the update function, and the aggregation scheme. Message propagation is handled automatically. Numerous GNN architectures can be implemented, such as GCN [97], SGC [187], GraphSAGE [66], GAT [176], and GIN [192]. DGL (Deep Graph Library) [179] also lets the user define the desired GNN model as a set of message passing primitives covering forward and backward pass. The central abstraction is the graph data structure *DGLGraph*. (Pre-)defined functions such as neighbor sampling directly operate on DGLGraph and return a subgraph object. Therefore, manually slicing tensors and manipulating graph data is made obsolete in contrast to frameworks such as PyG. Another message passing based programming interface is introduced by FeatGraph [78]. In addition to customizing the GNN model, the user is able to determine the parallelization strategy for the vertex- and edge-wise feature dimension computation.

For applying the optimization techniques proposed by P³ [47], the system provides a message passing API that developers can use to implement GNN models that automatically include the optimizations. The P-TAGS API consists of six functions: partition, scatter, gather, transform, sync, apply. All functions are user defined and target a different step in the GNN training process. An independent partition function is provided where the developer can implement an individual partitioning algorithm to reduce communication. Whereas scatter is a message passing function defined on each edge, gather assembles the messages vertex-wise with a commutative and associative function. The transform function applies the given neural network operations on each vertex to compute the partial activations. Then, a neighborhood representation is computed with neural network operations such as convolution. Those representations are collected over the network with sync, and a composite apply function is used to update the vertices' states.

Oriented on the GAS model [58], NeuGraph [117] introduces the SAGA-NN (Scatter-Applyedge-Gather-Applyvertex with Neural Networks) programming scheme. The vertex-centric model

6:18 J. Vatter et al.

expresses one GNN forward pass as four steps: scatter, applyedge, gather, and applyvertex. The scatter and gather steps are pre-defined and responsible for data propagation, whereas the other two, applyedge and applyvertex, are defined by the user and are expressed as dataflow with tensor-based operations. In the first phase, namely scatter, information about the vertices is passed onto adjacent edges where the values are aggregated and subsequently combined to form a single edge value in applyedge. The gather step propagates the updated values to the vertices where they are assembled. The vertex state is updated in the last step, applyvertex. Within each step, the computation is parallelized. The abstraction combines graph processing and neural network training by uniting the dataflow model with a vertex-centric view. In general, SAGA-NN follows the common iterative GNN computation model, which makes it applicable to various architectures [97, 110, 167].

To allow for efficient execution of GNN training on an accelerator, GReTA [95] introduces four stateless user-defined functions: gather, reduce, transform, and activate. The gather operation loads and aggregates edge and vertex data. The reduce operation merges the data into one single value. The current and previous reduced vertex state are combined in transform. Finally, activate updates the vertices with new values. A GNN layer is expressed as one or multiple GReTA programs, making it expressive enough for a diverse set of GNN models [11, 66, 97, 192]. NN-TGAR proposed by GraphTheta [108] provides user-friendly programming and enables training on a cluster of machines. Moreover, it unites graph processing and graph learning frameworks. In contrast to the GAS model and GReTA, GraphTheta lets the user implement the GNN model in a vertex- and edge-centric view. Some functions are applied to each vertex, whereas other functions are applied to each edge. The first step of the abstraction, the NN-T operation, transforms values vertex-wise and generates corresponding messages. After that, NN-G (gather) is applied to each edge to update the edge values and transmit the message to the destination vertex. The sum operation in turn operates on each vertex and combines the received messages with methods like averaging or concatenation (NN-A). Then, the result is applied to the vertices and the parameters are updated with reduce. NN-T, NN-G, and NN-A are implemented as neural networks, making it easy to perform the forward pass and subsequent gradient computation. Additionally, an encoding layer is decomposed into subsequent independent stages allowing for general applicability.

FlexGraph [178] introduces the programming model NAU (neighborselection, aggregation, and update). In contrast to models based on GAS abstraction [58, 117], NAU comprises neighborselection, which builds HDGs (Hierarchical Dependency Graphs) including chosen neighbors to capture the dependencies among vertices. After that, the neighborhood features are aggregated and a neighborhood representation is computed in the aggregation step. In the update phase, a new representation is calculated, consisting of old features and the new neighborhood representation. Moreover, one single message comprises multiple features and messages are assembled to reduce traffic. As opposed to programming models like SAGA-NN [117] and its variants, NAU is not limited to 1-hop neighbors during computation. Additional to flat aggregation operations, hierarchical aggregation can be used to support various GNN architectures. Therefore, NAU also supports GNN models with indirect neighbors and hierarchical aggregations, such as PinSage [200], MAGNN [46], P-GNN [201], and JK-Net [193], whereas SAGA-NN merely supports architectures where direct neighbors and flat aggregations are regarded [97, 121, 192].

Inspired by Pregel [119], the underlying programming model of Seastar [188] is realized in a vertex-centric fashion. From the viewpoint of a vertex, the user defines functions to implement the GNN architecture. Seastar then executes the given operations on each vertex. This improves usability compared to message passing systems [42, 117, 179] and dataflow programming systems [4, 117, 197]. With the proposed abstraction, it is possible to implement GNN models more easily

Year	System		Expressiveness	Optimizations	Algorithms
2019	DGL	[179]	Message passing abstraction	Operates on DGLGraph	GCN, SGC, GraphSAGE, GAT, GIN
2019	PyG	[42]	Message passing abstraction	Optimized sparse softmax kernels	GCN, SGC, GraphSAGE, GAT, GIN, ARMA, APPNP
2019	GReTA	[95]	Dataflow abstraction	One or multiple GReTA programs per GNN layer	GCN, G-GCN, GraphSAGE, GIN
2019	NeuGraph	[117]	Dataflow model with vertex-centric view, direct neighbors, and flat aggregations	Tensor-based operations	CommNet, GCN, GG-NN
2020	FeatGraph	[78]	Message passing abstraction	Custom parallelization strategy	GCN, GraphSAGE, GAT
2021	GraphTheta	[108]	Vertex- and edge-centric abstraction	Independent steps are implemented as neural networks	GCN, FastGCN, VR-GCN
2021	FlexGraph	[178]	Indirect neighbors and hierarchical aggregations	Hierarchical dependency graphs	PinSage, MAGNN, P-GNN, JK-Net
2021	P^3	[47]	Vertex- and edge-centric abstraction	User-defined partition function	S-GCN, GCN, GraphSAGE, GAT
2021	Seastar	[188]	Vertex-centric abstraction	Improved usability	GCN, GAT, APPNP, R-GCN

Table 5. Categorization of Programming Abstractions

Table 6. Categorization of Inter-Process Communication Methods

			Stor	age	Cac	hing	
Year	System		Centralized	Distributed	Data	Objective	Optimizations
2019	DGL	[179]	√	√	-	-	Leaves memory management and caching to base framework
2019	AliGraph	[197]		√	Neighbors of selected vertices	Based on importance value	-
2020	ROC	[87]		√	Intermediate tensors on GPU	Minimize cost model based on the graph, GNN model, and GPU device	-
2020	PaGraph	[111]	√		Frequently accessed feature vectors	Minimize computation and communication	-
2020	DistDGL	[213]		√	-	-	KVStore, co-location of data, and computation
2021	GraphTheta	[108]		√	-	-	Task-oriented layout
2021	P^3	[47]	✓		Graph and/or features	User defined	Store partial activations
2021	GNNAdvisor	[183]		√	-	-	Vertex reordering

and the implementation can be adjusted faster. More details on the various programming abstractions are shown in Table 5.

3.2.4 Inter-Process Communication. Before starting the GNN training process, how to store the partitions, samples, and corresponding features and whether to cache any vertex data needs to be determined. DGL [179] is built on top of DNN frameworks like TensorFlow [2] or PyTorch [138] and leaves the memory management to those frameworks instead of developing its own storing and caching strategy. Even though those frameworks are able to store datasets used for DNN training efficiently, more specialized techniques are beneficial for GNN training. Here, samples might contain overlapping neighborhoods, some vertices are repeatedly accessed, and it is crucial to preserve the connections within the graph. Therefore, the following systems employ more sophisticated methods, which are summarized in Table 6.

AliGraph [197] proposes to cache neighbors of important vertices. An *importance* value for each vertex with respect to the number of *k*-hop in- and out-neighbors is calculated. The out-neighbors of vertices with an *importance* value exceeding a user-defined threshold are locally cached. In this way, frequently needed data becomes easily accessible and communication cost is reduced. ROC [87] optimizes runtime performance by caching intermediate tensors on GPUs while maintaining the remaining data in the host memory. By caching these tensors, the data transfers between CPU and GPU are decreased. A cost model is built with respect to a given input graph, a GNN model, and a GPU device. This cost model is minimized with a dynamic programming algorithm to find the globally optimal caching strategy. As the data copy operation from CPU to GPU is a major

6:20 J. Vatter et al.

bottleneck in distributed GNN training, PaGraph [111] uses a computation-aware caching mechanism to minimize data copy. Vertex features as well as structural information about the graph are stored in a *Graph Store Server* on the CPU. This shared memory is globally accessible. Additionally, a cache on each GPU keeps frequently accessed feature vectors. Before deciding which vertices to cache, Lin et al. [111] analyze the characteristics of the training process. GNNs including a sampling technique randomly shuffle the samples in each epoch, making it impossible to predict at runtime which vertices belong to which mini-batch. As a consequence, it is not possible to forecast which vertices are accessed at the next training iteration. However, the out-degree of a vertex indicates how likely it is to be sampled in the whole epoch. With a higher out-degree, it is an in-vertex for a higher number of neighbors. Thus, those vertices are chosen more often in other samples, yield higher computation costs, and should be easily accessible. The caching policy of PaGraph is oriented on those findings. It pre-sorts the vertices by out-degree and fills up the cache following that order. This leads to a high cache hit ratio and reduced data transfer.

A distributed file system is used by AGL [204] to store the neighborhoods. During computation, one or a batch of them is loaded instead of the whole graph. This highly decreases communication between graph stores and workers. AGL can be run on a single machine or a CPU cluster. Dist-DGL [213] also works on multiple CPUs. Thus, the graph structure, corresponding features, and embeddings are stored on multiple machines. The so-called distributed key-value store (*KVStore*) is globally accessible for all trainer processes. DistDGL co-locates data and computation, meaning that the distribution of vertices and edges among the KVStore servers on the machines resemble the obtained graph partitions in the partitioning step. Consequently, trainer processes can directly access the data and communication is reduced. Euler [4] also exploits a distributed storage architecture. The graph engine layer is responsible for loading and dividing the graph into subgraphs and distributing them among the machines.

A more specialized memory optimization is introduced by GNNAdvisor [183]. The underlying idea is to couple vertices and the computing units where they are processed more tightly. Therefore, graph reordering is performed as by Rabbit Order [5] and the *GO-PQ* algorithm [185]. Neighbor groups being close to each other are assigned consecutive vertex IDs, increasing the possibility for them to be scheduled closely on the same machine. As two adjacent neighborhoods often share common neighbors, the L1 cache is more efficiently used and data locality is exploited. ZIPPER [210] also includes a vertex reordering technique. Here, a heuristic degree sorting strategy is used to group the out-edges of the source vertices. As a consequence, vertex data is more efficiently reused and redundancies are minimized.

GraphTheta [108] also stores subgraphs in a distributed fashion. To achieve low-latency access and reduced memory overhead, the proposed parallel tensor storage utilizes a task-oriented lay-out. The memory needed for each task (e.g., forward pass, backward pass, or aggregation phase) is grouped process-wise. The task-specific memory includes raw data and tensors that are further sliced into frames for more efficient access. For each frame, memory is allocated and deallocated immediately after usage throughout the whole computation process to reduce the memory utilization.

Gandhi and Iyer [47] extend the KVStore introduced in DistDGL [213]. In addition to vertex and edge data, P³ also stores partial activations in the KVStore. The extended KVStore coordinates data movement across machines. As soon as the machines are synchronized, the accumulated activation is moved to the device memory and shared with the trainer process. Additionally, P³ allows the user to define a caching strategy. A simple method tested by the authors is to store the input on a minimum number of machines and replicate partitions on so far unused machines.

3.2.5 Parallelism. In distributed DNN training, the appropriate type of parallelism can scale computations to large sets of data. The proposed methods, namely data parallelism, model

		Туре	of Para	llelism		
Year	System	Data	Model	Hybrid	Main Concepts	
2019	DGL	[179]	√			Edge and vertex parallelism
2019	NeuGraph	[117]	✓			Mini-batch training
2020	PaGraph	[111]	✓			Mini-batch training
2021	GraphTheta	[108]			✓	Data and operations are split
2021	P^3	[47]			✓	Push-pull parallelism
2021	DistGNN	[130]	✓			Mini-batch training
2021	ZIPPER	[210]			√	Tile- and operator-level parallelism

Table 7. Categorization of Types of Parallelism

parallelism, and hybrid parallelism, have proven to work well. For that reason, researchers apply methods taken from DNN training to GNN training.

The most common method is data parallelism. Analog to parallelism in DNN training, the graph is split into subgraphs. The model is replicated on the machines while each machine handles its own subgraph. Systems like NeuGraph [117], PaGraph [111], and DistGNN [130] rely on data parallelism. Two different implementations of data parallelism, namely edge and vertex parallelism, are introduced by DGL [179]. Two types of matrix multiplications are distinguished to determine whether to process the data edge- or vertex-parallel. Vertex-parallel computation is used for generalized sparse-dense matrix multiplication. In this case, the entire adjacency list of a vertex is managed by one thread. The edge-parallel strategy is used for generalized sampled dense-dense matrix multiplication where one edge is managed by one thread. Here, the workload is balanced implicitly while the workload extent of vertex-parallel processing depends on the vertex degree.

A hybrid between data and model parallelism is employed in P³ by Gandhi and Iyer [47] to tackle issues like ineffectiveness of partitioning and GPU underutilization. First, the model is partitioned and distributed among the machines. After having computed the partial activations for layer 1, the machines apply a reduce function to aggregate those activations. Then, P³ switches to data parallelism to finish the forward pass. The backward pass is quite similar. Until layer 1, data parallelism is exploited and the error gradient is exchanged among the machines. Then, P³ switches back to model-parallel execution to perform the remaining steps of the backward pass locally.

GraphTheta [108] also deploys a form of hybrid parallelism to overcome the scalability issue when handling graphs with highly skewed vertex degree distribution. When processing a full iteration with a high-degree vertex, a worker could run out of memory. For that reason, GraphTheta not only splits up and distributes the input graph among workers but also the operations forming forward and backward pass. This ensures an efficient training phase with natural graphs.

Data and model parallelism are exploited by ZIPPER [210]. Subgraphs are formed by applying grid-based partitioning on the adjacency matrix. The partitions are processed in parallel resulting in data parallelism. Further, model parallelism is achieved by separating and overlapping the operations forming forward and backward pass. Now, the different operations can be executed concurrently on selected partitions to speed up computation and use the memory more efficiently. Table 7 summarizes the types of parallelism.

3.2.6 Message Propagation. Similar to the exchange of data in graph processing system, machines in GNN systems may synchronize by pushing or pulling the relevant values. NeuGraph [117] and GReTA [95] propagate messages by pushing them directly to adjacent vertices. Messages containing graph features and other required information are sent across the network. The pull-based approach is utilized by systems like DGL [179], Dorylus [172], and GNNAutoScale [43]. The associated features as well as the k-hop neighborhood are pulled from memory to construct the computation graph and perform a training step. However, the moving of features across the

6:22 J. Vatter et al.

network may lead to high communication. For that reason, no features are transferred across the network by the P³ system [47], except for partial activations and error gradients. Moreover, the system proposes a push-pull parallelism that switches between pushing and pulling during the training phase. First, P³ pulls the desired neighborhood of a vertex to build a computation graph that is pushed to all of the machines to start the training phase. After having computed the partial activations at layer 1, the machines pull them from all other machines. Then, the computation of the forward pass is performed until the last layer and the backward pass starts. At layer 1, the error gradients are pushed back to all machines and the backward pass ends. The authors chose to switch between the push and pull method to decrease the messages transferred over the network.

3.2.7 Synchronization Mode. When performing tasks in parallel, it is important to determine the execution mode. It needs to be decided whether to follow a synchronous, asynchronous, or hybrid scheme. As seen in distributed graph processing and distributed neural network training, it depends on the specific architecture as to which mode is best. Therefore, AliGraph [197] does not enforce a particular synchronization method but chooses the updating mode based on the provided training algorithm. If the implemented algorithm uses synchronous updates, the system will adopt the synchronous scheme, whereas the asynchronous mode will be chosen if the given training algorithm is based on asynchronous execution. Besides AliGraph, GraphTheta [108] is also not fixed to a specific mode.

In DistGNN [130], three update algorithms with varying communication intensity during the aggregation phase are implemented and compared. The update algorithms regard target vertices and their replicas that emerged during vertex-cut partitioning. The first algorithm does not allow any communication between split-vertices in local partitions and their cloned vertices. Hence, there is no need for synchronization. The second algorithm supports communication of local partitions with their replicas, and the vertices send partial aggregates to their replicas. Only if all vertices have finished communication do the vertices move to the next step of the training phase. A delayed update mechanism is exploited in the third algorithm. It is an asynchronous execution mode where the vertices send partial aggregates in the current epoch and receive them in a consecutive one. In this way, remote communication and local computation are overlapped. Communication is further avoided by only regarding selected split-vertices during each epoch. The overall results show that the zero-communication strategy is the fastest while maintaining only slight fluctuations in accuracy, followed by the asynchronous delayed update algorithm.

Dorylus [172] compares three execution mode variants—a synchronous version and two asynchronous ones—that differ in the choice of the staleness threshold. Synchronization is performed at the gather operation, meaning that if the neighbors of a vertex have not finished to scatter their updated values, the vertex cannot start computing the next layer. For the asynchronous versions, the staleness threshold s determines which stale values of neighbors are allowed to be used by a vertex. For one experiment, the authors chose a value of s=0. In this case, stale values of neighbors might be used if the neighbor is in the same epoch. A staleness value of s=1 was chosen for another experiment allowing for two successive epochs. By employing asynchronous updates with s=0, the per-epoch time could be sped up by around $1.234\times$. A staleness value that is too high induces slow convergence. Even though the time needed for one epoch decreases with s=0 compared to synchronous execution, the number of epochs to obtain the same accuracy rises.

The execution with P^3 [47] needs to be highly coordinated as all machines switch concurrently from data to model parallelism. Additionally, during the data-parallel phase, global gradient synchronization is performed. Therefore, P^3 follows a synchronous execution mode.

3.2.8 Scheduling. To distribute the workload evenly across all workers, tasks and data need to be assigned in an intelligent way. Therefore, scheduling methods are applied that help to increase

	System		Static vs. Dynamic				
Year	ear System		Static	Dynamic	Main Concepts		
2019	NeuGraph [117]			✓	Selective scheduling		
2020	AGL [204] ✓			Parallel preprocessing and model computation stage			
2021	GraphTheta [108]			✓	Work stealing technique		
2021	FlexGraph	[178]	✓		Computation cost based		
2021	P^3	[47]	✓		Inspired by PipeDream [133], based on dependencies within computation graph		
2021	Dorylus	ylus [172] ✓		✓	Divide tasks based on data and computation type		
2021	ZIPPER [210] ✓			Co-locate operations of different subgraphs			

Table 8. Categorization of Scheduling Strategies

workload balance and minimize idle times. In the following, we describe important scheduling techniques. An overview and categorization can be found in Table 8.

The AGL [204] pipeline divides the training procedure into two main stages: a preprocessing stage where the data is loaded and a model computation stage. Instead of performing the two stages sequentially, AGL schedules the stages in parallel. The time needed for the preprocessing stage is smaller compared to the model computation stage. Thus, the training time almost equals the time needed for model computation after some iterations.

Selective scheduling in NeuGraph [117] chooses the most important vertices for computing the edge values based on costs for data copy and transfer. Thus, only a subset of vertex data is transmitted to the GPU and unnecessary vertices are not regarded. Further, the system makes use of pipeline scheduling to find the best execution configuration. To hide the transfer latency, transfer of data chunks between host and device memory and computation is overlapped. An initial scheduling plan is iteratively refined during the training process. The initial random order is gradually adjusted by swapping pairs of chunks while monitoring computation and transfer time to ensure an optimal final schedule. GraphTheta [108] adopts a work-stealing scheduling technique [10]. Tasks are assigned to all machines which then start computing. As soon as a machine has finished its tasks, it "steals" tasks that are queued for other machines and processes them. Benefits of this method are improved load balance and efficiency due to reduced idle times.

FlexGraph [178] deploys workload balancing using a cost function to reduce communication. In place of metrics like vertex weight or edge weight, the proposed cost function is based upon the GNN training cost per partition. To predict the computation cost of a vertex, features like the number of neighbors as well as the size of each neighborhood are taken into account. The predicted costs of all vertices are summed to estimate the final computation cost of the partition. An online workload balancing strategy uses the estimations to construct a fixed number of balancing plans where certain vertices should be moved from overloaded to other partitions. Finally, the system chooses the plan cutting the least number of edges. For an even more efficient computational process, FlexGraph uses a pipeline processing strategy overlapping computation and communication.

Inspired by PipeDream [133], P³ exploits a simple pipelining mechanism. As soon as a computation phase of a mini-batch is dependent on another phase, communication starts. This communication is overlapped with the computation of other mini-batches to avoid stalls. Due to the pipeline delay, weight staleness occurs. Consequently, a weight update function regarding weights from the previous forward and backward pass is applied. Dorylus [172] decomposes forward and backward pass into fine-grained tasks. The tasks are categorized based on *data type* and *computation type*. Depending on the type, the tasks are processed differently and can be performed concurrently. Hereby, communication latency is avoided. Furthermore, the tasks are pooled, and whenever a worker is ready, it takes the one that is scheduled next and executes it. ZIPPER [210] co-locates operations of different subgraphs with a pipelining strategy. As different operations target different resources, the overall performance increases due to more efficiently utilized resources.

6:24 | I. Vatter et al.

				1	Task Typ	e	
Name		#Vertices	#Edges	Vertex	Edge	Graph	Systems
CiteSeer	[54]	3,327	4,732	√	√		PyG, GraphTheta, GNNAdvisor, GNNAutoScale
CORA	[128]	2,708	5,429	√	√		PyG, AGL, GraphTheta, GNNAdvisor, GNNAutoScale
PubMed	[158]	19,717	44,338	√	~		PyG, NeuGraph, ROC, GraphTheta, GNNAdvisor, GNNAutoScale
PPI	[66, 217]	2,373	61,318	√			PyG, ROC, AGL, GNNAdvisor, GNNAutoScale
Reddit	[66]	232,965	114,848,857	√		√	DGL, GReTA, NeuGraph, ROC, PaGraph, GraphTheta, FlexGraph, Dorylus, DistGNN, DeepGalois
LiveJournal	[198]	4,847,571	68,993,773		√		GReTA, PaGraph, ZIPPER
OGBL-ppa	[77]	576,289	30,326,273		√		DGL
OGBL-citation2	[77]	2,927,963	30,561,187		V		DGL
OGBN-arxiv	[77]	169,343	1,166,243	√			DGL, GNNAutoScale
OGBN-proteins	[77]	132,534	39,561,252	√			DGL, GNNAdvisor
OGBN-products	[77]	2,449,029	61,859,140	√			DGL, DistDGL, P3, GNNAutoScale, DistGNN,
							DeepGalois
OGBN-papers100M	[77]	111,059,956	1,615,685,872	√			DistDGL, P3, DistGNN
MAG240M	[76]	244,160,499	1,728,364,232	√			-
WikiKG90Mv2	[76]	91,230,610	601,062,811		√		-
PCQM4Mv2	[76]	52,970,652	54,546,813			√	-

Table 9. Overview of Graph Datasets

3.2.9 Coordination. Section 2.3.3 showed that it is possible to perform the training phase in a centralized, decentralized, or hybrid manner. DistDGL [213] leaves the choice of whether to operate central or decentral to the underlying framework. For example, if DistDGL is built on top of PyTorch [138], an all-reduce primitive is executed to collect and distribute information. However, if the backend framework is TensorFlow [2], DistDGL supports a parameter server implementation.

AGL [204] operates central and makes use of a parameter server as introduced in Section 2.3.3. The parameter server stores all required data and features. Each machine accesses it to fetch the assigned graph partition and exchanges updates without extra communication from machine to machine. GraphTheta [108] also supports computation in a central fashion. Whereas systems like AGL store current model parameters, the parameter server in GraphTheta keeps multiple version of parameters. In this manner, machines can fetch the required parameter version at any time helping to concurrently execute tasks with the appropriate parameters. DistGNN [130] shares updates in a decentralized way with an all-reduce operation and direct communication from machine to machine. Consequently, the need for a parameter server is eliminated. Another decentral system is PaGraph [111]. Here, trainer processes directly interact to exchange locally computed gradients.

3.2.10 Datasets and Benchmarks. This section gives an overview of commonly used datasets in the literature to provide a summary of applications and use cases of GNN systems. We highlight selected publicly available graph datasets and show their characteristics (see Table 9). There are some early citation graph datasets also used to evaluate graph processing systems, including Cite-Seer [54], CORA [128], and PubMed [158]. Here, the vertices represent documents and the edges represent the citations between them. The size is rather small with approximately 3,000 vertices and 5,000 edges in CiteSeer and CORA and around 19,700 vertices and 44,300 edges in PubMed. Predictions about the vertices and edges can be made; however, no graph-level tasks are currently included. Those sets are included in systems like PyG [42], GraphTheta [108], GNNAdvisor [183], and GNNAutoScale [43]. The PPI (Protein-Protein-Interaction) dataset [66, 217] models the role of proteins in different types of human tissue. It contains 20 graphs, each with an average number of 2,373 vertices, and is applicable to vertex-level tasks and included in systems like PyG [42], ROC [87], and AGL [204].

Today, a common strategy to acquire graph structured data is to crawl social networks and use community information as basis for the resulting graph. About 233,000 Reddit posts from different communities are included in the Reddit [66] dataset, and the LiveJournal [198] set represents

around 4.8 million users and their connections. Especially the Reddit dataset is used by numerous systems [87, 95, 108, 111, 117, 130, 179] to measure performance. In contrast to the PPI set, vertex-and graph-level tasks may be carried out on the Reddit set.

OBG (Open Graph Benchmark) [77] comprises a collection of datasets of varying size, origin, and task types. It is differentiated between small, medium, and large sets. The small ones consist of up to 170,000 vertices (OGBN-arxiv), the medium of up to 2.9 million vertices (OGBL-citation2), and the large ones of up to 111 million vertices (OGBN-papers100M). Recently, even larger sets have been added in the course of a large-scale challenge [76] to represent real-world data. The largest set, namely MAG240M, includes an academic graph representing papers, paper subjects, authors, and institutions. To store the approximately 244 million vertices and 1 billion edges, more than 200 GB are needed. The WikiKG90Mv2 knowledge graph consists of 91 million vertices and around 601 million edges resulting in a file size of up to 160 GB. The third set, PCQM4Mv2, is around 8 GB large and contains more than 3,700 graphs with a total vertex number of around 53 million and 54 million edges. OGB also provides a unified evaluation and benchmarking suite. In this manner, researchers are able to run, test, and compare the performance of their model to the existing state of the art.

To date, there is no established standard dataset used by all systems for evaluation. Thus, comparison of their performance is difficult. One could categorize the datasets depending on the sizes and task types as done for the OGB datasets to compare the systems. However, the characteristics of the graphs might differ. For example, some graphs with a similar number of vertices may have varying numbers of edges resulting in more sparse or more dense graphs. A special case is fully connected graphs, where each vertex is connected to each other vertex. Another issue is the continuously growing size of real-world graphs. If a graph might be suitable for representing real-world data right now, it could not be suitable anymore in a couple of years and a new or updated dataset is needed, making comparison of system performance extremely difficult.

3.2.11 Availability and Compatibility. To better asses which system to choose, it is important to know which ones are freely accessible and which frameworks are compatible (see Table 10). We found seven systems to be publicly available, namely PyG [42], DGL [179], AliGraph [197], Dist-DGL [213], GNNAdvisor [183], GNNAutoScale [43], and Dorylus [172]. PyG is a library built upon PyTorch [138] and is fully implemented in Python. It comes with multi-GPU support to achieve scalability. Additionally, there is a GitHub community¹ with more than 240 contributors. In contrast to PyG, DGL is framework agnostic and GNN models can be built with PyTorch, TensorFlow [2], or Apache MXNet [22]. DGL includes CPU and GPU support and is implemented in Python and C++. The GitHub community² includes almost 200 contributors. DistDGL is integrated in DGL as a module.³ The used programming language is Python, and it runs on a cluster of CPUs. Ali-Graph⁴ is compatible with PyTorch and TensorFlow and uses Python and C++. GNNAdvisor⁵ is also implemented with Python and C++. It can either be built upon DGL, PyG, or Gunrock [181] as the underlying framework and includes CPU and GPU support. GNNAutoScale, also referred to as PyGAS, is implemented in PyTorch and uses PyG. The code can be downloaded on GitHub.6 Dorylus combines data servers with serverless computing. The main logic is written in Python and

¹https://github.com/pyg-team/pytorch_geometric.

²https://github.com/dmlc/dgl.

³https://docs.dgl.ai/api/python/dgl.distributed.html.

⁴https://github.com/alibaba/graph-learn.

⁵https://github.com/YukeWang96/OSDI21 AE.

⁶https://github.com/rusty1s/pyg_autoscale.

6:26 J. Vatter et al.

			Progran	Programming Language			
Year	System		Python	C/C++	CPU	GPU	Compatibility
2019	DGL	[179]	√	✓	√	√	PyTorch, TensorFlow, MXNet
2019	PyG	[42]	✓		✓	✓	PyTorch
2019	AliGraph	[197]	✓	√	✓	✓	PyTorch, TensorFlow
2020	DistDGL	[213]	✓		✓		DGL
2021	Dorylus	[172]	✓	✓	Serve	erless	_
2021	GNNAdvisor	[183]	✓	✓	√	√	PyG, DGL, Gunrock
2021	GNNAutoScale	[43]	✓	✓	✓	✓	PyG

Table 10. Overview of Availability and Compatibility of Publicly Available Systems

Table 11. Connections Across Systems for Graph Processing, DL, and GNNs

	Graph Processing Systems	DL Systems	GNN Systems
Partitioning	Section 2.2.2	-	Section 3.2.1
Sampling	Section 2.2.1	-	Section 3.2.2
Programming Abstraction	Section 2.2	-	Section 3.2.3
Inter-Process Communication	Section 2.2.3	-	Section 3.2.4
Parallelism	_	Section 2.3.1	Section 3.2.5
Message Propagation	Section 2.2.5	-	Section 3.2.6
Synchronization Mode	Section 2.2.4	Section 2.3.2	Section 3.2.7
Scheduling	Section 2.2.4	_	Section 3.2.8
Coordination	_	Section 2.3.3	Section 3.2.9

C++, and the code is available on GitHub.⁷ Besides the target use case, it is important to be aware of the framework support as well as utilized programming languages when deciding on a system. Some systems support various underlying frameworks, such as DGL, AliGraph, or GNNAdvisor. Other systems are bound to a specific setup—for example, GNNAutoScale is built upon PyG, and PyG is implemented on top of PyTorch. Ultimately, the user needs to decide which one suits the application and the personal preferences the best.

3.2.12 Performance Assessment. After reviewing the optimizations, availabilities, and compatibilities of a plethora of systems, it would be interesting to understand the performance behavior of each approach. As a full-fledged benchmarking effort is beyond the scope of this survey, we now present a selection of performance results available from the literature. DistDGL [213] gains an overall speedup of 2.2× over Euler [4] and is more than 5× faster in the data copy phase. GNNAdvisor [183] outperforms DGL [179] by a factor of 3 and NeuGraph [117] by a factor of up to 4 when measuring training time. When evaluating ROC [87], the system achieves to perform up to 4× higher as measured in number of epochs per second in the given experiments than NeuGraph. ROC, in turn, is outperformed by P³ [47], which completes epochs up to 2× faster. Although some general points can be made earlier about how well the systems perform, the used datasets and hardware setups differ across the reported systems. This makes it difficult to draw explicit conclusions from the reported quantifications across different papers. Consequently, the need for a comprehensive performance comparison of GNN systems arises, which is a worthy endeavor for future work.

3.3 Connections Across Graph Processing, DL, and GNN Systems

We have introduced methods used to distribute and optimize graph processing (Section 2.2) and DL (Section 2.3) and techniques commonly used by systems for GNNs in Section 3.2. Table 11 shows

⁷https://github.com/uclasystem/dorylus.

how the introduced methods and the systems are connected. Systems for GNNs and systems for graph processing share a wide range of principles, such as partitioning, message propagation, and scheduling. DL systems share ideas of parallelism, synchronization, and coordination with GNN systems. Sampling occurs in GNN training and in graph processing. However, sampling in GNN training is not exactly the same as sampling in graph processing. In GNN training, sampling is used to exclude certain vertices during a training epoch and to create mini-batches, whereas sampling in graph processing means to sparsify the whole input graph before starting the computation.

4 DISCUSSION AND OUTLOOK

We have seen that designing systems for GNN training is a challenging task. The recently proposed systems face issues like workload imbalance, redundant vertex accesses, communication overhead, or changing the parallelism. In the following, we discuss current topics that are increasingly investigated as well as open challenges that still arise when developing GNN systems.

4.1 Current Research Trends

An increasing amount of research deals with the co-design of software and hardware to accelerate GNN training [23, 95, 96, 203]. Here, not only are software and algorithms optimized, but also hardware modules are developed to better address the characteristics of GNNs. Another interesting approach examines the acceleration of quantized GNN architectures [182]. Quantized GNNs [32, 40, 151, 168] have increasingly emerged in the past years and incorporate compressed weights and node embeddings to reduce the memory footprint and computation. Additionally, they are robust and the loss of accuracy is marginal. Systems adapted to quantized GNNs can further accelerate training and inference times. Beyond quantized GNNs, acceleration techniques for dynamic graphs have been proposed [61, 180]. Dynamic graphs change over time, edges might be added or deleted, and vertex features evolve. Some architectures exist that are able to process dynamic graphs, such as SDG [45], Dynamic-GRCNN [139], and DyGNN [118]. Dynamic graphs and architectures for processing them pose new research challenges that are currently being solved. Ongoing research also aims at optimizing inference for GNNs [50, 206], as real-time inference might be crucial for applications such as self-driving cars [214]. Methods range from combining multilayer perceptrons [85] with GNNs [206], introducing a novel propagation scheme that is adapted per node [50] and pruning channels of the feature matrices that leads to pruned weight matrices [214]. Moreover, work focusing on improving the data loading and preprocessing step is proposed [86, 113]. When measuring the time of the different training steps, it can be noted that a prominent proportion of the overall training time is consumed by reading in and preprocessing the graphs. By investigating and improving data I/O and preprocessing, the training process can be made more efficient in future work. Lately, neural architecture search and automated methods for GNNs have caught the attention of many experts [15, 51, 79, 109, 212]. Going a step further, mechanisms like neural architecture search can inspire researchers to also include automated methods assessing which optimization technique fits best for the given architecture, data, and system. For instance, it could be automatically made a recommendation as to which partitioning strategy is most suitable depending on the requirements of the user. Not only single analyses could be made but also an overall estimation about the combination and composition of optimizations.

4.2 Open Challenges

As GNNs and systems for GNNs are young research fields, there still are open challenges to be solved. Most systems are evaluated on static graphs and standard GNN models like GCN, GAT, and GraphSAGE, but there is no information on how the systems would perform on more specialized models and types of data. There are, for instance, GNNs for hypergraphs [41, 195] and

6:28 J. Vatter et al.

multigraphs [53, 135]. As they come with different characteristics than general GNNs, it is likely that optimization techniques need to be adapted to those types of GNNs. Beyond that, deeper GNN architectures with numerous layers have recently been proposed [105, 106, 112] that could pose new challenges for developing GNN systems. Another topic is the size of the feature vectors. When evaluating the systems, fixed feature vector lengths are used, but there is no consistent size within the presented systems. Now, the question is whether the systems perform equally well with variable feature vector sizes, as the size of the feature vector might have a great effect on storage, data loading, and computation time [80]. Moreover, the presented systems are all either based upon an existing graph processing system or a DL framework. Then, the missing operations needed for GNN training are added and optimized. Although this is an effective approach to design a GNN system, the characteristics of GNNs are not fully exploited. A GNN training iteration alternately incorporates dense computation of neural network operations with regular memory access and a sparse aggregation phase with irregular memory accesses [183]. Furthermore, the first layers of a GNN generally are the most compute-intensive ones. Hence, it could be beneficial to adapt the system to handle the computations of the first layers differently compared to the subsequent ones. P³ is the first system going this direction, but it is difficult to find the optimal spot for when to change from model to data parallelism and vice versa. In addition, finding a good way to partition and distribute the model among the machines is challenging. In general, such a method is not favorable if the underlying assumption that the activations are significantly smaller than the features is not met.

5 CONCLUSION

GNNs are increasingly used in various fields of application. With the steadily growing size of realworld graphs, the need for large-scale GNN systems arises. To better comprehend the main concepts of GNN systems, we first provided an overview of two fundamental topics: systems for graph processing and systems for DNN training. We established connections between the used methods and showed that many ideas of GNN systems are inspired by the two related fields. In the main part of this survey, we discussed, categorized, and compared concepts for distributed GNN training. This included partitioning strategies, sampling methods, and different types of parallelism, as well as efficient scheduling and caching. We further investigated datasets and benchmarks for evaluation as well as availability and compatibility of the systems. Although the current systems are able to scale to large graphs, there still are unresolved issues. For instance, the support for specialized GNN architectures like dynamic GNNs or those using hierarchical aggregation are not fully explored. Further, it should be investigated how to handle variable feature vector sizes, as the systems only provide insights into a fixed length. When looking into the future, we see a trend in investigating system support for quantized GNNs, as they are robust and only have a small memory footprint. Another interesting type of GNN architecture that could be increasingly supported is dynamic GNNs. The software-hardware co-design of systems is receiving growing attention, and systems even more adapted to the characteristics of GNNs can further improve the performance. Lastly, a closer look at the data loading and preprocessing phase can help to minimize the overall training time.

REFERENCES

- [1] Sergi Abadal, Akshay Jain, Robert Guirado, Jorge López-Alonso, and Eduard Alarcón. 2021. Computing graph neural networks: A survey from algorithms to accelerators. ACM Computing Surveys 54, 9 (2021), 1–38.
- [2] Martín Abadi, Paul Barham, Jianmin Chen, Zhifeng Chen, Andy Davis, Jeffrey Dean, Matthieu Devin, et al. 2016. TensorFlow: A system for large-scale machine learning. In *Proceedings of the 12th USENIX Symposium on Operating Systems Design and Implementation (OSDI'16)*. 265–283.

- [3] Zainab Abbas, Vasiliki Kalavri, Paris Carbone, and Vladimir Vlassov. 2018. Streaming graph partitioning: An experimental study. *Proceedings of the VLDB Endowment* 11, 11 (2018), 1590–1603.
- [4] Alibaba. 2020. Euler. Retrieved May 28, 2023 from https://github.com/alibaba/euler.
- [5] Junya Arai, Hiroaki Shiokawa, Takeshi Yamamuro, Makoto Onizuka, and Sotetsu Iwamura. 2016. Rabbit Order: Just-in-time parallel reordering for fast graph analysis. In *Proceedings of the 2016 IEEE International Parallel and Distributed Processing Symposium (IPDPS'16)*. IEEE, Los Alamitos, CA, 22–31.
- [6] Alexandru T. Balaban. 1985. Applications of graph theory in chemistry. Journal of Chemical Information and Computer Sciences 25, 3 (1985), 334–343.
- [7] Omar Batarfi, Radwa El Shawi, Ayman G. Fayoumi, Reza Nouri, Seyed-Mehdi-Reza Beheshti, Ahmed Barnawi, and Sherif Sakr. 2015. Large scale graph processing systems: Survey and an experimental evaluation. *Cluster Computing* 18, 3 (2015), 1189–1213.
- [8] Tal Ben-Nun and Torsten Hoefler. 2019. Demystifying parallel and distributed deep learning: An in-depth concurrency analysis. ACM Computing Surveys 52, 4 (2019), 1–43.
- [9] Maciej Besta and Torsten Hoefler. 2022. Parallel and distributed graph neural networks: An in-depth concurrency analysis. arXiv preprint arXiv:2205.09702 (2022).
- [10] Robert D. Blumofe and Charles E. Leiserson. 1999. Scheduling multithreaded computations by work stealing. *Journal of the ACM* 46, 5 (1999), 720–748.
- [11] Xavier Bresson and Thomas Laurent. 2017. Residual gated graph convnets. arXiv preprint arXiv:1711.07553 (2017).
- [12] Tom B. Brown, Benjamin Mann, Nick Ryder, Melanie Subbiah, Jared Kaplan, Prafulla Dhariwal, Arvind Neelakantan, et al. 2020. Language models are few-shot learners. arXiv preprint arXiv:2005.14165 (2020).
- [13] Yingyi Bu, Vinayak Borkar, Jianfeng Jia, Michael J. Carey, and Tyson Condie. 2014. Pregelix: Big (ger) graph analytics on a dataflow engine. arXiv preprint arXiv:1407.0455 (2014).
- [14] Alan Bundy and Lincoln Wallen. 1984. Breadth-first search. In Catalogue of Artificial Intelligence Tools. Symbolic Computation Series. Springer, Berlin, Germany, 13.
- [15] Shaofei Cai, Liang Li, Jincan Deng, Beichen Zhang, Zheng-Jun Zha, Li Su, and Qingming Huang. 2021. Rethinking graph neural network search from message-passing. arXiv e-prints arXiv:2103.14282 (2021).
- [16] Karanbir Singh Chahal, Manraj Singh Grover, Kuntal Dey, and Rajiv Ratn Shah. 2020. A hitchhiker's guide on distributed training of deep neural networks. Journal of Parallel and Distributed Computing 137 (2020), 65–76.
- [17] Ines Chami, Sami Abu-El-Haija, Bryan Perozzi, Christopher Ré, and Kevin Murphy. 2020. Machine learning on graphs: A model and comprehensive taxonomy. *arXiv preprint arXiv:2005.03675* (2020).
- [18] Cen Chen, Kenli Li, Yangfan Li, and Xiaofeng Zou. 2022. ReGNN: A redundancy-eliminated graph neural networks accelerator. In *Proceedings of the 2022 IEEE International Symposium on High-Performance Computer Architecture (HPCA'22)*. IEEE, Los Alamitos, CA, 429–443.
- [19] Jie Chen, Tengfei Ma, and Cao Xiao. 2018. FastGCN: Fast learning with graph convolutional networks via importance sampling. arXiv preprint arXiv:1801.10247 (2018).
- [20] Jianmin Chen, Xinghao Pan, Rajat Monga, Samy Bengio, and Rafal Jozefowicz. 2016. Revisiting distributed synchronous SGD. arXiv preprint arXiv:1604.00981 (2016).
- [21] Rong Chen, Jiaxin Shi, Yanzhe Chen, and Haibo Chen. 2015. PowerLyra: Differentiated graph computation and partitioning on skewed graphs. In *Proceedings of the 10th European Conference on Computer Systems (EuroSys'15)*. ACM, New York, NY, Article 1, 15 pages.
- [22] Tianqi Chen, Mu Li, Yutian Li, Min Lin, Naiyan Wang, Minjie Wang, Tianjun Xiao, Bing Xu, Chiyuan Zhang, and Zheng Zhang. 2015. MXNet: A flexible and efficient machine learning library for heterogeneous distributed systems. arXiv preprint arXiv:1512.01274 (2015).
- [23] Xiaobing Chen, Yuke Wang, Xinfeng Xie, Xing Hu, Abanti Basak, Ling Liang, Mingyu Yan, et al. 2021. Rubik: A hierarchical architecture for efficient graph neural network training. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems 41, 4 (2021), 936–949.
- [24] Wei-Lin Chiang, Xuanqing Liu, Si Si, Yang Li, Samy Bengio, and Cho-Jui Hsieh. 2019. Cluster-GCN: An efficient algorithm for training deep and large graph convolutional networks. In *Proceedings of the 25th ACM SIGKDD International Conference on Knowledge Discovery and Data Mining*. 257–266.
- [25] Trishul Chilimbi, Yutaka Suzue, Johnson Apacible, and Karthik Kalyanaraman. 2014. Project Adam: Building an efficient and scalable deep learning training system. In Proceedings of the 11th USENIX Symposium on Operating Systems Design and Implementation (OSDI'14). 571–582.
- [26] James Cipar, Qirong Ho, Jin Kyu Kim, Seunghak Lee, Gregory R. Ganger, Garth Gibson, Kimberly Keeton, and Eric Xing. 2013. Solving the straggler problem with bounded staleness. In Proceedings of the 14th Workshop on Hot Topics in Operating Systems (HotOS'13).
- [27] Miguel E. Coimbra, Alexandre P. Francisco, and Luís Veiga. 2021. An analysis of the graph processing landscape. Journal of Big Data 8, 1 (2021), 1–41.

6:30 J. Vatter et al.

[28] Paul Czerwionka, Miao Wang, and Fabian Wiesel. 2011. Optimized route network graph as map reference for autonomous cars operating on German autobahn. In *Proceedings of the 5th International Conference on Automation, Robotics, and Applications.* IEEE, Los Alamitos, CA, 78–83.

- [29] Jeffrey Dean, Greg Corrado, Rajat Monga, Kai Chen, Matthieu Devin, Mark Mao, Marc'aurelio Ranzato, et al. 2012. Large scale distributed deep networks. In Proceedings of the 25th International Conference on Neural Information Processing Systems. 1223–1231.
- [30] Jeffrey Dean and Sanjay Ghemawat. 2008. MapReduce: Simplified data processing on large clusters. *Communications of the ACM* 51, 1 (2008), 107–113.
- [31] Edsger W. Dijkstra. 1959. A note on two problems in connexion with graphs. *Numerische Mathematik* 1, 1 (1959), 269–271.
- [32] Mucong Ding, Kezhi Kong, Jingling Li, Chen Zhu, John Dickerson, Furong Huang, and Tom Goldstein. 2021. VQ-GNN: A universal framework to scale up graph neural networks using vector quantization. In Proceedings of the 35th Conference on Neural Information Processing Systems. 1–14.
- [33] David Dominguez-Sal, Norbert Martinez-Bazan, Victor Muntes-Mulero, Pere Baleta, and Josep Lluis Larriba-Pey. 2010. A discussion on the design of graph database benchmarks. In Proceedings of the Technology Conference on Performance Evaluation and Benchmarking. 25–40.
- [34] Sanghamitra Dutta, Gauri Joshi, Soumyadip Ghosh, Parijat Dube, and Priya Nagpurkar. 2018. Slow and stale gradients can win the race: Error-runtime trade-offs in distributed SGD. In Proceedings of the International Conference on Artificial Intelligence and Statistics. 803–812.
- [35] Wenfei Fan. 2012. Graph pattern matching revised for social network analysis. In *Proceedings of the 15th International Conference on Database Theory.* 8–21.
- [36] Wenfei Fan, Ping Lu, Wenyuan Yu, Jingbo Xu, Qiang Yin, Xiaojian Luo, Jingren Zhou, and Ruochun Jin. 2020. Adaptive asynchronous parallelization of graph algorithms. ACM Transactions on Database Systems 45, 2 (2020), 1–45.
- [37] Wenqi Fan, Yao Ma, Qing Li, Yuan He, Eric Zhao, Jiliang Tang, and Dawei Yin. 2019. Graph neural networks for social recommendation. In Proceedings of the World Wide Web Conference. 417–426.
- [38] Wenfei Fan, Jingbo Xu, Yinghui Wu, Wenyuan Yu, and Jiaxin Jiang. 2017. GRAPE: Parallelizing sequential graph computations. *Proceedings of the VLDB Endowment* 10, 12 (2017), 1889–1892.
- [39] Wenfei Fan, Ruiqi Xu, Qiang Yin, Wenyuan Yu, and Jingren Zhou. 2022. Application-driven graph partitioning. *VLDB Journal* 32 (2022), 149–172.
- [40] Boyuan Feng, Yuke Wang, Xu Li, Shu Yang, Xueqiao Peng, and Yufei Ding. 2020. SGQuant: Squeezing the last bit on graph neural networks with specialized quantization. In Proceedings of the 2020 IEEE 32nd International Conference on Tools with Artificial Intelligence (ICTAI'20). IEEE, Los Alamitos, CA, 1044–1052.
- [41] Yifan Feng, Haoxuan You, Zizhao Zhang, Rongrong Ji, and Yue Gao. 2019. Hypergraph neural networks. In *Proceedings of the AAAI Conference on Artificial Intelligence*, Vol. 33. 3558–3565.
- [42] Matthias Fey and Jan Eric Lenssen. 2019. Fast graph representation learning with PyTorch Geometric. arXiv preprint arXiv:1903.02428 (2019).
- [43] Matthias Fey, Jan E. Lenssen, Frank Weichert, and Jure Leskovec. 2021. GNNAutoScale: Scalable and expressive graph neural networks via historical embeddings. In Proceedings of the International Conference on Machine Learning. 3294–3304.
- [44] Apache Software Foundation. 2012. Giraph. Retrieved May 28, 2023 from https://giraph.apache.org/.
- [45] Dongqi Fu and Jingrui He. 2021. SDG: A simplified and dynamic graph neural network. In Proceedings of the 44th International ACM SIGIR Conference on Research and Development in Information Retrieval. 2273–2277.
- [46] Xinyu Fu, Jiani Zhang, Ziqiao Meng, and Irwin King. 2020. MAGNN: Metapath aggregated graph neural network for heterogeneous graph embedding. In Proceedings of The Web Conference 2020. 2331–2341.
- [47] Swapnil Gandhi and Anand Padmanabha Iyer. 2021. P3: Distributed deep graph learning at scale. In Proceedings of the 15th USENIX Symposium on Operating Systems Design and Implementation (OSDI'21). 551–568.
- [48] Difei Gao, Ke Li, Ruiping Wang, Shiguang Shan, and Xilin Chen. 2020. Multi-modal graph neural network for joint reasoning on vision and scene text. In Proceedings of the IEEE/CVF Conference on Computer Vision and Pattern Recognition. 12746–12756.
- [49] Jianliang Gao, Tengfei Lyu, Fan Xiong, Jianxin Wang, Weimao Ke, and Zhao Li. 2020. MGNN: A multimodal graph neural network for predicting the survival of cancer patients. In Proceedings of the 43rd International ACM SIGIR Conference on Research and Development in Information Retrieval. 1697–1700.
- [50] Xinyi Gao, Wentao Zhang, Yingxia Shao, Quoc Viet Hung Nguyen, Bin Cui, and Hongzhi Yin. 2022. Efficient graph neural network inference at large scale. arXiv preprint arXiv:2211.00495 (2022).
- [51] Yang Gao, Hong Yang, Peng Zhang, Chuan Zhou, and Yue Hu. 2020. Graph neural architecture search. In Proceedings of the 29th International Joint Conference on Artificial Intelligence (IJCAI'20), Vol. 20. 1403–1409.

- [52] Thomas Gaudelet, Ben Day, Arian R. Jamasb, Jyothish Soman, Cristian Regep, Gertrude Liu, Jeremy B. R. Hayter, et al. 2021. Utilizing graph machine learning within drug discovery and development. *Briefings in Bioinformatics* 22, 6 (2021), bbab159.
- [53] Xu Geng, Yaguang Li, Leye Wang, Lingyu Zhang, Qiang Yang, Jieping Ye, and Yan Liu. 2019. Spatiotemporal multigraph convolution network for ride-hailing demand forecasting. In *Proceedings of the AAAI Conference on Artificial Intelligence*, Vol. 33. 3656–3663.
- [54] C. Lee Giles, Kurt D. Bollacker, and Steve Lawrence. 1998. CiteSeer: An automatic citation indexing system. In Proceedings of the 3rd ACM Conference on Digital Libraries. 89–98.
- [55] Gurbinder Gill, Roshan Dathathri, Loc Hoang, and Keshav Pingali. 2018. A study of partitioning policies for graph analytics on large-scale distributed platforms. Proceedings of the VLDB Endowment 12, 4 (2018), 321–334.
- [56] Justin Gilmer, Samuel S. Schoenholz, Patrick F. Riley, Oriol Vinyals, and George E. Dahl. 2017. Neural message passing for quantum chemistry. In *Proceedings of the International Conference on Machine Learning*. 1263–1272.
- [57] Michelle Girvan and Mark E. J. Newman. 2002. Community structure in social and biological networks. Proceedings of the National Academy of Sciences 99, 12 (2002), 7821–7826.
- [58] Joseph E. Gonzalez, Yucheng Low, Haijie Gu, Danny Bickson, and Carlos Guestrin. 2012. PowerGraph: Distributed graph-parallel computation on natural graphs. In *Proceedings of the 10th USENIX Symposium on Operating Systems Design and Implementation (OSDI'12)*. 17–30.
- [59] Ian Goodfellow, Yoshua Bengio, and Aaron Courville. 2016. Deep feedforward networks. In *Deep Learning*. MIT Press, Cambridge, MA, 164–223.
- [60] Marco Gori, Gabriele Monfardini, and Franco Scarselli. 2005. A new model for learning in graph domains. In Proceedings of the 2005 IEEE International Joint Conference on Neural Networks, Vol. 2. IEEE, Los Alamitos, CA, 729–734.
- [61] Mingyu Guan, Anand Padmanabha Iyer, and Taesoo Kim. 2022. DynaGraph: Dynamic graph neural networks at scale. In Proceedings of the 5th ACM SIGMOD Joint International Workshop on Graph Data Management Experiences and Systems (GRADES) and Network Data Analytics (NDA). 1–10.
- [62] Chuang-Yi Gui, Long Zheng, Bingsheng He, Cheng Liu, Xin-Yu Chen, Xiao-Fei Liao, and Hai Jin. 2019. A survey on graph processing accelerators: Challenges and opportunities. *Journal of Computer Science and Technology* 34, 2 (2019), 339–371.
- [63] Qingyu Guo, Fuzhen Zhuang, Chuan Qin, Hengshu Zhu, Xing Xie, Hui Xiong, and Qing He. 2022. A survey on knowledge graph-based recommender systems. IEEE Transactions on Knowledge and Data Engineering 34 (2022), 3549–3568.
- [64] Pankaj Gupta, Ashish Goel, Jimmy Lin, Aneesh Sharma, Dong Wang, and Reza Zadeh. 2013. WTF: The who to follow service at Twitter. In Proceedings of the 22nd International Conference on World Wide Web. 505–514.
- [65] William L. Hamilton. 2020. Graph Representation Learning. Synthesis Lectures on Artificial Intelligence and Machine Learning. Springer.
- [66] William L. Hamilton, Rex Ying, and Jure Leskovec. 2017. Inductive representation learning on large graphs. In Proceedings of the 31st International Conference on Neural Information Processing Systems. 1025–1035.
- [67] William L. Hamilton, Rex Ying, and Jure Leskovec. 2017. Representation learning on graphs: Methods and applications. arXiv preprint arXiv:1709.05584 (2017).
- [68] Masatoshi Hanai, Toyotaro Suzumura, Wen Jun Tan, Elvis Liu, Georgios Theodoropoulos, and Wentong Cai. 2019. Distributed edge partitioning for trillion-edge graphs. arXiv preprint arXiv:1908.05855 (2019).
- [69] Anfeng He, Chong Luo, Xinmei Tian, and Wenjun Zeng. 2018. A twofold Siamese network for real-time object tracking. In *Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition*. 4834–4843.
- [70] Safiollah Heidari, Yogesh Simmhan, Rodrigo N. Calheiros, and Rajkumar Buyya. 2018. Scalable graph processing frameworks: A taxonomy and open challenges. *ACM Computing Surveys* 51, 3 (2018), 1–53.
- [71] Bruce Hendrickson and Robert Leland. 1993. *The Chaco Users Guide. Version 1.0.* Technical Report. Sandia National Labs, Albuquerque, NM.
- [72] Qirong Ho, James Cipar, Henggang Cui, Seunghak Lee, Jin Kyu Kim, Phillip B. Gibbons, Garth A. Gibson, Greg Ganger, and Eric P. Xing. 2013. More effective distributed ML via a stale synchronous parallel parameter server. In Proceedings of the 26th International Conference on Neural Information Processing Systems. 1223–1231.
- [73] Loc Hoang, Xuhao Chen, Hochan Lee, Roshan Dathathri, Gurbinder Gill, and Keshav Pingali. 2021. Efficient distribution for deep learning on large graphs. Update 1050 (2021), 1.
- [74] Loc Hoang, Roshan Dathathri, Gurbinder Gill, and Keshav Pingali. 2019. CuSP: A customizable streaming edge partitioner for distributed graph analytics. In Proceedings of the 2019 IEEE International Parallel and Distributed Processing Symposium (IPDPS'19). IEEE, Los Alamitos, CA, 439–450.
- [75] Sepp Hochreiter and Jürgen Schmidhuber. 1997. Long short-term memory. Neural Computation 9, 8 (1997), 1735–1780.

6:32 J. Vatter et al.

[76] Weihua Hu, Matthias Fey, Hongyu Ren, Maho Nakata, Yuxiao Dong, and Jure Leskovec. 2021. OGB-LSC: A large-scale challenge for machine learning on graphs. arXiv preprint arXiv:2103.09430 (2021).

- [77] Weihua Hu, Matthias Fey, Marinka Zitnik, Yuxiao Dong, Hongyu Ren, Bowen Liu, Michele Catasta, and Jure Leskovec. 2020. Open graph benchmark: Datasets for machine learning on graphs. In Proceedings of the 34th Conference on Neural Information Processing Systems. 22118–22133.
- [78] Yuwei Hu, Zihao Ye, Minjie Wang, Jiali Yu, Da Zheng, Mu Li, Zheng Zhang, Zhiru Zhang, and Yida Wang. 2020.
 FeatGraph: A flexible and efficient backend for graph neural network systems. In Proceedings of the International Conference for High Performance Computing, Networking, Storage, and Analysis (SC'20). IEEE, Los Alamitos, CA, 1–13.
- [79] Huan Zhao, Quanming Yao, and Weiwei Tu. 2021. Search to aggregate neighborhood for graph neural network. In Proceedings of the 2021 IEEE 37th International Conference on Data Engineering (ICDE'21). IEEE, Los Alamitos, CA, 552–563.
- [80] Kezhao Huang, Jidong Zhai, Zhen Zheng, Youngmin Yi, and Xipeng Shen. 2021. Understanding and bridging the gaps in current GNN performance optimizations. In Proceedings of the 26th ACM SIGPLAN Symposium on Principles and Practices of Parallel Programming (PPoPP'21). 119–132.
- [81] Yanping Huang, Youlong Cheng, Ankur Bapna, Orhan Firat, Dehao Chen, Mia Chen, HyoukJoong Lee, et al. 2019. GPipe: Efficient training of giant neural networks using pipeline parallelism. In Proceedings of the 33rd International Conference on Neural Information Processing Systems. 103–112.
- [82] Zan Huang, Wingyan Chung, Thian-Huat Ong, and Hsinchun Chen. 2002. A graph-based recommender system for digital library. In *Proceedings of the 2nd ACM/IEEE-CS Joint Conference on Digital Libraries*. 65–73.
- [83] Anand Padmanabha Iyer, Zaoxing Liu, Xin Jin, Shivaram Venkataraman, Vladimir Braverman, and Ion Stoica. 2018.
 ASAP: Fast, approximate graph pattern mining at scale. In Proceedings of the 13th USENIX Symposium on Operating Systems Design and Implementation (OSDI'18). 745–761.
- [84] Anand Padmanabha Iyer, Aurojit Panda, Shivaram Venkataraman, Mosharaf Chowdhury, Aditya Akella, Scott Shenker, and Ion Stoica. 2018. Bridging the GAP: Towards approximate graph analytics. In Proceedings of the 1st ACM SIGMOD Joint International Workshop on Graph Data Management Experiences and Systems (GRADES) and Network Data Analytics (NDA). 1–5.
- [85] Anil K. Jain, Jianchang Mao, and K. Moidin Mohiuddin. 1996. Artificial neural networks: A tutorial. Computer 29, 3 (1996), 31–44.
- [86] Abhinav Jangda, Sandeep Polisetty, Arjun Guha, and Marco Serafini. 2021. Accelerating graph sampling for graph machine learning using GPUs. In *Proceedings of the 16th European Conference on Computer Systems*. 311–326.
- [87] Zhihao Jia, Sina Lin, Mingyu Gao, Matei Zaharia, and Alex Aiken. 2020. Improving the accuracy, scalability, and performance of graph neural networks with ROC. *Proceedings of Machine Learning and Systems* 2 (2020), 187–198.
- [88] Zhihao Jia, Sina Lin, Rex Ying, Jiaxuan You, Jure Leskovec, and Alex Aiken. 2020. Redundancy-free computation for graph neural networks. In *Proceedings of the 26th ACM SIGKDD International Conference on Knowledge Discovery and Data Mining*. 997–1005.
- [89] Peter H. Jin, Qiaochu Yuan, Forrest Iandola, and Kurt Keutzer. 2016. How to scale distributed deep learning? arXiv preprint arXiv:1611.04581 (2016).
- [90] Vasiliki Kalavri, Vladimir Vlassov, and Seif Haridi. 2017. High-level programming abstractions for distributed graph processing. *IEEE Transactions on Knowledge and Data Engineering* 30, 2 (2017), 305–324.
- [91] George Karypis. 1997. METIS: Unstructured Graph Partitioning and Sparse Matrix Ordering System. Technical Report 97-061. University of Minnesota.
- [92] George Karypis and Vipin Kumar. 1998. A fast and high quality multilevel scheme for partitioning irregular graphs. SIAM Journal on Scientific Computing 20, 1 (1998), 359–392.
- [93] George Karypis and Vipin Kumar. 1998. Multilevel algorithms for multi-constraint graph partitioning. In *Proceedings* of the 1998 ACM/IEEE Conference on Supercomputing (SC'98). IEEE, Los Alamitos, CA, 28.
- [94] Zuhair Khayyat, Karim Awara, Amani Alonazi, Hani Jamjoom, Dan Williams, and Panos Kalnis. 2013. Mizan: A system for dynamic load balancing in large-scale graph processing. In Proceedings of the 8th ACM European Conference on Computer Systems. 169–182.
- [95] Kevin Kiningham, Philip Levis, and Christopher Ré. 2020. GReTA: Hardware optimized graph processing for GNNs. In *Proceedings of the Workshop on Resource-Constrained Machine Learning (ReCoML'20)*.
- [96] Kevin Kiningham, Christopher Re, and Philip Levis. 2020. GRIP: A graph neural network accelerator architecture. arXiv preprint arXiv:2007.13828 (2020).
- [97] Thomas N. Kipf and Max Welling. 2016. Semi-supervised classification with graph convolutional networks. arXiv preprint arXiv:1609.02907 (2016).
- [98] Thomas N. Kipf and Max Welling. 2016. Variational graph auto-encoders. arXiv preprint arXiv:1611.07308 (2016).

- [99] Alexandros Koliousis, Pijika Watcharapichat, Matthias Weidlich, Luo Mai, Paolo Costa, and Peter Pietzuch. 2019.
 CROSSBOW: Scaling deep learning with small batch sizes on multi-GPU servers. arXiv preprint arXiv:1901.02244 (2019).
- [100] Alex Krizhevsky, Ilya Sutskever, and Geoffrey E. Hinton. 2017. ImageNet classification with deep convolutional neural networks. Communications of the ACM 60, 6 (2017), 84–90.
- [101] Srijan Kumar, Xikun Zhang, and Jure Leskovec. 2019. Predicting dynamic embedding trajectory in temporal interaction networks. In Proceedings of the 25th ACM SIGKDD International Conference on Knowledge Discovery and Data Mining. 1269–1278.
- [102] Jeannette Lawrence. 1993. Introduction to Neural Networks. California Scientific Software.
- [103] Alexander LeClair, Sakib Haque, Lingfei Wu, and Collin McMillan. 2020. Improved code summarization via a graph neural network. In *Proceedings of the 28th International Conference on Program Comprehension*. 184–195.
- [104] Yan LeCun and Yoshua Bengio. 1995. Convolutional networks for images, speech, and time series. In *The Handbook of Brain Theory and Neural Networks*. MIT Press, Cambridge, MA, 255–258.
- [105] Guohao Li, Matthias Müller, Bernard Ghanem, and Vladlen Koltun. 2021. Training graph neural networks with 1000 layers. In *Proceedings of the International Conference on Machine Learning*. 6437–6449.
- [106] Guohao Li, Chenxin Xiong, Ali Thabet, and Bernard Ghanem. 2020. DeeperGCN: All you need to train deeper GCNs. arXiv preprint arXiv:2006.07739 (2020).
- [107] Hao Li, Asim Kadav, Erik Kruus, and Cristian Ungureanu. 2015. Malt: Distributed data-parallelism for existing ML applications. In *Proceedings of the 10th European Conference on Computer Systems*. 1–16.
- [108] Houyi Li, Yongchao Liu, Yongyong Li, Bin Huang, Peng Zhang, Guowei Zhang, Xintan Zeng, Kefeng Deng, Wenguang Chen, and Changhua He. 2021. GraphTheta: A distributed graph neural network learning system with flexible training strategy. arXiv preprint arXiv:2104.10569 (2021).
- [109] Yaoman Li and Irwin King. 2020. AutoGraph: Automated graph neural network. In Neural Information Processing. Lecture Notes in Computer Science, Vol. 12533. Springer, 189–201.
- [110] Yujia Li, Daniel Tarlow, Marc Brockschmidt, and Richard Zemel. 2015. Gated graph sequence neural networks. arXiv preprint arXiv:1511.05493 (2015).
- [111] Zhiqi Lin, Cheng Li, Youshan Miao, Yunxin Liu, and Yinlong Xu. 2020. PaGraph: Scaling GNN training on large graphs via computation-aware caching. In Proceedings of the 11th ACM Symposium on Cloud Computing. 401–415.
- [112] Meng Liu, Hongyang Gao, and Shuiwang Ji. 2020. Towards deeper graph neural networks. In *Proceedings of the 26th ACM SIGKDD International Conference on Knowledge Discovery and Data Mining*. 338–348.
- [113] Tianfeng Liu, Yangrui Chen, Dan Li, Chuan Wu, Yibo Zhu, Jun He, Yanghua Peng, Hongzheng Chen, Hongzhi Chen, and Chuanxiong Guo. 2021. BGL: GPU-efficient GNN training by optimizing graph data I/O and preprocessing. arXiv preprint arXiv:2112.08541 (2021).
- [114] Yucheng Low. 2013. GraphLab: A Distributed Abstraction for Large Scale Machine Learning. University of California.
- [115] Yucheng Low, Joseph Gonzalez, Aapo Kyrola, Danny Bickson, Carlos Guestrin, and Joseph M. Hellerstein. 2012. Distributed GraphLab: A framework for machine learning in the cloud. arXiv preprint arXiv:1204.6078 (2012).
- [116] Yucheng Low, Joseph E. Gonzalez, Aapo Kyrola, Danny Bickson, Carlos E. Guestrin, and Joseph Hellerstein. 2014. GraphLab: A new framework for parallel machine learning. arXiv preprint arXiv:1408.2041 (2014).
- [117] Lingxiao Ma, Zhi Yang, Youshan Miao, Jilong Xue, Ming Wu, Lidong Zhou, and Yafei Dai. 2019. Neugraph: Parallel deep neural network computation on large graphs. In *Proceedings of the 2019 USENIX Annual Technical Conference (USENIX ATC'19)*. 443–458.
- [118] Yao Ma, Ziyi Guo, Zhaocun Ren, Jiliang Tang, and Dawei Yin. 2020. Streaming graph neural networks. In *Proceedings* of the 43rd International ACM SIGIR Conference on Research and Development in Information Retrieval. 719–728.
- [119] Grzegorz Malewicz, Matthew H. Austern, Aart J. C. Bik, James C. Dehnert, Ilan Horn, Naty Leiser, and Grzegorz Czajkowski. 2010. Pregel: A system for large-scale graph processing. In Proceedings of the 2010 ACM SIGMOD International Conference on Management of Data. 135–146.
- [120] Christopher D. Manning, Mihai Surdeanu, John Bauer, Jenny Rose Finkel, Steven Bethard, and David McClosky. 2014. The Stanford CoreNLP natural language processing toolkit. In *Proceedings of the 52nd Annual Meeting of the Association for Computational Linguistics: System Demonstrations.* 55–60.
- [121] Diego Marcheggiani and Ivan Titov. 2017. Encoding sentences with graph convolutional networks for semantic role labeling. arXiv preprint arXiv:1703.04826 (2017).
- [122] Oliver Mason and Mark Verwoerd. 2007. Graph theory and networks in biology. *IET Systems Biology* 1, 2 (2007), 89–119.
- [123] Christian Mayer, Ruben Mayer, Muhammad Adnan Tariq, Heiko Geppert, Larissa Laich, Lukas Rieger, and Kurt Rothermel. 2018. Adwise: Adaptive window-based streaming edge partitioning for high-speed graph processing. In *Proceedings of the 2018 IEEE 38th International Conference on Distributed Computing Systems (ICDCS'18)*. IEEE, Los Alamitos, CA, 685–695.

6:34 J. Vatter et al.

[124] Ruben Mayer and Hans-Arno Jacobsen. 2020. Scalable deep learning on distributed infrastructures: Challenges, techniques, and tools. ACM Computing Surveys 53, 1 (2020), 1–37.

- [125] Ruben Mayer and Hans-Arno Jacobsen. 2021. Hybrid edge partitioner: Partitioning large power-law graphs under memory constraints. In Proceedings of the 2021 International Conference on Management of Data. 1289–1302.
- [126] Ruben Mayer, Kamil Orujzade, and Hans-Arno Jacobsen. 2020. 2PS: High-quality edge partitioning with two-phase streaming. arXiv preprint arXiv:2001.07086 (2020).
- [127] Ruben Mayer, Kamil Orujzade, and Hans-Arno Jacobsen. 2022. Out-of-core edge partitioning at linear run-time. In *Proceedings of the 2022 IEEE 38th International Conference on Data Engineering (ICDE'22).* IEEE, Los Alamitos, CA, 2629–2642.
- [128] Andrew Kachites McCallum, Kamal Nigam, Jason Rennie, and Kristie Seymore. 2000. Automating the construction of Internet portals with machine learning. *Information Retrieval* 3, 2 (2000), 127–163.
- [129] Robert Ryan McCune, Tim Weninger, and Greg Madey. 2015. Thinking like a vertex: A survey of vertex-centric frameworks for large-scale distributed graph processing. ACM Computing Surveys 48, 2 (2015), 1–39.
- [130] Vasimuddin Md, Sanchit Misra, Guixiang Ma, Ramanarayan Mohanty, Evangelos Georganas, Alexander Heinecke, Dhiraj Kalamkar, Nesreen K. Ahmed, and Sasikanth Avancha. 2021. DistGNN: Scalable distributed training for large-scale graph neural networks. arXiv preprint arXiv:2104.06700 (2021).
- [131] Nikolai Merkel, Ruben Mayer, Tawkir Ahmed Fakir, and Hans-Arno Jacobsen. 2023. Partitioner selection with EASE to optimize distributed graph processing. In *Proceedings of the 2023 IEEE 39th International Conference on Data Engineering (ICDE'23)*. 1–15.
- [132] George A. Miller. 1995. WordNet: A lexical database for English. Communications of the ACM 38, 11 (1995), 39-41.
- [133] Deepak Narayanan, Aaron Harlap, Amar Phanishayee, Vivek Seshadri, Nikhil R. Devanur, Gregory R. Ganger, Phillip B. Gibbons, and Matei Zaharia. 2019. PipeDream: Generalized pipeline parallelism for DNN training. In Proceedings of the 27th ACM Symposium on Operating Systems Principles. 1–15.
- [134] Shuo Ouyang, Dezun Dong, Yemao Xu, and Liquan Xiao. 2021. Communication optimization strategies for distributed deep neural network training: A survey. Journal of Parallel and Distributed Computing 149 (2021), 52–65.
- [135] Yi Ouyang, Bin Guo, Xing Tang, Xiuqiang He, Jian Xiong, and Zhiwen Yu. 2019. Learning cross-domain representation with multi-graph neural network. *arXiv preprint arXiv:1905.10095* (2019).
- [136] Anil Pacaci and M. Tamer Özsu. 2019. Experimental analysis of streaming algorithms for graph partitioning. In *Proceedings of the 2019 International Conference on Management of Data*. 1375–1392.
- [137] Lawrence Page, Sergey Brin, Rajeev Motwani, and Terry Winograd. 1999. *The PageRank Citation Ranking: Bringing Order to the Web.* Technical Report. Stanford InfoLab.
- [138] Adam Paszke, Sam Gross, Francisco Massa, Adam Lerer, James Bradbury, Gregory Chanan, Trevor Killeen, et al. 2019. PyTorch: An imperative style, high-performance deep learning library. In *Proceedings of the 33rd Conference on Neural Information Processing Systems.* 1–12.
- [139] Hao Peng, Hongfei Wang, Bowen Du, Md. Zakirul Alam Bhuiyan, Hongyuan Ma, Jianwei Liu, Lihong Wang, et al. 2020. Spatial temporal incidence dynamic graph neural networks for traffic flow forecasting. *Information Sciences* 521 (2020), 277–290.
- [140] Fabio Petroni, Leonardo Querzoni, Khuzaima Daudjee, Shahin Kamali, and Giorgio Iacoboni. 2015. HDRF: Stream-based partitioning for power-law graphs. In *Proceedings of the 24th ACM International Conference on Information and Knowledge Management*. 243–252.
- [141] Usha Nandini Raghavan, Réka Albert, and Soundar Kumara. 2007. Near linear time algorithm to detect community structures in large-scale networks. *Physical Review E* 76, 3 (2007), 036106.
- [142] Liva Ralaivola, Sanjay J. Swamidass, Hiroto Saigo, and Pierre Baldi. 2005. Graph kernels for chemical informatics. Neural Networks 18, 8 (2005), 1093–1110.
- [143] Prajit Ramachandran, Barret Zoph, and Quoc V. Le. 2017. Searching for activation functions. arXiv preprint arXiv:1710.05941 (2017).
- [144] Pau Riba, Andreas Fischer, Josep Lladós, and Alicia Fornés. 2018. Learning graph distances with message passing neural networks. In *Proceedings of the 2018 24th International Conference on Pattern Recognition (ICPR'18)*. IEEE, Los Alamitos, CA, 2239–2244.
- [145] Herbert Robbins and Sutton Monro. 1951. A stochastic approximation method. *Annals of Mathematical Statistics* 22, 3 (1951), 400–407.
- [146] Emanuele Rossi, Ben Chamberlain, Fabrizio Frasca, Davide Eynard, Federico Monti, and Michael Bronstein. 2020. Temporal graph networks for deep learning on dynamic graphs. arXiv preprint arXiv:2006.10637 (2020).
- [147] Benedek Rozemberczki, Oliver Kiss, and Rik Sarkar. 2020. Little ball of fur: A Python library for graph sampling. In Proceedings of the 29th ACM International Conference on Information and Knowledge Management. 3133–3140.
- [148] Sebastian Ruder. 2016. An overview of gradient descent optimization algorithms. arXiv preprint arXiv:1609.04747 (2016).

- [149] David E. Rumelhart, Richard Durbin, Richard Golden, and Yves Chauvin. 1995. Backpropagation: The basic theory. In *Backpropagation: Theory, Architectures and Applications*, Y. Chauvin and D. E. Rumelhart (Eds.). Lawrence Erlbaum Associates, 1–34.
- [150] David E. Rumelhart, Geoffrey E. Hinton, and Ronald J. Williams. 1986. Learning representations by back-propagating errors. Nature 323, 6088 (1986), 533–536.
- [151] Leila Ben Saad and Baltasar Beferull-Lozano. 2021. Quantization in graph convolutional neural networks. In Proceedings of the 2021 29th European Signal Processing Conference (EUSIPCO'21). IEEE, Los Alamitos, CA, 1855–1859.
- [152] Semih Salihoglu and Jennifer Widom. 2013. GPS: A graph processing system. In Proceedings of the 25th International Conference on Scientific and Statistical Database Management. 1–12.
- [153] Peter Sanders, Kurt Mehlhorn, Martin Dietzfelbinger, and Roman Dementiev. 2019. Sequential and Parallel Algorithms and Data Structures. Springer, 403–404.
- [154] Paul-Edouard Sarlin, Daniel DeTone, Tomasz Malisiewicz, and Andrew Rabinovich. 2020. SuperGlue: Learning feature matching with graph neural networks. In Proceedings of the IEEE/CVF Conference on Computer Vision and Pattern Recognition. 4938–4947.
- [155] Franco Scarselli, Marco Gori, Ah Chung Tsoi, Markus Hagenbuchner, and Gabriele Monfardini. 2008. The graph neural network model. *IEEE Transactions on Neural Networks* 20, 1 (2008), 61–80.
- [156] Sebastian Schlag, Christian Schulz, Daniel Seemaier, and Darren Strash. 2019. Scalable edge partitioning. In Proceedings of the 21st Conference on Algorithm Engineering and Experiments (ALENEX'19). 211–225.
- [157] Michael Schramm, Sukanya Bhowmik, and Kurt Rothermel. 2022. Flexible application-aware approximation for modern distributed graph processing frameworks. In *Proceedings of the 5th ACM SIGMOD Joint International Workshop on Graph Data Management Experiences and Systems (GRADES) and Network Data Analytics (NDA).* 1–10.
- [158] Prithviraj Sen, Galileo Namata, Mustafa Bilgic, Lise Getoor, Brian Galligher, and Tina Eliassi-Rad. 2008. Collective classification in network data. *AI Magazine* 29, 3 (2008), 93.
- [159] Marco Serafini and Hui Guan. 2021. Scalable graph neural network training: The case for sampling. ACM SIGOPS Operating Systems Review 55, 1 (2021), 68–76.
- [160] Sagar Sharma, Simone Sharma, and Anidhya Athaiya. 2017. Activation functions in neural networks. *Towards Data Science* 6, 12 (2017), 310–316.
- [161] Alex Sherstinsky. 2020. Fundamentals of recurrent neural network (RNN) and long short-term memory (LSTM) network. Physica D: Nonlinear Phenomena 404 (2020), 132306.
- [162] Shaohuai Shi, Zhenheng Tang, Xiaowen Chu, Chengjian Liu, Wei Wang, and Bo Li. 2020. A quantitative survey of communication optimizations in distributed deep learning. *IEEE Network* 35, 3 (2020), 230–237.
- [163] Xuanhua Shi, Zhigao Zheng, Yongluan Zhou, Hai Jin, Ligang He, Bo Liu, and Qiang-Sheng Hua. 2018. Graph processing on GPUs: A survey. ACM Computing Surveys 50, 6 (2018), 1–35.
- [164] Nitai B. Silva, Ren Tsang, George D. C. Cavalcanti, and Jyh Tsang. 2010. A graph-based friend recommendation system using genetic algorithm. In *Proceedings of the IEEE Congress on Evolutionary Computation*. IEEE, Los Alamitos, CA. 1–7.
- [165] B. Sobota, Cs. Szabó, and J. Perhac. 2008. Using path-finding algorithms of graph theory for route-searching in geographical information systems. In Proceedings of the 2008 6th International Symposium on Intelligent Systems and Informatics. IEEE, Los Alamitos, CA, 1–6.
- [166] Isabelle Stanton and Gabriel Kliot. 2012. Streaming graph partitioning for large distributed graphs. In Proceedings of the 18th ACM SIGKDD International Conference on Knowledge Discovery and Data Mining. 1222–1230.
- [167] Sainbayar Sukhbaatar, Arthur Szlam, and Rob Fergus. 2016. Learning multiagent communication with backpropagation. In Proceedings of the 30th International Conference on Neural Information Processing Systems. 2252–2260.
- [168] Shyam A. Tailor, Javier Fernandez-Marques, and Nicholas D. Lane. 2020. Degree-Quant: Quantization-aware training for graph neural networks. arXiv preprint arXiv:2008.05000 (2020).
- [169] Lei Tang and Huan Liu. 2010. Graph mining applications to social network analysis. In Managing and Mining Graph Data. Springer, 487–513.
- [170] Zhenheng Tang, Shaohuai Shi, Xiaowen Chu, Wei Wang, and Bo Li. 2020. Communication-efficient distributed deep learning: A comprehensive survey. arXiv preprint arXiv:2003.06307 (2020).
- [171] Robert Tarjan. 1972. Depth-first search and linear graph algorithms. SIAM Journal on Computing 1, 2 (1972), 146–160.
- [172] John Thorpe, Yifan Qiao, Jonathan Eyolfson, Shen Teng, Guanzhou Hu, Zhihao Jia, Jinliang Wei, et al. 2021. Dorylus: Affordable, scalable, and accurate GNN training with distributed CPU servers and serverless threads. In Proceedings of the 15th USENIX Symposium on Operating Systems Design and Implementation (OSDI'21). 495–514.
- [173] Yuanyuan Tian, Andrey Balmin, Severin Andreas Corsten, Shirish Tatikonda, and John McPherson. 2013. From "think like a vertex" to "think like a graph." *Proceedings of the VLDB Endowment* 7, 3 (2013), 193–204.
- [174] Charalampos Tsourakakis, Christos Gkantsidis, Bozidar Radunovic, and Milan Vojnovic. 2014. Fennel: Streaming graph partitioning for massive scale graphs. In *Proceedings of the 7th ACM International Conference on Web Search and Data Mining.* 333–342.

6:36 J. Vatter et al.

[175] Leslie G. Valiant. 1990. A bridging model for parallel computation. Communications of the ACM 33, 8 (1990), 103–111.

- [176] Petar Veličković, Guillem Cucurull, Arantxa Casanova, Adriana Romero, Pietro Lio, and Yoshua Bengio. 2017. Graph attention networks. arXiv preprint arXiv:1710.10903 (2017).
- [177] Guozhang Wang, Wenlei Xie, Alan J. Demers, and Johannes Gehrke. 2013. Asynchronous large-scale graph processing made easy. In Proceedings of the 6th Biennial Conference on Innovative Data Systems Research (CIDR'13). 3-6.
- [178] Lei Wang, Qiang Yin, Chao Tian, Jianbang Yang, Rong Chen, Wenyuan Yu, Zihang Yao, and Jingren Zhou. 2021.
 FlexGraph: A flexible and efficient distributed framework for GNN training. In Proceedings of the 16th European Conference on Computer Systems (EuroSys'21). 67–82.
- [179] Minjie Wang, Lingfan Yu, Da Zheng, Quan Gan, Yu Gai, Zihao Ye, Mufei Li, et al. 2019. Deep Graph Library: Towards efficient and scalable deep learning on graphs. *arXiv:1909.01315* (2019).
- [180] Xuhong Wang, Ding Lyu, Mengjian Li, Yang Xia, Qi Yang, Xinwen Wang, Xinguang Wang, et al. 2021. APAN: Asynchronous propagation attention network for real-time temporal graph embedding. In Proceedings of the 2021 International Conference on Management of Data. 2628–2638.
- [181] Yangzihao Wang, Andrew Davidson, Yuechao Pan, Yuduo Wu, Andy Riffel, and John D. Owens. 2016. Gunrock: A high-performance graph processing library on the GPU. In *Proceedings of the 21st ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming*. 1–12.
- [182] Yuke Wang, Boyuan Feng, and Yufei Ding. 2022. QGTC: Accelerating quantized graph neural networks via GPU tensor core. In *Proceedings of the 27th ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming*. 107–119.
- [183] Yuke Wang, Boyuan Feng, Gushu Li, Shuangchen Li, Lei Deng, Yuan Xie, and Yufei Ding. 2021. GNNAdvisor: An adaptive and efficient runtime system for GNN acceleration on GPUs. In *Proceedings of the 15th USENIX Symposium on Operating Systems Design and Implementation (OSDI'21).* 515–531.
- [184] Pijika Watcharapichat, Victoria Lopez Morales, Raul Castro Fernandez, and Peter Pietzuch. 2016. Ako: Decentralised deep learning with partial gradient exchange. In Proceedings of the 7th ACM Symposium on Cloud Computing. 84–97.
- [185] Hao Wei, Jeffrey Xu Yu, Can Lu, and Xuemin Lin. 2016. Speedup graph processing by graph ordering. In Proceedings of the 2016 International Conference on Management of Data. 1813–1828.
- [186] Bernard Widrow and Michael A. Lehr. 1990. 30 years of adaptive neural networks: Perceptron, Madaline, and back-propagation. Proceedings of the IEEE 78, 9 (1990), 1415–1442.
- [187] Felix Wu, Amauri Souza, Tianyi Zhang, Christopher Fifty, Tao Yu, and Kilian Weinberger. 2019. Simplifying graph convolutional networks. In *Proceedings of the International Conference on Machine Learning*. 6861–6871.
- [188] Yidi Wu, Kaihao Ma, Zhenkun Cai, Tatiana Jin, Boyang Li, Chenguang Zheng, James Cheng, and Fan Yu. 2021. Seastar: Vertex-centric programming for graph neural networks. In *Proceedings of the 16th European Conference on Computer Systems*. 359–375.
- [189] Zonghan Wu, Shirui Pan, Fengwen Chen, Guodong Long, Chengqi Zhang, and S. Yu Philip. 2020. A comprehensive survey on graph neural networks. IEEE Transactions on Neural Networks and Learning Systems 32, 1 (2020), 4–24.
- [190] Chenning Xie, Rong Chen, Haibing Guan, Binyu Zang, and Haibo Chen. 2015. Sync or async: Time to fuse for distributed graph-parallel computation. ACM SIGPLAN Notices 50, 8 (2015), 194–204.
- [191] Cong Xie, Ling Yan, Wu-Jun Li, and Zhihua Zhang. 2014. Distributed power-law graph computing: Theoretical and empirical analysis. In *Proceedings of the 28th Conference on Neural Information Processing Systems*. 1673–1681.
- [192] Keyulu Xu, Weihua Hu, Jure Leskovec, and Stefanie Jegelka. 2018. How powerful are graph neural networks? *arXiv* preprint arXiv:1810.00826 (2018).
- [193] Keyulu Xu, Chengtao Li, Yonglong Tian, Tomohiro Sonobe, Ken-Ichi Kawarabayashi, and Stefanie Jegelka. 2018. Representation learning on graphs with jumping knowledge networks. In Proceedings of the International Conference on Machine Learning. 5453–5462.
- [194] Qiumin Xu, Hyeran Jeon, and Murali Annavaram. 2014. Graph processing on GPUs: Where are the bottlenecks? In *Proceedings of the 2014 IEEE International Symposium on Workload Characterization (IISWC'14)*. IEEE, Los Alamitos, CA, 140–149.
- [195] Naganand Yadati, Madhav Nimishakavi, Prateek Yadav, Vikram Nitin, Anand Louis, and Partha Talukdar. 2019. HyperGCN: A new method for training graph convolutional networks on hypergraphs. In Proceedings of the 33rd International Conference on Neural Information Processing Systems. 1511–1522.
- [196] Da Yan, James Cheng, Yi Lu, and Wilfred Ng. 2014. Blogel: A block-centric framework for distributed computation on real-world graphs. Proceedings of the VLDB Endowment 7, 14 (2014), 1981–1992.
- [197] Hongxia Yang. 2019. AliGraph: A comprehensive graph neural network platform. In Proceedings of the 25th ACM SIGKDD International Conference on Knowledge Discovery and Data Mining. 3165–3166.
- [198] Jaewon Yang and Jure Leskovec. 2015. Defining and evaluating network communities based on ground-truth. *Knowledge and Information Systems* 42, 1 (2015), 181–213.

- [199] Liang Yao, Chengsheng Mao, and Yuan Luo. 2019. Graph convolutional networks for text classification. In *Proceedings of the AAAI Conference on Artificial Intelligence*, Vol. 33. 7370–7377.
- [200] Rex Ying, Ruining He, Kaifeng Chen, Pong Eksombatchai, William L. Hamilton, and Jure Leskovec. 2018. Graph convolutional neural networks for web-scale recommender systems. In Proceedings of the 24th ACM SIGKDD International Conference on Knowledge Discovery and Data Mining. 974–983.
- [201] Jiaxuan You, Rex Ying, and Jure Leskovec. 2019. Position-aware graph neural networks. In Proceedings of the International Conference on Machine Learning. 7134–7143.
- [202] Hanqing Zeng, Hongkuan Zhou, Ajitesh Srivastava, Rajgopal Kannan, and Viktor Prasanna. 2019. GraphSAINT: Graph sampling based inductive learning method. arXiv preprint arXiv:1907.04931 (2019).
- [203] Bingyi Zhang, Hanqing Zeng, and Viktor Prasanna. 2020. Hardware acceleration of large scale GCN inference. In *Proceedings of the 2020 IEEE 31st International Conference on Application-Specific Systems, Architectures, and Processors (ASAP'20)*. IEEE, Los Alamitos, CA, 61–68.
- [204] Dalong Zhang, Xin Huang, Ziqi Liu, Zhiyang Hu, Xianzheng Song, Zhibang Ge, Zhiqiang Zhang, et al. 2020. AGL: A scalable system for industrial-purpose graph machine learning. arXiv preprint arXiv:2003.02454 (2020).
- [205] Fuyang Zhang, Nelson Nauata, and Yasutaka Furukawa. 2020. Conv-MPN: Convolutional message passing neural network for structured outdoor architecture reconstruction. In *Proceedings of the IEEE/CVF Conference on Computer Vision and Pattern Recognition*. 2798–2807.
- [206] Shichang Zhang, Yozen Liu, Yizhou Sun, and Neil Shah. 2022. Graph-less neural networks: Teaching old MLPs new tricks via distillation. In *Proceedings of the International Conference on Learning Representations*. https://openreview.net/forum?id=4p6_5HBWPCw
- [207] Yao Zhang, Yun Xiong, Dongsheng Li, Caihua Shan, Kan Ren, and Yangyong Zhu. 2021. CoPE: Modeling continuous propagation and evolution on interaction graph. In Proceedings of the 30th ACM International Conference on Information and Knowledge Management. 2627–2636.
- [208] Zhen Zhang, Chaokun Chang, Haibin Lin, Yida Wang, Raman Arora, and Xin Jin. 2020. Is network the bottleneck of distributed training? In Proceedings of the Workshop on Network Meets Artificial Intelligence and Machine Learning. 8–13
- [209] Ziwei Zhang, Peng Cui, and Wenwu Zhu. 2022. Deep learning on graphs: A survey. IEEE Transactions on Knowledge and Data Engineering 34 (2022), 249–270.
- [210] Zhihui Zhang, Jingwen Leng, Shuwen Lu, Youshan Miao, Yijia Diao, Minyi Guo, Chao Li, and Yuhao Zhu. 2021.
 ZIPPER: Exploiting tile- and operator-level parallelism for general and scalable graph neural network acceleration.
 arXiv preprint arXiv:2107.08709 (2021).
- [211] Zhaoning Zhang, Lujia Yin, Yuxing Peng, and Dongsheng Li. 2018. A quick survey on large scale distributed deep learning systems. In *Proceedings of the 2018 IEEE 24th International Conference on Parallel and Distributed Systems (ICPADS'18)*. IEEE, Los Alamitos, CA, 1052–1056.
- [212] Yiren Zhao, Duo Wang, Daniel Bates, Robert Mullins, Mateja Jamnik, and Pietro Lio. 2020. Learned low precision graph neural networks. arXiv preprint arXiv:2009.09232 (2020).
- [213] Da Zheng, Chao Ma, Minjie Wang, Jinjing Zhou, Qidong Su, Xiang Song, Quan Gan, Zheng Zhang, and George Karypis. 2020. DistDGL: Distributed graph neural network training for billion-scale graphs. In Proceedings of the 2020 IEEE/ACM 10th Workshop on Irregular Applications: Architectures and Algorithms (IA3'20). IEEE, Los Alamitos, CA, 36–44.
- [214] Hongkuan Zhou, Ajitesh Srivastava, Hanqing Zeng, Rajgopal Kannan, and Viktor Prasanna. 2021. Accelerating large scale real-time GNN inference using channel pruning. Proceedings of the VLDB Endowment 14, 9 (Oct. 2021), 1597–1605.
- [215] Jie Zhou, Ganqu Cui, Shengding Hu, Zhengyan Zhang, Cheng Yang, Zhiyuan Liu, Lifeng Wang, Changcheng Li, and Maosong Sun. 2020. Graph neural networks: A review of methods and applications. *AI Open* 1 (2020), 57–81.
- [216] Xiaowei Zhu, Wentao Han, and Wenguang Chen. 2015. GridGraph: Large-scale graph processing on a single machine using 2-level hierarchical partitioning. In Proceedings of the 2015 USENIX Annual Technical Conference (USENIX ATC'15). 375–386.
- [217] Marinka Zitnik and Jure Leskovec. 2017. Predicting multicellular function through multi-layer tissue networks. *Bioinformatics* 33, 14 (2017), i190–i198.
- [218] Difan Zou, Ziniu Hu, Yewen Wang, Song Jiang, Yizhou Sun, and Quanquan Gu. 2019. Layer-dependent importance sampling for training deep and large graph convolutional networks. *arXiv preprint arXiv:1911.07323* (2019).

Received 9 June 2022; revised 25 April 2023; accepted 2 May 2023