

# NOT RECOMMENDED FOR NEW DESIGN USE DMP2045U



DMP2100U

#### P-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON) MAX</sub>	Package	I <sub>D</sub> T <sub>A</sub> = +25°C
-20V	38mΩ @ V <sub>GS</sub> = -10V		-4.3A
	43mΩ @ V <sub>GS</sub> = -4.5V	SOT23	-4.0A
	75mΩ @ V <sub>GS</sub> = -2.5V		-2.8A

### Description

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### **Applications**

- Load Switch
- Power Management Functions

### **Features**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 3kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

### Mechanical Data

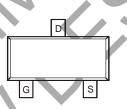
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
  Solderable per MIL-STD-202, Method 208 @3
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



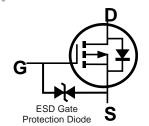


SOT23

Top View



Top View Internal Schematic



Equivalent Circuit (Note 5)

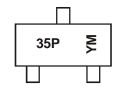
### Ordering Information (Notes 5 & 6)

Part Number	Compliance	Case	Packaging
DMP2100U-7	Standard	SOT23	3,000/Tape & Reel
DMP2100UQ-7	Automotive	SOT23	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-tree/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-tree
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to https://www.diodes.com/quality/.
- 5. The ESD gate protection diode is only designed to protect against ESD events. No gate-source voltage greater than the maximum V<sub>GSS</sub> rating (given on page 2) can be applied.
- 6. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



35P = Product Type Marking Code YM = Date Code Marking Y = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Kev

Year	2008	~	2017	2018	201	9 20	)20 2	2021	2022	2023	2024	2025
Code	V	~	Е	F	G	i	Н	I	J	K	L	М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	2	1	5	6	7	Ω	۵	0	N	D



### NOT RECOMMENDED FOR NEW DESIGN **USE DMP2045U**

**DMP2100U** 

## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V <sub>DSS</sub>	-20	V	
Gate-Source Voltage (Note 7)	V <sub>GSS</sub>	±10	V		
State $T_{\Lambda} = +7$		T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-4.3 -3.4	А
Continuous Drain Current (Note 9) $V_{GS} = -10V$ $t<5s$ $T_A = +25$ $T_A = +70$			Ι <sub>D</sub>	-5.5 -4.3	А
Maximum Continuous Body Diodes Forward Curr	ent (Note 9	Is	-2	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1	<b>%</b> )	I <sub>DM</sub>	-30	Α	
Pulsed Body Diodes Forward Current (10µs Pulse	e, Duty Cyc	le = 1%)	I <sub>SM</sub>	-30	Α

### **Thermal Characteristics**

Characteristic	•	Symbol	Value	Unit
Total Power Dissipation (Note 8)	$T_A = +25$ °C $T_A = +70$ °C	P <sub>D</sub>	0.8	W
Thermal Resistance, Junction to Ambient (Note 8)	Steady State	R <sub>0</sub> JA	161	°C/W
Total Power Dissipation (Note 9)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	PD	1.3	W
Thermal Resistance, Junction to Ambient (Note 9)	Steady State t<5s	$R_{\theta JA}$	99 60	°C/W
Thermal Resistance, Junction to Case (Note 9)		R <sub>0</sub> JC	15	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 10)						
Drain-Source Breakdown Voltage	$BV_{DSS}$	-20	+	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	IDSS	~ \	1	-1	μΑ	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>			±10	μΑ	$V_{GS} = \pm 8V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 10)				•		
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.3	_	-1.4	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
			25	38		$V_{GS} = -10V, I_D = -3.5A$
Static Drain-Source On-Resistance		\\\\	29	43	mΩ	$V_{GS} = -4.5V, I_{D} = -3A$
Static Diam-Source Off-Resistance	RDS(ON)	_	37	75	11122	$V_{GS} = -2.5V, I_{D} = -1A$
		_	47	_		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -0.5A
Forward Transfer Admittance	Y <sub>fs</sub>	_	3	_	S	$V_{DS} = -5V, I_{D} = -4A$
DYNAMIC CHARACTERISTICS (Note 11)						
Input Capacitance	Ciss	_	216	_	pF	15// 1/ 0//
Output Capacitance	Coss	-	90	_	рF	$V_{DS} = -15V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	I	24	_	рF	1 = 1.0101112
Gate Resistnace	$R_g$	_	250	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
SWITCHING CHARACTERISTICS (Note 11)						
Total Gate Charge	$Q_g$	-	9.1	_	nC	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
Gate-Source Charge	$Q_{gs}$		1.6	_	nC	$V_{GS} = -4.5V, V_{DS} = -10V$ $I_{D} = -4A$
Gate-Drain Charge	$Q_{gd}$	l	2.0	_	nC	ID = -4A
Turn-On Delay Time	t <sub>D(ON)</sub>	_	80	_	ns	
Turn-On Rise Time	t <sub>R</sub>	1	155	_	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	688	_	ns	$R_D = 2.5\Omega, R_G = 3.0\Omega$
Turn-Off Fall Time	t <sub>F</sub>	_	423		ns	

Notes:

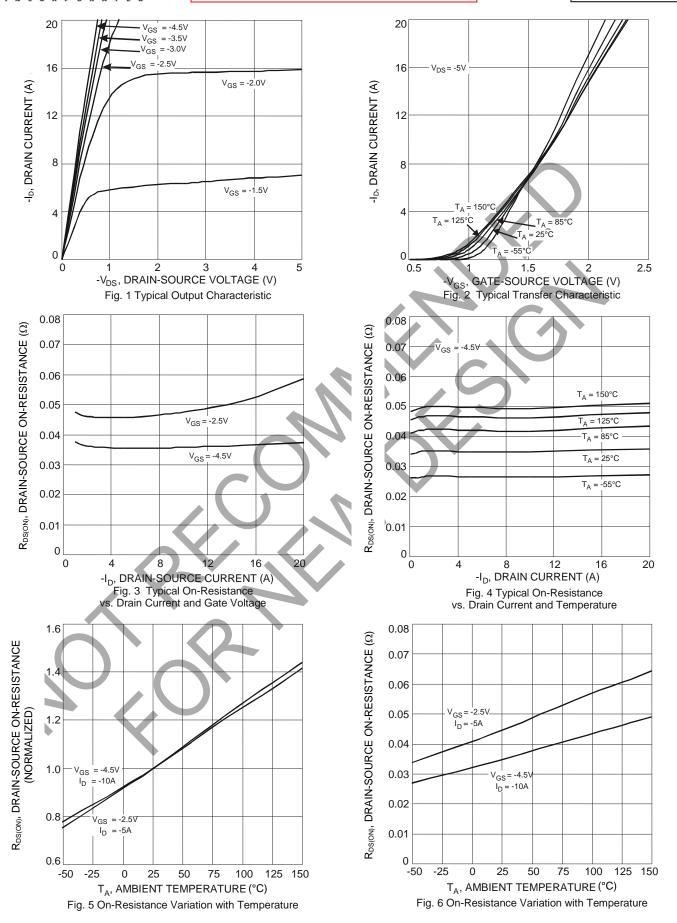
- 7. AEC-Q101  $V_{GS}$  maximum is  $\pm 9.6 V$ .
- 8. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

  9. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 10. Short duration pulse test used to minimize self-heating effect.
- 11. Guaranteed by design. Not subject to product testing.



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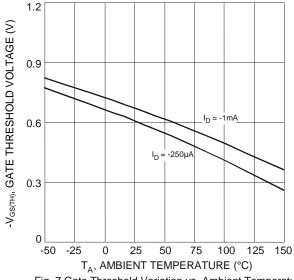
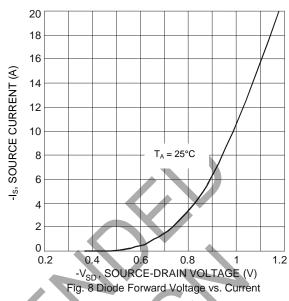
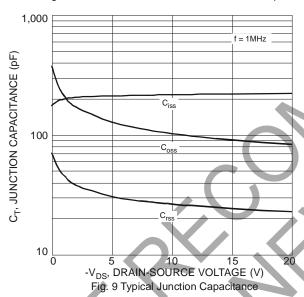
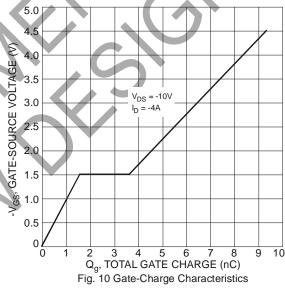


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







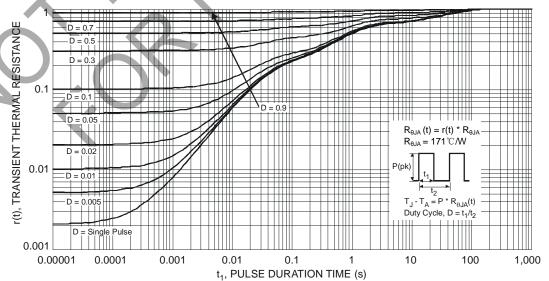


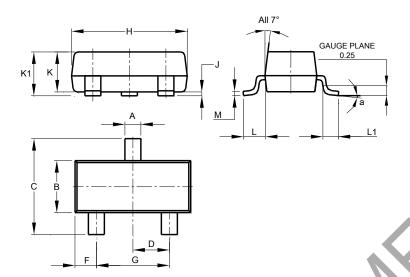
Fig. 11 Transient Thermal Response



### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT23

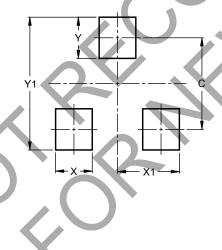


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All	Dimens	ions in	mm				

## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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