DISCIPLINE OF ELECTRICAL AND COMPUTER ENGINEERING

FYP Student - LAB ACCESS REQUEST



Rev. 0.4 12/02/2016

FYP St	udent: reque	est ac	ditional rooms for a	an existing Electronic Access Card	
Applic	ant Details:				
]	Last Name:				
I	First Name:				
Student No.: Phone (BH): Phone (AH):					
Ema	il Address:				
Details	of Access R	equi	red:		
User Category:			9am – 5pm access, M	Monday – Friday (University working days)	
			7am – 10pm access, 7		
Additio	onal Doors Rec	quirec	l:		
$\overline{\checkmark}$	Building EE	Exte	rnal Door		
	■ EEG02 Final Year Project Laboratory				
	■ EEG03 Electronics Laboratory				
■ EEG05 Research & Project Laboratory					
■ EEG06 Electrical Machines Laboratory					
	■ EEG06a Electrical Machines Research Laboratory				
	■ EE103 Circuit Theory Laboratory				
■ EE103a Circuit Theory Laboratory					
■ EE104 Digital Systems ■ EE105 Communication ■ EE107/108 Computing			stems Laboratory		
			nications Laboratory		
			uting Laboratory		
	ES404 Contr	rol La	boratory		
Condit	ions:				
It is your responsibility to advise Security when Access is no longer required.					
•	 The Access Card must not to be lent or shared with other people. If used after hours, all internal doors must be secured and lights turned off. 				
•	The Access card	must n		occupant of the room or building.	
NB.	these conditio Key is submit		the same as those agre	eed to when an application for an Electronic Acce	SS
l have	read and acc	ept t	he above conditions	s	
Signed:				Date:	-
Acaden	nic Supervisor	. Sign	ned:	Date:	