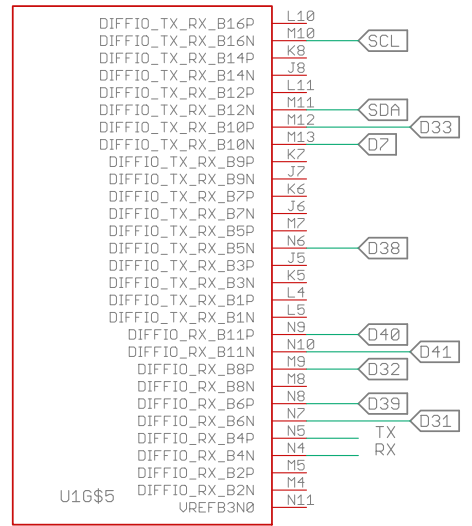
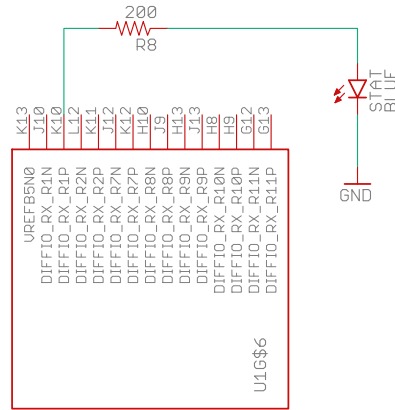
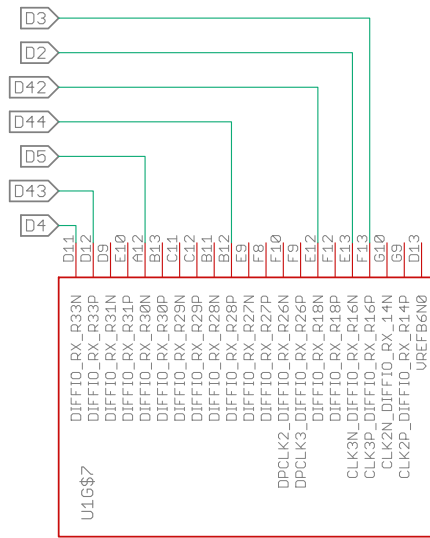
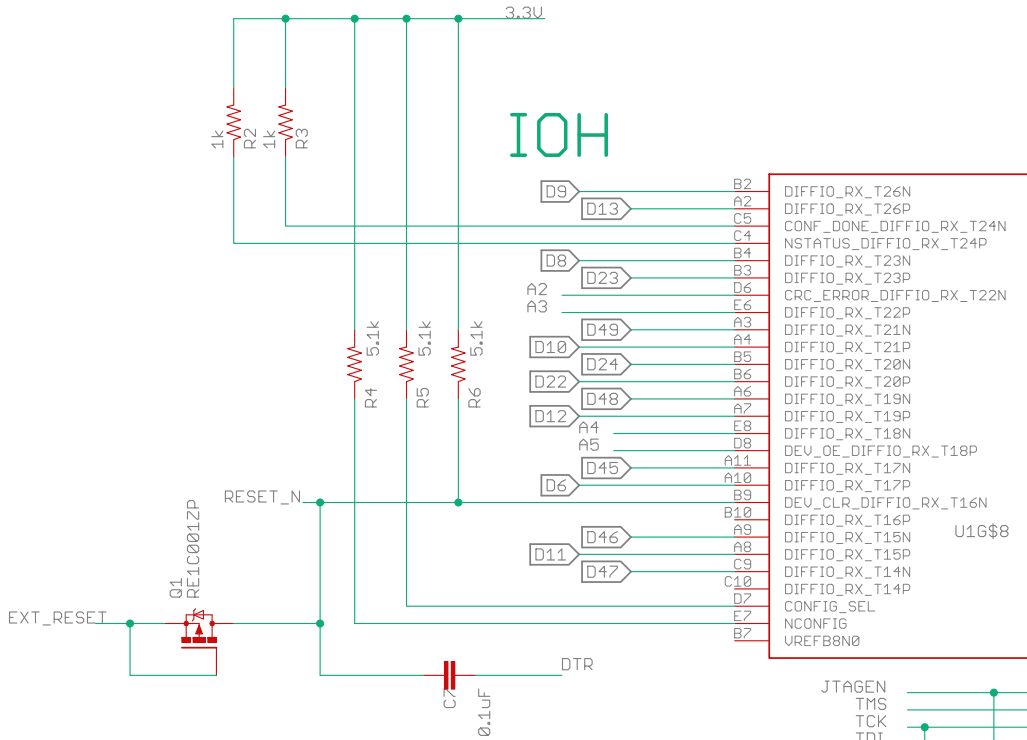
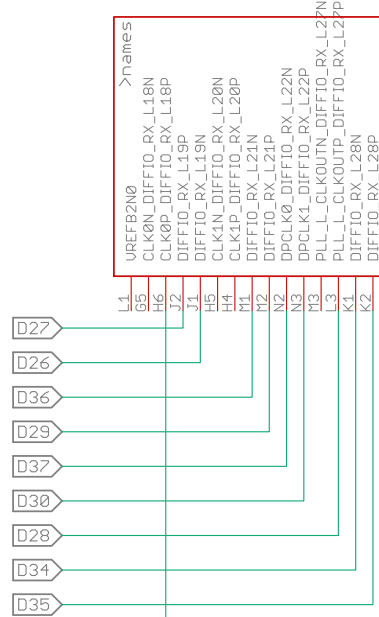
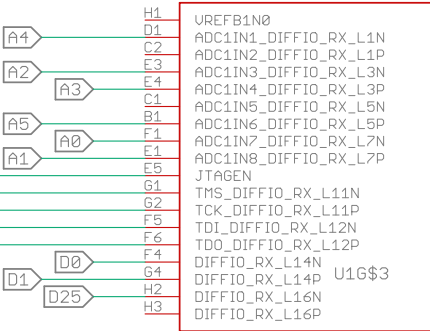


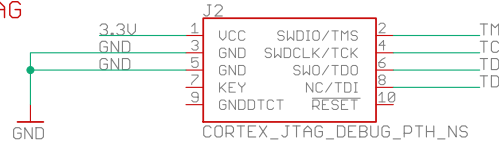
FPGA



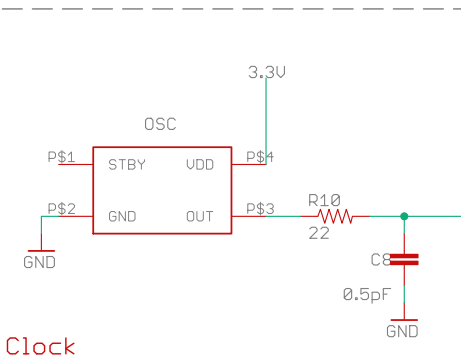
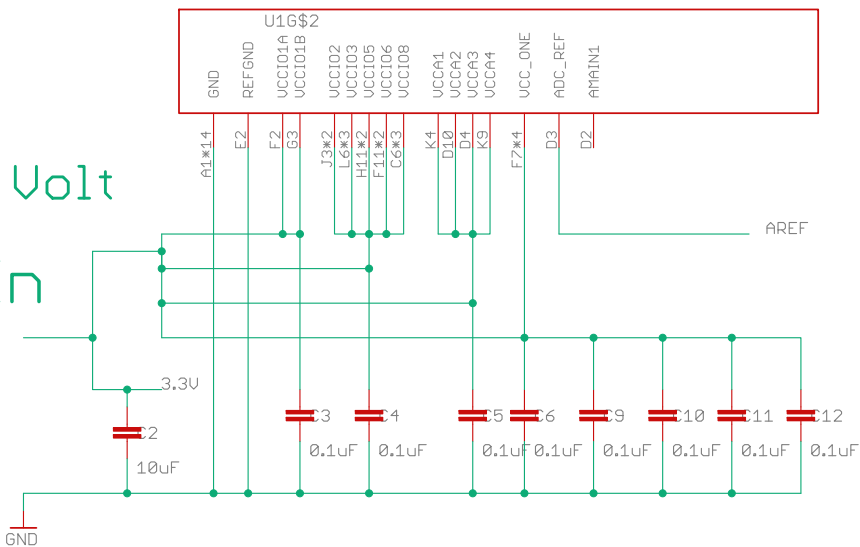
## AD-H-L



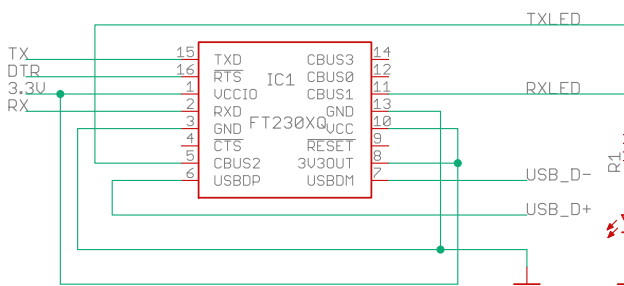
## JTAG



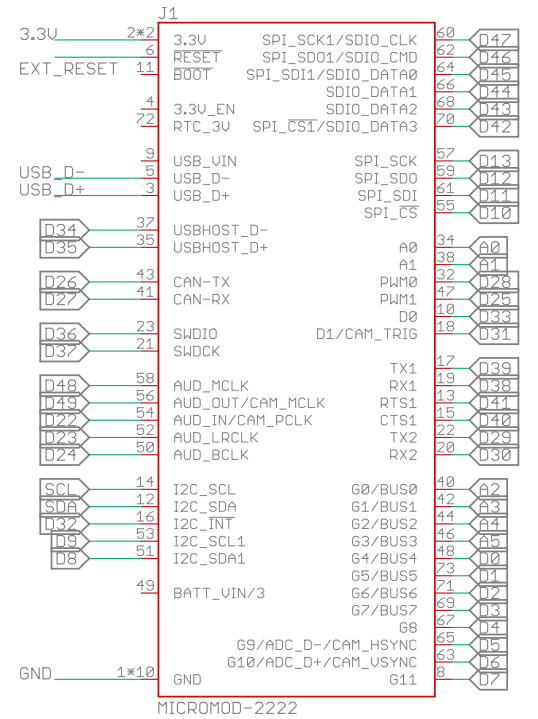
## FPGA Power & GND



Clock



## USB



## MicroMod

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TITLE: MicroMod\_Sno\_Processor

Design by:

REV:

Date: 02/03/2021 11:31

Sheet: 1/1