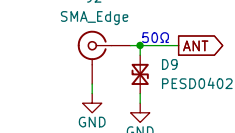




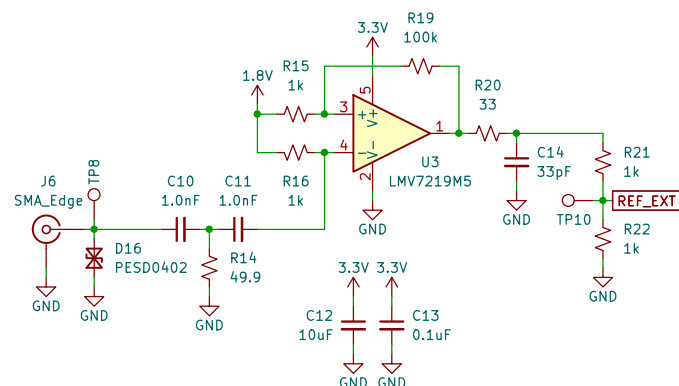
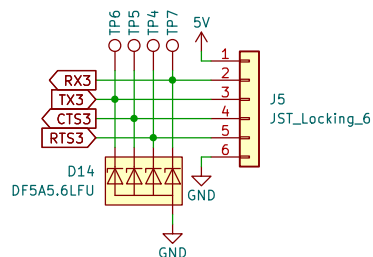
1	
---	--



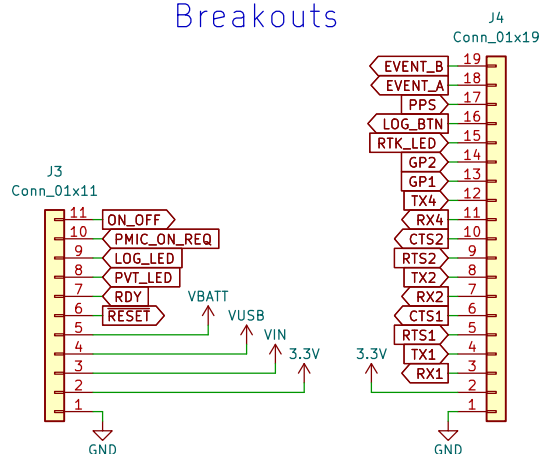
Microstrip (Coplanar Single Ended) 50Ω Calculation:  
<https://jlcpcb.com/pcb-impedance-calculator>  
 Board thickness: 1.6mm. Layers: 4. Er: 4.4 (7628 Prepreg)  
 Dielectric Thickness (Layer 1 to 2): 8.28mil/0.2104mm (JLC7628)  
 Copper Thickness (1oz outer / 0.5oz inner): 1.38mil/0.035mm  
 Polygon Isolation: 7mil/0.1778mm  
 Trace Width: 11.1mil/0.2819mm

---

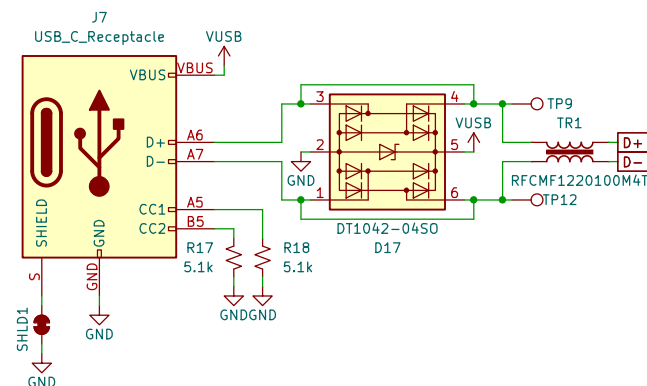
3



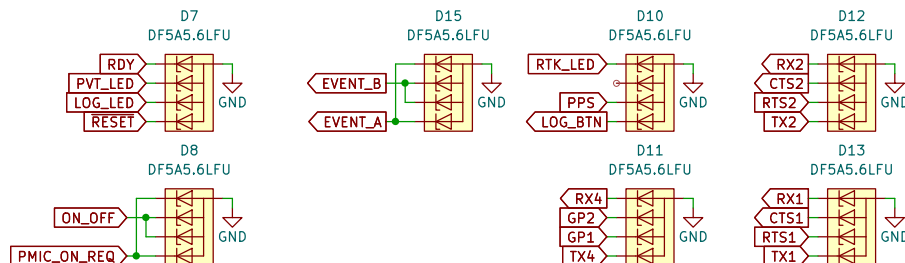
---



---



---



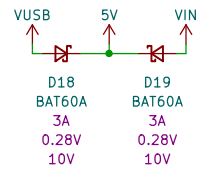
USB Track Impedance: Coplanar Differential Pair @ 90Ω  
<https://jlcpcb.com/pcb-impedance-calculator>  
 Board Thickness: 1.6mm. Layers: 4. Er: 4.4 (7628 Prepreg)  
 Dielectric Thickness (Layer 1 to 2): 8.28mil/0.21mm (JLC7628)  
 Copper Thickness (1oz outer / 0.5oz inner): 1.38mil/0.035mm  
 Polygon Isolation: 7mil/0.1778mm  
 Trace Spacing: 9.07mil/0.2304mm  
 Trace Width: 10.93mil/0.2776mm  
 Trace center-to-center: 20.0mil/0.508mm

Sheet: /Connectors/  
File: Connectors.kicad\_sch

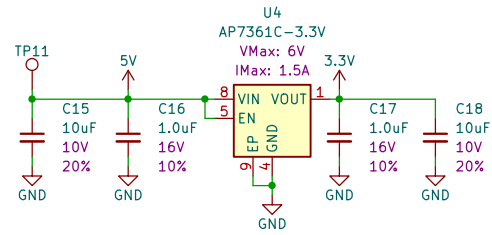
<b>Title: Connectors</b>	
Size: USLetter	Date:
KiCad E.D.A. 9.0.0	

Rev:
Id: 2/3

## Power Mux



## Voltage Regulation – AP7361C–3.3V



Sheet: /Power/  
File: Power.kicad\_sch

### Title: Power

Size: USLetter Date:  
KiCad E.D.A. 9.0.0

Rev:  
Id: 3/3