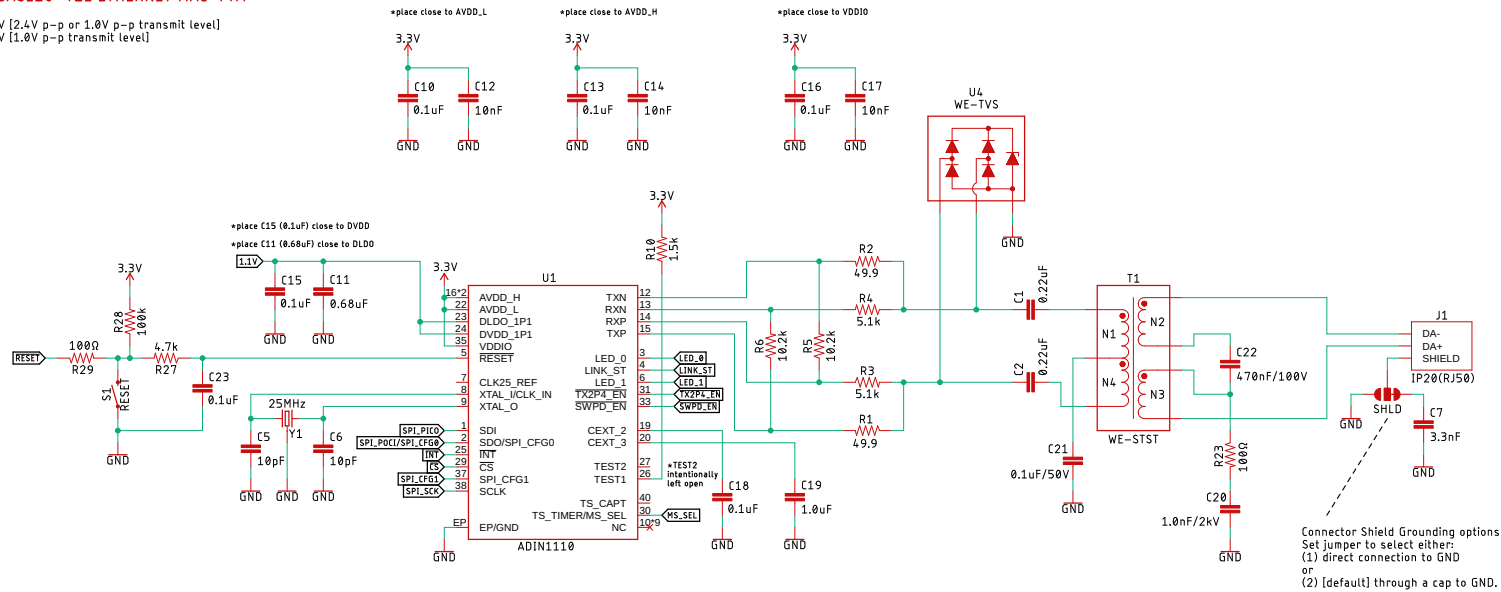
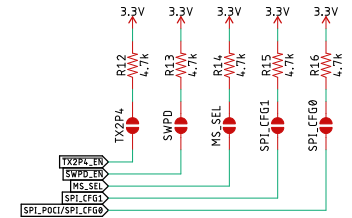


# ADIN1110 BASE10-T1L ETHERNET MAC-PHY

VDD Ranges  
3.13V - 3.46V [2.4V p-p or 1.0V p-p transmit level]  
1.71V - 3.46V [1.0V p-p transmit level]

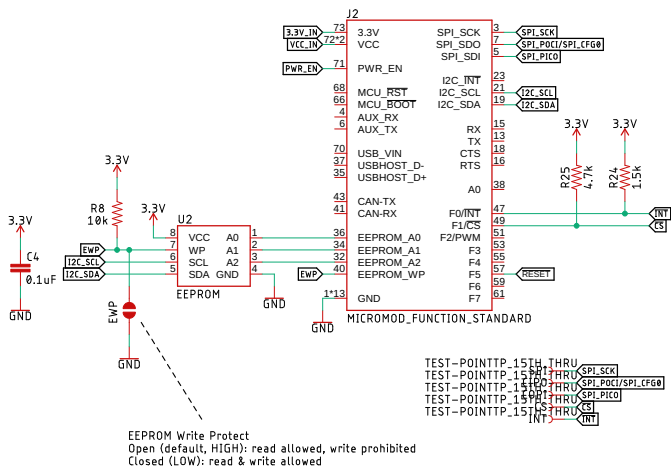


## Configuration Pins/Jumpers



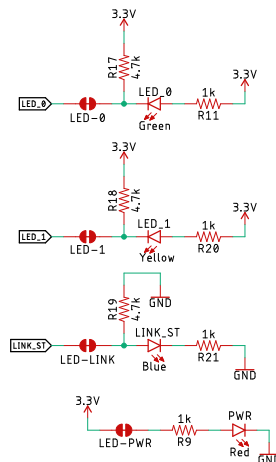
\*All hardware config pins default low with internal pull down resistors  
See datasheet page 16 for all available options.

## MicroMod Function Connector

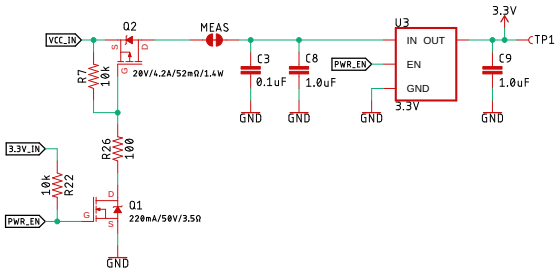


## STATUS LEDs

Cut each jumper to disable each LED



## Power Enable



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Special Instructions

TITLE: SparkFun\_MicroMod\_Function\_ADIN1110

Design by: Pete Lewis

Based on a MM Ethernet board by N. Seidle

REV:

v12

Date: 6/22/2022 3:36 PM

Sheet: 1/1