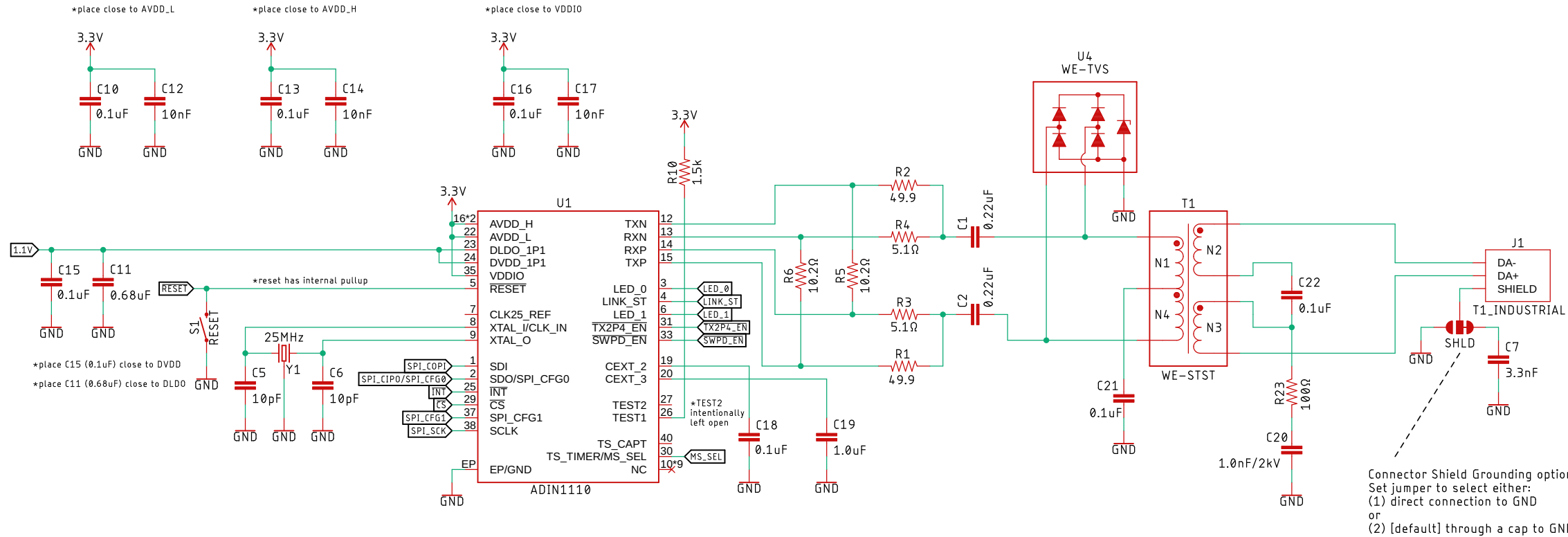
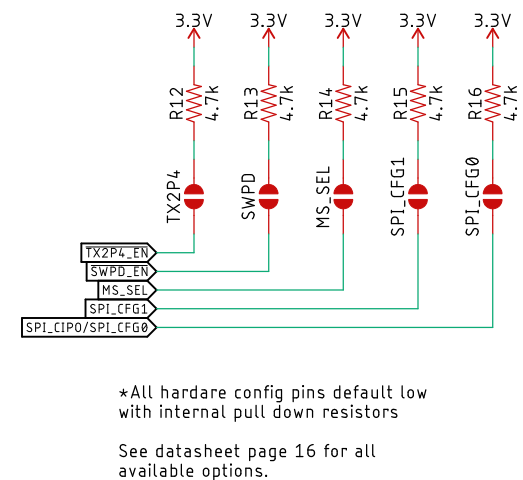


ADIN1110 BASE10-T1L ETHERNET MAC-PHY

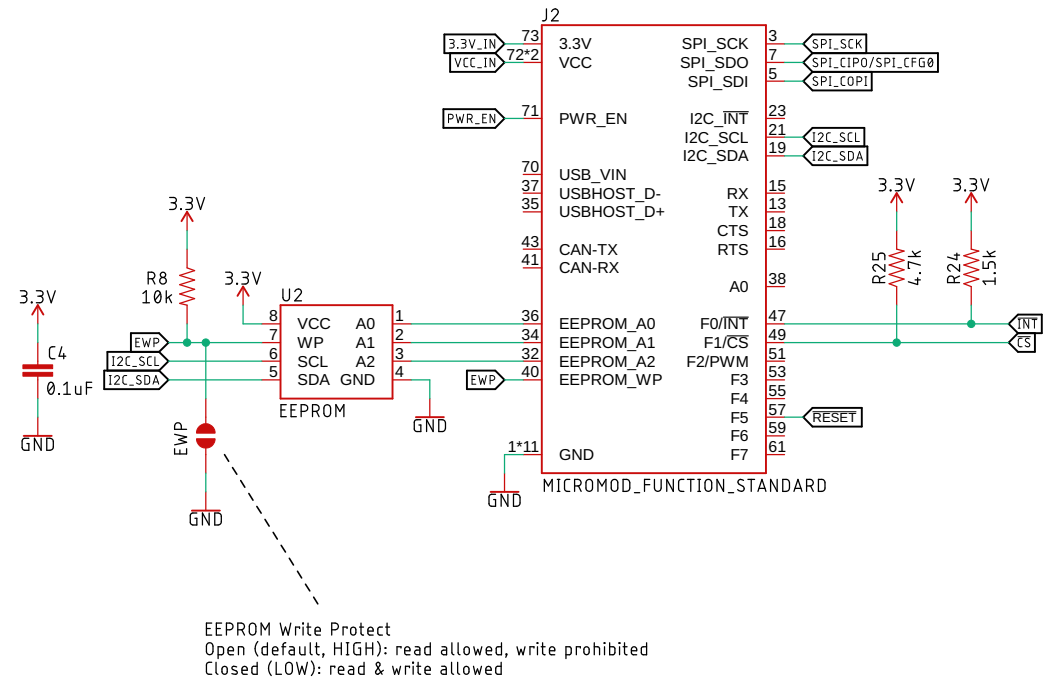
VDD Ranges
3.13V – 3.46V [2.4V p-p or 1.0V p-p transmit level]
1.71V – 3.46V [1.0V p-p transmit level]



Configuration Pins/Jumpers

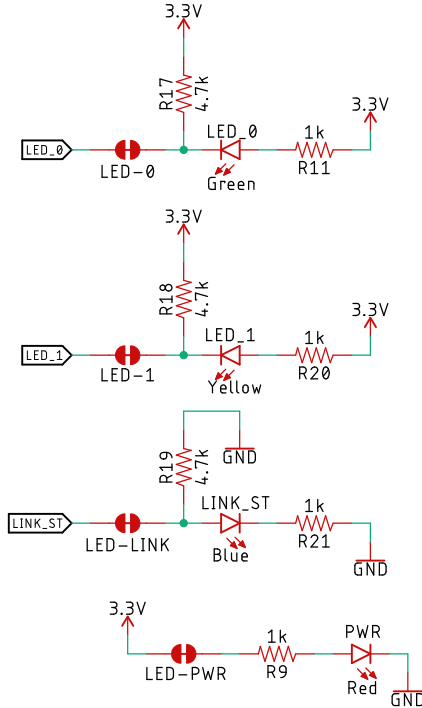


MicroMod Function Connector

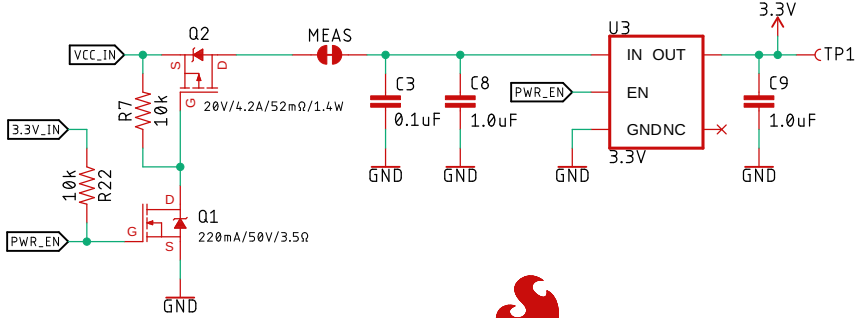


STATUS LEDS

Cut each jumper to disable each LED



Power Enable



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TITLE: SparkFun_MicroMod_Function_ADIN1110

Design by: Pete Lewis Based on a MM Ethernet board by N. Seidle REV: v10

Date: 1/7/2022 10:34 AM Sheet: 1/1