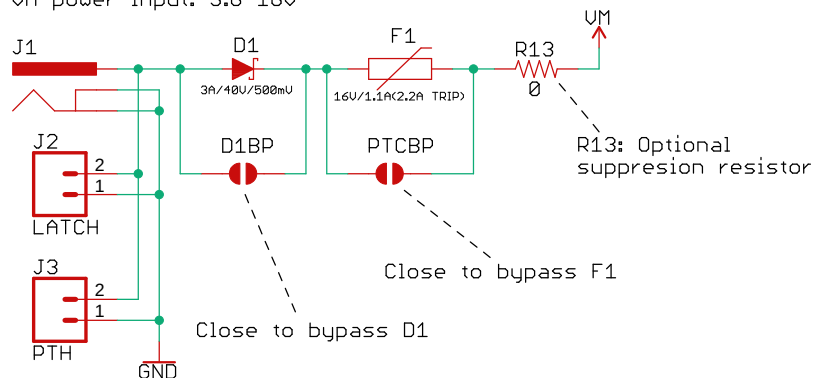


VM power input: 3.6-16V



The diagram illustrates the input stage of the ADXL345. A 3.3V supply is connected to a network of resistors (R7, R8, R9, R10, R12) and switches (MODE0, MODE1, MODE2, MODE3). The output of the network is connected to the STBY pin of the ADXL345, which is also connected to GND.

**J4**

4  
3  
2  
1

STBY  
ERR  
EN

GND

LATCH

**J5**

4  
3  
2  
1

MODE0  
MODE1  
MODE2  
MODE3

LATCH

**J6**

10  
9  
8  
7  
6  
5  
4  
3  
2  
1

STBY  
ERR  
EN  
MODE0  
MODE1  
MODE2  
MODE3  
UREF

GND

3.3V

PTH

**J7**

4  
3  
2  
1

OUT<sub>+</sub>  
OUT<sub>-</sub>  
OUT<sub>+</sub>  
OUT<sub>-</sub>

LATCH

**J8**

4  
3  
2  
1

PTH

[illegible]

Calcs:  
 LOGIC=3.3V  
 $R6=8.2K$   
 $UR1=0-10K$   
 $UREF=0-1.8V$

### Special Instructions

THIS IS A 4-LAYER BOARD



open hardware

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TITLE: SparkFun\_ProDriver\_TC78H670FTG

Design by: Pete Lewis

REV:  
v10

Date: 7/21/2020 9:08 AM

Sheet: 1/1