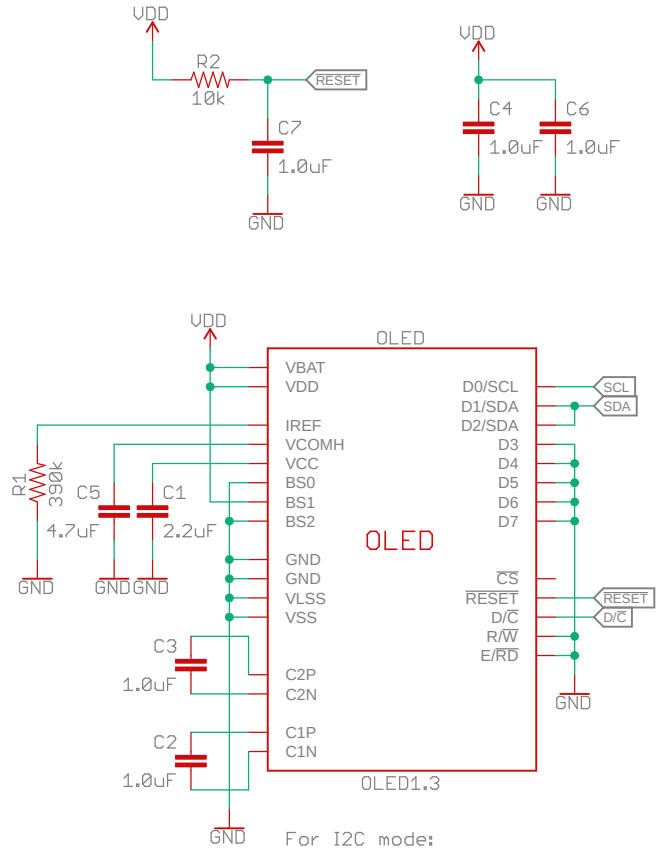


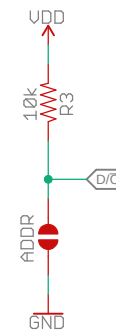
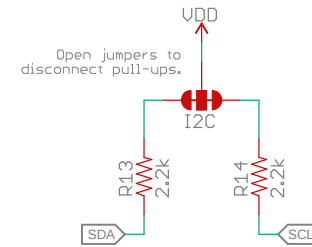
OLED (SSD1306)



```
For I2C mode:
BS1 pulled HIGH
BS0 and BS2 pulled LOW
```

D1 and D2 are shorted for I2C mode.

I2C

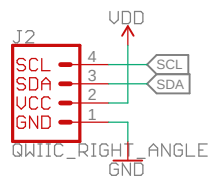
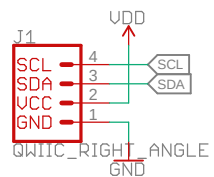
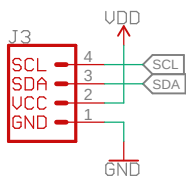


In I2C mode, D/C sets the lower bit of the 7-bit address:

D/C	I2C Address
0	0x3C
1	0x3D (Default)



Connectors / Breakouts



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TITLE: Qwiic_OLED_1.3in



Design by: Paul Clark

REV:
v10

Date: 05/09/2023 08:52

Sheet: 1/1