OLED (SSD1306)

VDD

R2 10k

RESET

C7 1.0uF

GND

VDD

C4 1.0uF   C6 1.0uF

GND   GND

VDD

OLED

VBAT
VDD
IREF
VCOMH
VCC
BS0
BS1
BS2
GND
GND
VLSS
VSS
C2P
C2N
C1P
C1N

D0/SCL
D1/SDA
D2/SDA
D3
D4
D5
D6
D7
CS
RESET
D/C
R/W
E/RD

SCL
SDA

RESET
D/C

GND

OLED

OLED1.3

R1 390k

C5 4.7uF   C1 2.2uF

GND   GNDGND

C3 1.0uF

C2 1.0uF

GND

For I2C mode:
BS1 pulled HIGH
BS0 and BS2 pulled LOW

D1 and D2 are shorted
for I2C mode.

I2C

VDD

Open jumpers to
disconnect pull-ups.

I2C

R13 2.2k   R14 2.2k

SDA   SCL

VDD

R3 10k

D/C

ADDR

GND
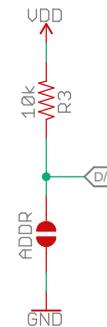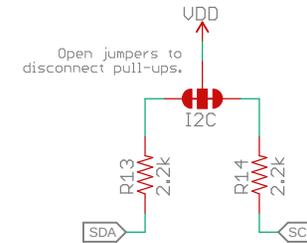
In I2C mode, D/C sets the lower
bit of the 7-bit address:

D/C   I2C Address
==================
0   0x3C
1   0x3D (Default)

Connectors / Breakouts

J3

VDD

4 SCL
3 SDA
2 VCC
1 GND

SCL
SDA

GND

J1

VDD

4 SCL
3 SDA
2 VCC
1 GND

SCL
SDA

GND

QWIIC_RIGHT_ANGLE

J2

VDD

4 SCL
3 SDA
2 VCC
1 GND

SCL
SDA

GND

QWIIC_RIGHT_ANGLE

qwiic

open hardware

sparkfun
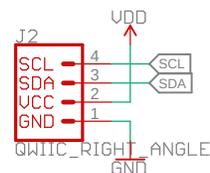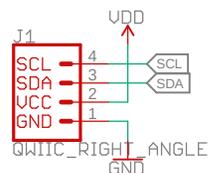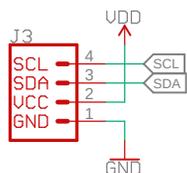
TITLE: Qwiic_OLED_1.3in

Design by: Paul Clark

REV: v10

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