

UM980eb V1.0

[illegible]

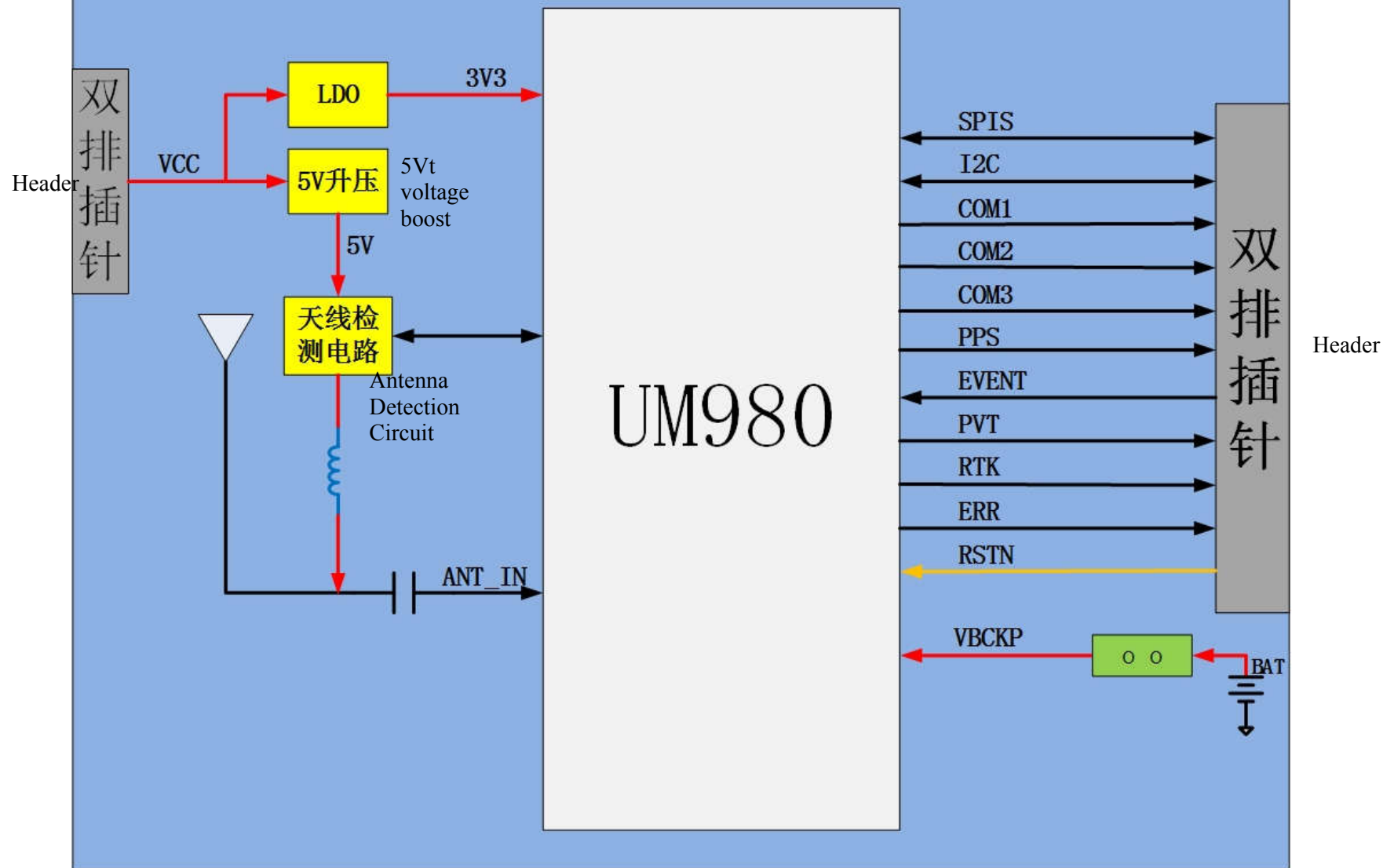
Author	Chen Huajun, Yang Shaozhuang	Date	Nov. 25, 2021
Approved	Zheng Ying	Date	Nov. 25, 2021
Approved	WangPeng, Wu Wenlong	Date	Nov. 25, 2021

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Title			
UM980eb			
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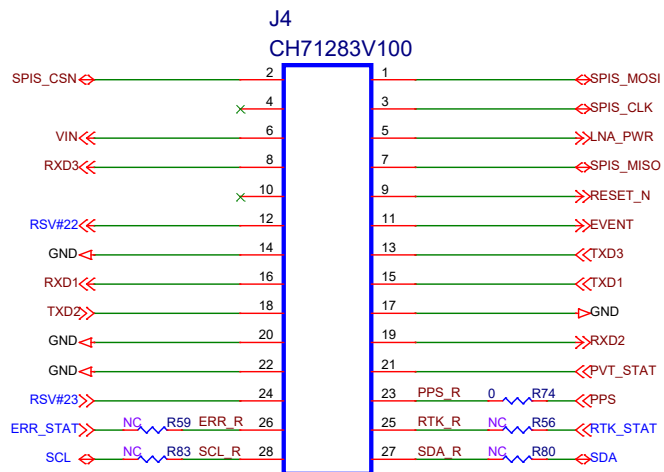
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Function Diagram Block

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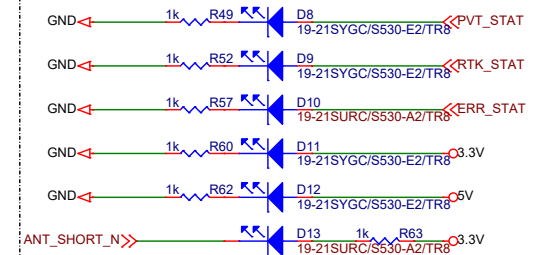
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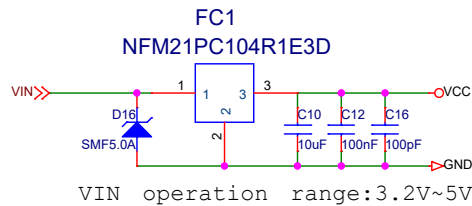
Interface



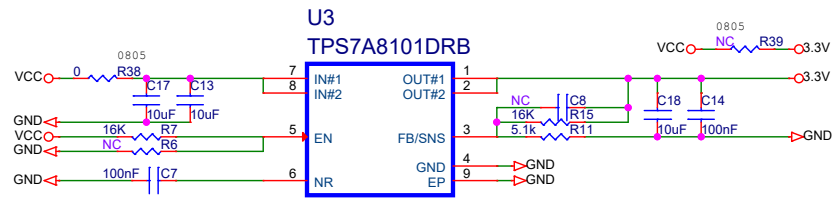
LEDs



LEDs



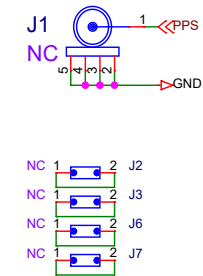
Power Filter



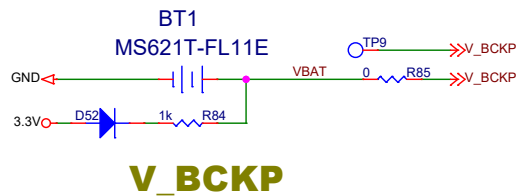
$$V_{out} = 0.8 * (1 + 16 / 5.1) = 3.31V$$

$$\text{Dropout Voltage: } 170mV @ 1A$$

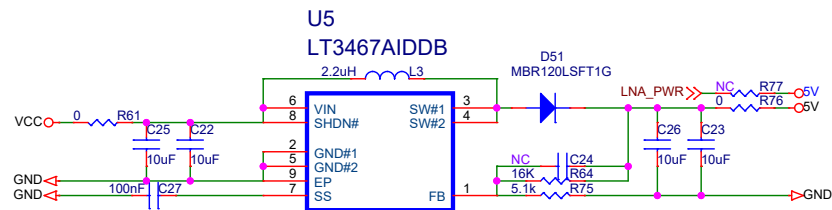
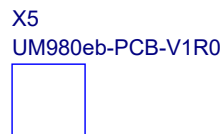
LDO 3.3V



Debug3

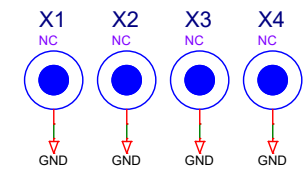


V_BCKP



$$V_{out} = 1.225 * (1 + 16 / 5.1) = 5.07V$$

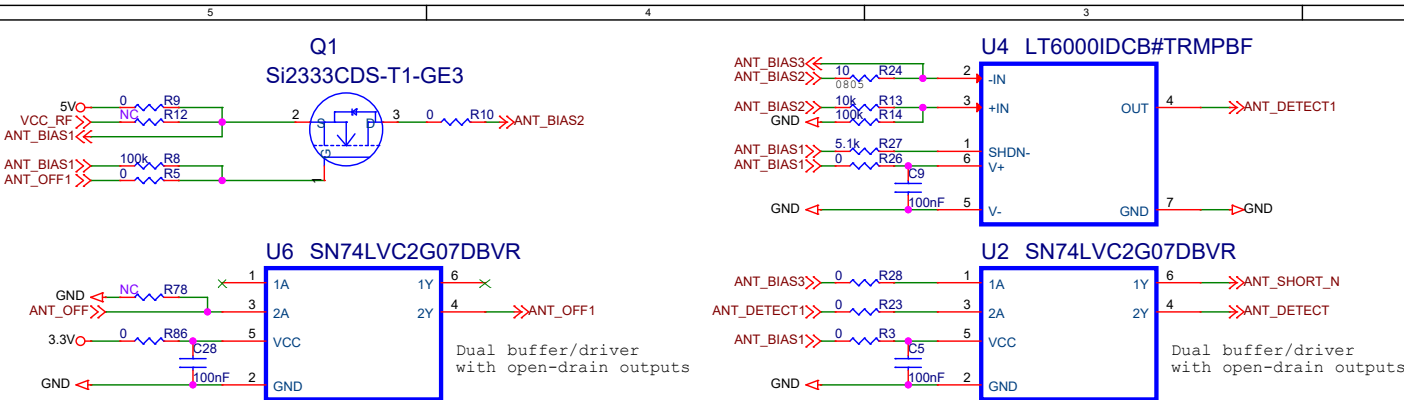
DC/DC 5V



Hole

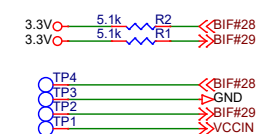
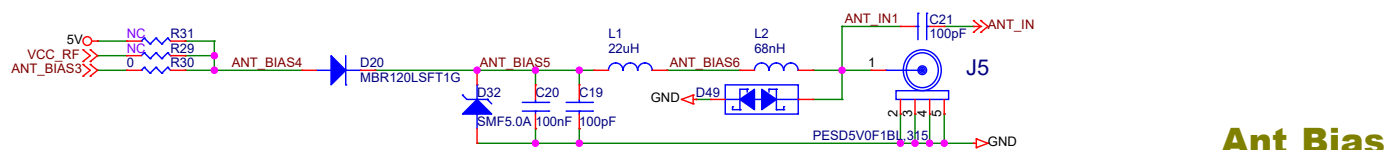
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B	Interface Power LEDs		
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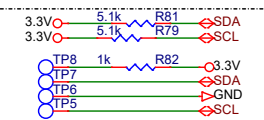


ANT-OFF	ANT-DETECT	ANT-SHORT-N	
1	X	X	Ant-Bias Turn off
0	0	0	Circuits Maybe error
0	0	1	Ant-Bias Current < 50mA
0	1	0	Ant-Bias Short to GND
0	1	1	Ant-Bias Current > 50mA

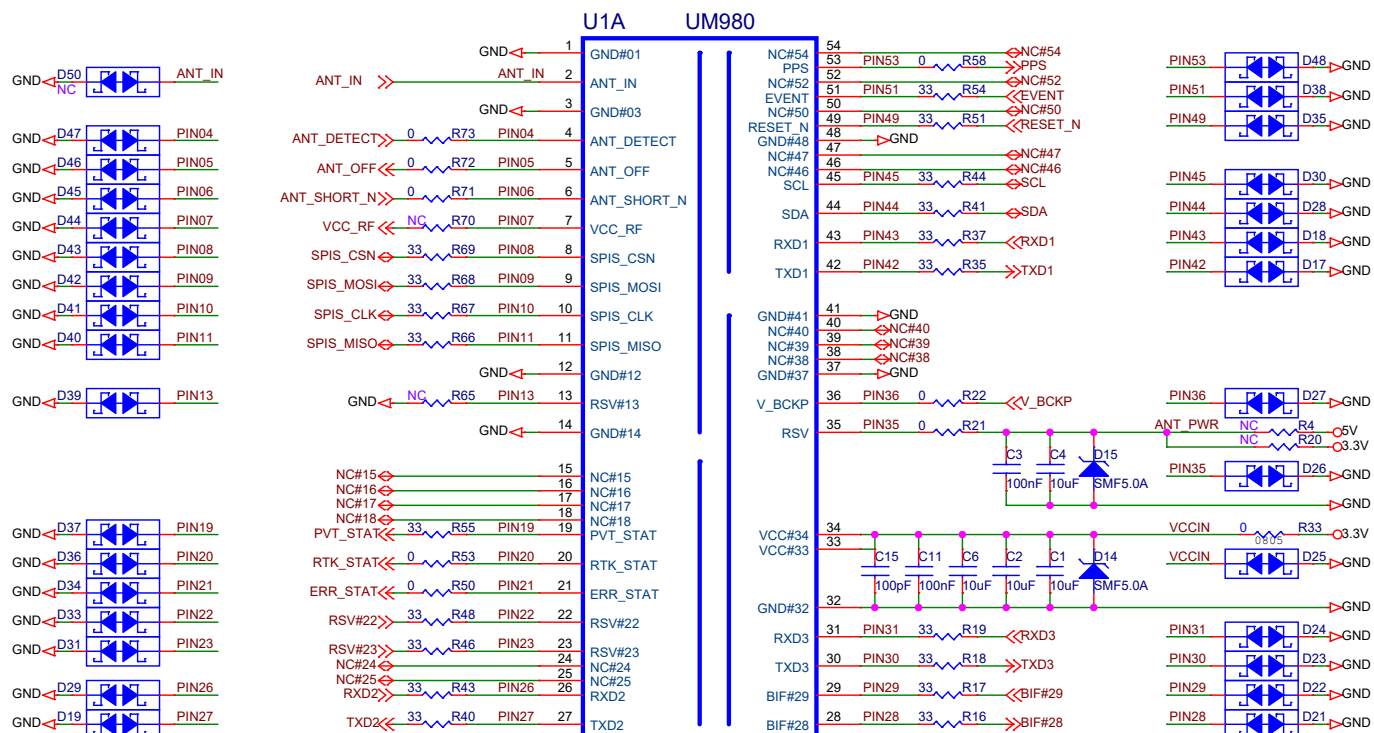
Ant Bias Status Detect



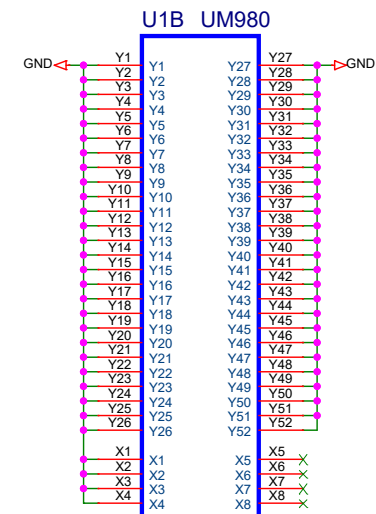
Debug1



Debug2



UM980 Module



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Revision History

Data	Revision	Change
Nov 25, 2021	Rev 1.0	Init Version