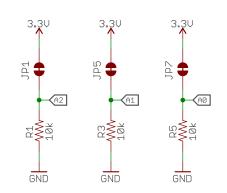
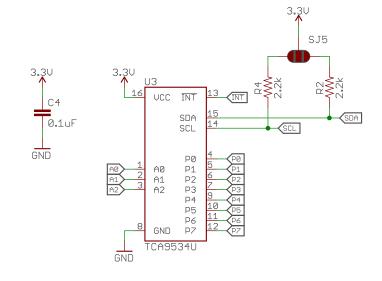


Address Bit Selection



A2	A1	A0	Address
0	0	0	0×20
0	0	1	0×21
0	1	0	0×22
0	1	1	0×23
1	0	0	0×24
1	0	1	0×25
1	1	0	0×26
1	1	1	0×27







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TITLE: Qwiic_GPIO

Design by:Kevin Kuwata

REV: X10

Date: 4/24/2018 4:38 PM

Sheet: 1/1