U2 — GRIDEYE

Pin	Name		Name	Pin
9	VDD		SCL	3
13	VPP		SDA	2
12	DVDD		ADDR	5
10	AVDD		INT	4
6	GND			

3.3V

C3 10uF
C4 0.1uF
C2 2.2uF
C1 1.0uF
R1 20

GND

R4 4.7k
R5 4.7k
R2 10k

JP7

SCL
SDA
INT

SJ1

7-Bit I2C Address Jumper
Open (default) = 0x3B
Closed = 0x3A

GND

J2 (SMD)

Pin	Name
4	SCL
3	SDA
2	VCC
1	GND

3.3V
SCL
SDA
GND

J3 (PTH)

Pin	Name
4	SCL
3	SDA
2	VCC
1	GND

3.3V
SCL
SDA
GND

J4 (SMD)

Pin	Name
4	SCL
3	SDA
2	VCC
1	GND

3.3V
SCL
SDA
GND

J1

INT

open hardware

SPARK X

TITLE: Qwiic_Grideye

Design by: N. Seidle

REV: X10

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