

The diagram shows a 3.3V voltage regulator (U3) and a 1.0uF capacitor (C8). The regulator has pins for VCC, IN, OUT, EN, and GNDNC. The input (IN) is connected to a 3.3V source. The output (OUT) is connected to a 3.3V output. The enable pin (EN) is connected to GND. The GNDNC pin is marked with a red 'X', indicating it should not be connected. A 1.0uF capacitor (C8) is connected between the output (OUT) and GND.

