EZ Connectors

IN
3.3V
PROC_PWR_EN

Bottom

1
2
3
4 SCLK
5 COPI
6 CIPO
7 CS0
8 CS1
9 CS2
10 GPIO0
11 GPIO1
12 SDA
13 SCL
14
15 V_USB
16

FPC_16_0.5MM_BACKFLIP
GND

OUT
3.3V
PROC_PWR_EN

Top

SCLK 1
COPI 2
CIPO 3
CS0 4
CS1 5
CS2 6
GPIO0 7
GPIO1 8
SDA 9
SCL 10
11
12
13
V_USB 14
15
16

FPC_16_0.5MM_BACKFLIP
GND

Disable Processor Board Regulator

PROC_PWR_EN

PROC_PWR_EN
GND

Boost Converter

Caution
=====
Ensure the battery polarity is correct
There is no reverse-polarity protection on this board

Recommended VIN (Min) : 1.5V (1 x AAA)
Recommended VIN (Max): 3.0V (2 x AAA)
Maximum VIN: 5.5V

VIN_
VIN
L2
4.7µH
3.3V

J1
GND
C1
10uF
R3
453k
C2
0.1uF
3V3_EN
UVLO 1.0V

U2
VIN   L
EN   VOUT
PS   VAUX
UVLO   FB
GND

TPS61200

R4
150K
GND
GND

C4
0.1uF
GND

FB
R1
1M
GND

C6
0.1uF
GND

C3
22uF
GND

R2
180k
GND

ATtiny43

AVR_SPI_PROG_3X2TESTPOINTS
J3
AT_CIPO   CIPO 1 2 VCC   AT_VCC
SCL   SCK   3 4 CIPO
RESET   RST   5 6 COPI SDA
GND
GND
AT_VCC

U1
D0   RESET/D15   RESET
D1   D14
D2   RXD/D13   RXD
D3   TXD/D12   TXD
SDA   D4/DI/COPI/SDA A3/D11   3V3_EN
AT_CIPO   D5/DO/CIPO   A2/D10
SCL   D6/SCK/SCL   A1/D9   VIN
D7/SS/INT0   A0/D8
AT_VCC   VCC   VBAT
GND   LSW
GND
ATTINY43_QFN20

1A/23V/620mV
D1
L1
15uH

C7
22uF
C8
0.1uF
GND
C5
4.7uF
GND
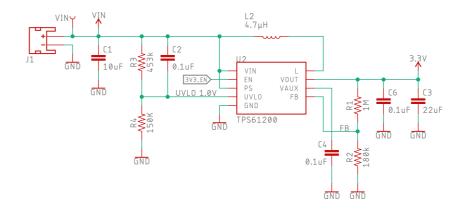
GND

ATtiny43
=======
Pin 4 is COPI and Pin 5 is CIPO when the chip is in peripheral mode or is being programmed
Pin 4 is CIPO and Pin 5 is COPI when the chip is operating in controller mode
The #defines in the ATTinyCore assume controller mode

TITLE: SparkX_EZ_Power_AAA

Design by: Paul Clark

REV:
X01

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