

UCC Range: 3.0 - 3.6V

U4

3.3V 3.3V 3.3V

C10 0.1uF C9 1.0uF C8 1.0uF

GND GND GND

V_BKCP ANT_ZED

33*2 36

2 RF_IN

7 V_RF

4 ANT_DETECT

5 ANT_OFF

6 ANT_SHORT

38

40 V_USB

39 USB_D+ USB_D-

27

R15 27 R16 27

90R

D+_ZED D-_ZED

49 RESET

51

44 R30 33

45 R19 33

46 R20 33

R26 33

42

43 R21 33

47 R22 33

27

26 R17 33

R18 33

20

19 R23 33

53 R24 33

R25 33

50

EXT_INT_ZED

SDA

SCL

TX_RDY

TXD1_ZED

RXD1_ZED

TXD2_ZED

RXD2_ZED

RTK

GEO

TP

SAFEBOOT_ZED

GND SAFEBOOT

U-BLOX_ZED-F9P

7-bit unshifted I2C address: 0x42

UCC Range: 3.0 - 3.6V

3.3V

C11 0.1uF

3.3V

C6 1.0uF

GND

GND

U1

23 VCC

22 V_BKCP

11 ANT_NEO

9 VCC_RF

14 ANT_OFF

15 ANT_DETECT

16 RXD2

17 RESET

18 SDA/C5

19 SCL/SCK

4 TXD2_NEO

5 RXD2_NEO

20 TXD1_NEO

21 RXD1_NEO

7 V_USB

6 TXD1/POCI

5 RXD1/PICO

2 D+ USB

1 D- USB

2 DSEL

1 SAFEBOOT

SAFEBOOT

U-BLOX_NEO-D9S-00B

7-bit unshifted I2C address: 0x43

Cut jumpers to isolate UART2

The diagram shows two headers with their pins connected by jumpers. The top header has TX2 (pin 1) and RX2 (pin 2). The bottom header has RX2 (pin 1) and TX2 (pin 2). A red double-headed arrow indicates the removal of the jumper between TX2 on the top header and RX2 on the bottom header. Another red double-headed arrow indicates the removal of the jumper between RX2 on the top header and TX2 on the bottom header. The text 'Cut jumpers to isolate UART2' is written above the top header.

AP2112K-3.3V

Iout (max): 600mA
 Vin (max): 6.5V
 Vdrop (max): 250mV
 Iq: 55uA

When populating L1:
use an 0603 part with
impedance > 500 Ohms
at 1.56GHz

Cut track to disable
UCC_RF antenna power

Cut jumpers to disable LEDs

LED1 RED PWR R1 1k 3.3V

LED2 BLUE RTK R27 1k RTK

LED3 BLUE GEO R28 1k GEO

LED4 BLUE TP R29 1k TP

The diagram shows two separate circuit sections. The top section is an I2C pullup circuit. It features a 3.3V power supply at the top. Two 2.2k resistors, labeled R7 and R8, are connected in series between the 3.3V supply and the I2C bus lines. R7 is connected to the SCL line, and R8 is connected to the SDA line. A red jumper symbol, consisting of two red circles with a horizontal line, is placed between the two resistors. A text label 'Close jumper to add I2C pullups to bus' points to this jumper. The bottom section shows shield isolation. It has two input lines on the left: SHLD_ZED and SHLD_NEO, both connected to GND. These lines pass through red jumper symbols (two red circles with a horizontal line) and then connect to output lines on the right: SHIELD_ZED and SHIELD_NEO.

0.5A/40V/120mV

D3

3.3V

R32 3.3k

U_BKCP

B1

RELOWABLE_BATTERY_MS421R

The top diagram illustrates the connection between a USB-C port (J3) and a ZED camera module (D2). The USB-C port provides V_USB, D+, D-, CC1, CC2, SHLD, and GND. The ZED module (D2) has pins 1, 2, 3, 4, 5, and 6. The connections are as follows: V_USB to pin 6, D+ to pin 1, D- to pin 2, CC1 to pin 3, CC2 to pin 4, SHLD to pin 5, and GND to pin 3. A 5.1k resistor (R12) is connected between D+ and GND, and a 5.1k resistor (R13) is connected between D- and GND. The ZED module is labeled PRTR5V0U2F.

The bottom diagram illustrates the connection between a USB-C port (J6) and a NEO module (D4). The USB-C port provides V_USB_2, D+, D-, CC1, CC2, SHLD, and GND. The NEO module (D4) has pins 1, 2, 3, 4, 5, and 6. The connections are as follows: V_USB_2 to pin 6, D+ to pin 1, D- to pin 2, CC1 to pin 3, CC2 to pin 4, SHLD to pin 5, and GND to pin 3. A 5.1k resistor (R23) is connected between D+ and GND, and a 5.1k resistor (R24) is connected between D- and GND. The NEO module is labeled PRTR5V0U2F.



Special Instructions

REV:
x01

Sheet: 1/1