

# tb\_fifo.v

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## AUTHORS

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## DATES

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## INFORMATION

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### Brief

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Test bench for fifo using fifo stim and clock stim.

### License MIT

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## tb\_fifo

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```
module tb_fifo #(
  parameter
    IN_FILE_NAME
    =
    in.bin,
  parameter
    OUT_FILE_NAME
    =
    out.bin,
  parameter
    FIFO_DEPTH
    =
    64,
  parameter
```

```
RAND_FULL
=
0
)
(
)
```

Test bench for fifo. This will run a file through the system and write its output. These can then be compared to check for errors. If the files are identical, no errors. A FST file will be written.

**Parameters**

- IN\_FILE\_NAME**      File name for input.  
*parameter*
- OUT\_FILE\_NAME**      File name for output.  
*parameter*
- FIFO\_DEPTH**      Number of transactions to buffer.  
*parameter*
- RAND\_READY**      0 = no random ready. 1 = randomize ready.

**INSTANTIATED MODULES**

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**clk\_stim**

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Generate a 50/50 duty cycle set of clocks and reset.

**write\_fifo\_stimulus**

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Device under test WRITE stimulus module.

**dut**

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Device under test, fifo

**read\_fifo\_stimulus**

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Device under test READ stimulus module.