

## Unit - I Number Systems

### Number System :

In digital electronics, the number system is used for representing the information. The number system has different bases and the most common of them are the decimal, binary, Octal and Hexadecimal. The base or radix of the number system is the total number of the digit used in the number system. Suppose if the number system representing the digit from 0-9 then the base of the system is 10.

### Types of Number Systems :

Some of the important types of Number Systems are

Decimal Number System.

Binary Number System.

Octal Number System.

Hexadecimal Number System.

**Decimal Number Systems:** The number system is having digit 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, this number known as a decimal Number System because total ten digits are involved. The base of the decimal Number System is 10.

**Binary Number Systems:** It is a positional weighted System. The base of this binary number system is 2.

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because it has only two digit 0 and 1.  
The digital electronic equipments are work on  
the binary number system & hence the  
decimal Number System is converted into  
binary System.

Octal Number System : The Octal Number System  
is also positional weighted  
System. The Octal System has the base of  
eight as it uses digits 0, 1, 2, 3, 4, 5, 6, 7.

Hexadecimal Number : The hexadecimal Number  
System has a base of 16.  
uses 16 symbols namely 0, 1, 2, 3, 4, 5, 6, 7, 8, 9,  
A, B, C, D, E, F. The Symbols A, B, C, D, E, F  
represent the 10, 11, 12, 13, 14, and 15 respectively.  
The least Significant position has a weight of  
16<sup>0</sup> i.e. 1. The highest Significant position are  
given weights in the ascending powers of  
i.e. 16<sup>1</sup>, 16<sup>2</sup>, 16<sup>3</sup> ... 16<sup>n</sup>.

Conversion of

10 Decimal

(53.625)

2 53  
2 2  
2 1  
2  
2

(821)

The integer

(53)<sub>10</sub>

0.625  
0.25  
0.50  
0.00

Multiplication

Conversion from One base to another

I<sup>o</sup> Decimal to Binary

$$(53.625)_{10} = \{ \quad \}_{2}$$

System weighted of.	2   53	LSB
	2   26 → 1 ↑	
	2   13 → 0	
	2   6 → 1	
	2   3 → 0	
	1 → 11.1	
	(110101)	MSB

The integer number is 53. The fraction is 0.625

$(53)_{10} \rightarrow (110101)_2$  Since the D.M is a fraction its binary equivalent is obtained by multiplying the Number continuously by 2,

$$0.625 \times 2 = 1.25 \rightarrow 1 \text{ MSB}$$

$$0.25 \times 2 = 0.50 \rightarrow 0$$

$$0.500 \times 2 = 1.00 \rightarrow 1$$

$$0.00 \times 2 = 0.00 \rightarrow 0$$

Multiplication by 2 is not possible since the product is 0.

$$(0.625)_{10} = (101)_2$$

$$(53.625)_{10} = (110101.101)_2$$

(b) Decimal to Octal

$$(444.96)_{10} \rightarrow (674.75341)_8$$

$$\begin{array}{r} 8 | 444 \\ 8 | 55 \rightarrow 4 \\ 8 | 6 \rightarrow 7 \end{array}$$

Fractional part

$$0.96 \times 8 = 7.68 \rightarrow 7 \text{ MSB}$$

$$0.68 \times 8 = 5.44 \rightarrow 5$$

$$0.44 \times 8 = 3.52 \rightarrow 3$$

This process will

Continue further, so

May take the result

up to 5 places

$$(0.96)_{10} = (75341)_8$$

II (a) Binary

$$(101.01)_2$$

$$1 \times 2^2 +$$

4 +

(b) Binary

The binary number

(11

(c) Decimal to Hexadecimal

$$(2586)_{10} \rightarrow (ATA)_{16}$$

$$\begin{array}{r} 16 | 2586 \\ 16 | 161 \rightarrow 10-A \\ 16 | 10 \rightarrow A \end{array}$$

$$(7864)_{10} \rightarrow (EB8)_{16}$$

$$\begin{array}{r} 16 | 7864 \\ 16 | 491 \rightarrow 8 \\ 16 | 30 \rightarrow B \\ 1 \rightarrow E \end{array}$$

(c) Bi

The binary number

(11

80

$$2^n - \dots \quad 2^4 \ 2^3 \ 2^2 \ 2^1 \ 2^0$$

16    8    4    2    1

II (a) Binary to Decimal

$(101.01)_2 = (5.25)_{10}$  - A Binary number can be converted into decimal.

$$\begin{array}{r} 1 \ 0 \ 1 \ . \ 0 \ 1 \\ \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \\ 1 \times 2^2 + 0 \times 2^1 + 1 \times 2^0 + 0 \times 2^{-1} + 1 \times 2^{-2} \end{array} \quad \begin{array}{l} \text{Number by multiplying} \\ \text{the binary number} \\ \text{by their weight} \end{array}$$

$$4 + 0 + 1 + 0 + 0.25$$

(b) Binary to Octal :

The binary Number can be Converted into octal number by making group of 3-bits.

$$(110101 \cdot 101010)_2 = (65.52)_8$$

$$\begin{array}{r} 4_2 \ 4_2 \ 1 \quad 4_2 \ 4_2 \ 1 \\ \overline{1 \ 1 \ 0 \ 1 \ 0 \ 1} \cdot \overline{1 \ 0 \ 1 \ 0 \ 1 \ 0} \\ \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \\ 6 \quad 5 \quad (5) \quad 2 \end{array} \quad \begin{array}{l} 1+4 \\ 1+4+8 \\ 1+8+8+1 \\ 1+1+8 \\ 1+1+1+8 \\ 1+1+1+1+8 \end{array}$$

(c) Binary to Hexadecimal

The binary number can be Converted into hexadecimal number by making group of 4-bits

$$(1101\ 0010\ 110\ 1110\ 010)_2 = (34B75)_{16}$$

$$\begin{array}{r} 8^4 \ 2 \quad 8^4 \ 2 \quad 1 \quad 8^4 \ 2 \quad 1 \quad 8^4 \ 2 \quad 1 \\ \overline{0 \ 0 \ 1 \ 1 \ 0 \ 0 \ 1 \ 0 \ 1 \ 1 \ 0 \ 1 \ 1 \ 0 \ 1 \ 0 \ 1} \\ \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \\ 3 \quad 4 \quad B \quad 7 \quad 5 \end{array}$$

### III (a) Octal to Binary

For obtaining the binary equivalent of an Octal number each significant digit in the given number is replaced by 3-bit binary equivalent.

$$(376)_8 = \begin{array}{r} 3 \quad 7 \quad 6 \\ - 011 \quad 111 \quad 110 \end{array}$$

$$(376)_8 = (01111110)_2$$

### (b) Octal to Decimal.

$$\bullet (237)_8 = (2 \times 8^2 + 3 \times 8^1 + 7 \times 8^0)_{10} = (2 \times 64 + 3 \times 8 + 7)_{10} = 128 + 24 + 7 = 159$$

$$2 \times 8^2 + 3 \times 8^1 + 7 \times 8^0 = (1 \ 1 \ 0 \ 1 \cdot 1 \ 1 \ 0 \ 1)_2$$

$$2 \times 64 + 3 \times 8 + 7 = 128 + 24 + 7$$

$$= 159$$

$$(c) \text{ Hexadecimal to Octal} - (46)_{16} = (10110)_8$$

Convert the Hexadecimal into Binary

$$(46)_{16} = (100110)_2$$

Form group of 3 bits -  $\begin{array}{r} 1 \ 0 \ 0 \ 1 \ 1 \ 0 \\ \hline 1 \ 0 \ 0 \ 1 \ 1 \ 0 \end{array}$

$$= (101)_8$$

$$\begin{array}{r} 8 \ 4 \ 2 \ 1 \ 8 \ 4 \ 2 \ 1 \\ \hline 0 \ 0 \ 1 \ 0 \ 0 \ 1 \ 1 \ 0 \\ \hline 7 \qquad 2 \qquad 6 \end{array}$$

$$(345)_8$$

### IV (a) Hex

$$\begin{array}{r} (2A59) \\ \downarrow \\ 2 \times 16^3 + \end{array}$$

### (b) Hexad

For obtain  
Number  
is 46

$$(2A5)_{16}$$

$$(2D5)_{16}$$

### (c) He

$$(47)_{16}$$

Form

$$(345)_8 =$$

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Page \_\_\_\_\_

#### IV (a) Hexadecimal to Decimal

number  
placed

$$(2A5)_{16} = (10,953)_{10}$$

$$\begin{array}{r} 2 \times 16^3 + 10 \times 16^2 + 12 \times 16^1 + 5 \times 16^0 \\ \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \\ = 10,953 \end{array}$$

#### (b) Hexadecimal to Binary

For obtaining the binary equivalent of hexadecimal Number each significant digit in the given number is replaced by 4-bit digit. Number

$$(2A5)_{16} = 0101001010101010_2$$

$$(2A5)_{16} = 0010\ 0101\ 1101\ 0101_2$$

$$(2A5)_{16} = (00101101010101)_2$$

#### (c) Hexadecimal to Octal - $(47)_{16} = (107)_8$

Convert the hexadecimal into Binary

$$(47)_{16} = (010\ 0111)_2$$

Form group of 3-bits -  $0\ 0\ 1\ 0\ 0\ 0\ 1\ 1\ 1$

$$\begin{array}{r} 4 \times 8^3 + 6 \times 8^2 \\ 32 + 6 \quad 38 \\ \hline 2 \times 8^3 + 6 \times 8^2 \\ 16 + 6 \quad 22 \end{array}$$

### Binary Coded Decimal (BCD) :

The BCD uses the binary number system to specify the decimal numbers 0 to 9. It has four bits. The weights are assigned according to the position occupied by these digits. The weight of the 1st position is  $2^0$ , the second  $(2)^1$ , the third  $(2)^2$  4, and the fourth  $(2)^3$  8. Reading from left to right the weights are 8-4-2-1 & hence it is called 8421 code.

The binary equivalent of 7 is  $[111]_2$ , but the same number is represented in 4-bit form as 0111. Also the numbers from 0 to 9 are represented in the same way as in the binary system, but after 9 the representation in BCD are different. For ex -

(12) in the binary system is  $[1100]_2$  but the same number is represented as [0001 0010] in Gray Code

Ex- Give the BCD code for the decimal Number 874

Decimal Number - 874

BCD Code - 1000 0111 0100

$(874)_{10} = (1000\ 0111\ 0100)_{BCD}$

## Excess - 3 Code :

Specify  
bits  
position  
the  $\pi$

The excess-3 represents a decimal number, in binary form, as a number greater than 3. An excess-3 code is obtained by adding 3 to a decimal Number.

[643]<sub>10</sub> in to excess-3 code

$$\begin{array}{r}
 \text{Decimal number} \quad 6 \quad 4 \quad 3 \\
 \text{Add } 3 \text{ to each digit} \quad 6 + 3 \quad 4 + 3 \quad 3 + 3 \\
 \text{Sum} \quad 9 \quad 7 \quad 6
 \end{array}$$

Converting the above Sum into BCD code  
Sum  $\rightarrow$  9 7 16 1 1

Sum → 9 7 16 1 1

~~600~~ ~~at firm~~

Excess-3 code for [643]<sub>10</sub> - 100101110110

Gray Cocks :

but 874

The Gray Codes belongs to a class of Codes called minimum-change Codes, in which only one bit in the Code group changes when moving from one step to the next. The Gray Code is a non-weighted Code. The Gray Code is a reflective digit Code which has a special property of two adjacent Code numbers that differ by only 1-bit. Therefore, it is also called a Unit-distance Code.

(1010110) =  
(1111101)

Conversion from

1. The first binary  
the first Gray

2. If the second  
bit is the 3rd

If the second  
bit is the 8th

3. Step 2 is

(110101) Gray

and write

Step-1 is 1101

↓

1101

Step-2 The

therefore the 2

inverse of the

↓

1

Step 3 The

the third

that of the

Ex- [10110] to Gray Code

Step 1 The first bit MSB of the Gray code is  
1 0 1 1 0. Binary is the same as the first  
bit of the binary no.

↓  
Gray

Step 2 Add the 1st bit of the binary to 2nd digit no.  
Add 1 + 0 - 1. The result is the 2nd bit of the binary

$$\begin{array}{r} 1 \oplus 0 \\ \hline 1 \end{array}$$

Binary  
Gray

Step 3 Add 2nd bits to 3rd bits of the binary

$$\begin{array}{r} 1 \boxed{0} \oplus 1 \\ \hline 1 \end{array}$$

Step 4 Add 3rd bit 1 to the 4th bit 1 & omit the carry

$$\begin{array}{r} 1 \boxed{1} \oplus 0 \\ \hline 1 \end{array}$$

Step 5 Add 4th bit to the last bit of the word

$$\begin{array}{r} 1 \boxed{1} \oplus 0 \\ \hline 1 \end{array}$$

$$(10110) \rightarrow (11101)_{\text{Gray}}$$

$$(10101101)_2 = C_{16}$$

$$C11111011.$$

10

Conversion from gray Code to Binary

1. The first binary bit (MSB) is same as that of the first Gray Code bit.

2. If the second Gray bit is 0, the second binary bit is the same as that of the first binary.

If the second gray bit is 1, the second binary bit is the inverse of its first binary bit.

3. Step 2 is repeated for each successive bit.

$$(110101)_\text{Gray} = (101011)_\text{Binary}$$

Step-1: 1 0 1 0 1 → Gray of the Gray code  
↓  
1 0 1 0 1 → Binary

Step-2: The Second bit of Gray Code is 1  
therefore the 2nd bit of the binary is 0.  
inverse of the 1st binary bit '1'

$$\begin{array}{r} 1 1 0 1 0 1 \\ \downarrow \downarrow \\ 1 0 \end{array} \quad \begin{array}{l} \text{Gray} \\ \text{Binary} \end{array}$$

Step-3: The 3rd of the Gray Code is 0, therefore the third bit of the binary is same as that of the second binary bit 0

$$110101$$

$$10 \rightarrow 0$$

## Arithmetic Op

### Binary Arith

The arithmetic  
Multiplication  
below  
Addition

$$0 + 0 = 0$$

$$0 + 1 = 0$$

$$1 + 0 = 0$$

$$1 + 1 = 10$$

### Binary Addit

### Binary Subtr

### Binary Mult

**ASCII Code**: A standardised code that has been widely accepted by the industry, the ASCII (pronounced "ass-kee") Code - American Standards Code for Information Interchange, used in most computers by its manufacturers. The ASCII Code represents a character with seven bits, which can be stored as one byte with one bit unused. The format of ASCII Code is  $x_6 x_5 x_4 x_3 x_2 x_1 x_0$  where each bit is a 0 or a 1.

**EBCDIC Code**: The full form of EBCDIC code is Extended Binary Coded Decimal Information Code. It differs from ASCII only in its code grouping for the different alphanumeric characters. It uses each eight bits for each character & a ninth bit for parity.

## Arithmetic Operation

### Binary Arithmetic

The arithmetic rules for Addition, Subtraction, Multiplication and Division of Binary numbers are given below

Addition	Subtraction	Multiplication	Division
----------	-------------	----------------	----------

$$0+0=0 \quad 0-0=0 \quad 0 \times 0=0 \quad 0 \div 1=0$$

$$0+1=1 \quad 1-0=1 \quad 0 \times 1=0 \quad 0 \div 1=1$$

$$1+0=1 \quad 1-1=0 \quad 1 \times 0=0 \quad 0 \div 0=\text{Not allowed}$$

$$1+1=10 \quad 1-1=0 \quad 1 \times 1=1 \quad 1 \div 0=\text{Not allowed}$$

### Binary Addition

$$\begin{array}{r} 1111 \\ + 0101 \\ \hline 1000 \end{array}$$

$$\begin{array}{r} 1101 \\ - 1001 \\ \hline 0100 \end{array}$$

$$1011 \times 1101$$

$$\begin{array}{r} 0101011 \\ \times 1101 \\ \hline 1011000 \\ 1011 \times x \\ \hline 1100111 \end{array}$$

Binary Division  $11001 \div 101$

$$\begin{array}{r} 101 \\ 101 \sqrt{11001} \\ 101 \\ \hline 0101 \\ 101 \\ \hline 0000 \end{array}$$

1's Complement

Subtraction of a number from another can be accomplished by adding the complement of the Subtrahend to the Minuend.

Ex Subtract (1010), from (1111), using the 1's Complement Method

$$\begin{array}{r} 111 \\ + 0101 \\ \hline 10010 \end{array}$$

Carry - 0 1 0 1 ← 1's Complement

$$\begin{array}{r} 111 \\ + 0100 \\ \hline 1011 \end{array}$$

Add Carry  $\begin{array}{r} 0101 \\ + 1 \\ \hline 0110 \end{array}$

Direct Subtraction

$$\begin{array}{r} 1111 \\ - 1010 \\ \hline 0101 \end{array}$$

$$\begin{array}{r} 1111 \\ \times 1010 \\ \hline 1111 \\ 0000 \\ \hline 10011 \end{array}$$

2's Complement

Subtract (1010), using 2's Complement

$$\begin{array}{r} 1010 \\ + 1101 \\ \hline 0101 \end{array}$$

1's Complement

2's Complement

$$\begin{array}{r} 1111 \\ + 0111 \\ \hline 0100 \end{array}$$

Carry → 0 1 0

Direct Subt

9's Compl

Find the  
(a) 19

(a) 99

$$\begin{array}{r} - 19 \\ \hline 80 \end{array}$$

(b) 99

$$\begin{array}{r} 14 \\ \hline 85 \end{array}$$

## 2's Complement

Subtract  $(1010)_2$  from  $(1111)_2$  using 2's Complement Method

$$\begin{array}{r} 1010 \\ - 0101 \\ \hline 0101 \end{array}$$

$$\begin{array}{r} 0101 \\ + 1 \\ \hline 0110 \end{array}$$

The Carry is discarded. Thus the answer is  $(0101)_2$

Direct Subtraction from  $(1111)_2$

$$\begin{array}{r} 1010 \\ - 0101 \\ \hline 0101 \end{array} \quad (\text{d}) \quad \text{P} \quad (\text{p})$$

## 9's Complement

Find the 9's Complement

$$(a) 19 \quad (b) 146 \quad (c) 469 \quad (\text{d}) 0$$

$$(a) 99 \quad (\text{d}) 0 \rightarrow 1$$

$$\begin{array}{r} - 19 \\ \hline 80 \end{array} \rightarrow 9's \text{ Complement of } 19 \quad (\text{d})$$

$$(b) 999 \quad (\text{d}) 0 \rightarrow 82$$

$$\begin{array}{r} - 146 \\ \hline 853 \end{array} \rightarrow 9's \text{ Complement of } 146 \quad (\text{d})$$

Ex Subtract 18 - 06 using 9's Complement

Regular Subtraction

$$\begin{array}{r} 99 \\ - 06 \\ \hline 93 \leftarrow 9\text{'s Complement of } 06 \end{array}$$

$$\begin{array}{r} 18 \\ - 06 \\ \hline 12 \end{array}$$

$$\begin{array}{r} 18 \\ + 93 \leftarrow 9\text{'s Complement of } 6 \\ \hline 1111 \end{array}$$

$\xrightarrow{+1}$  Add Carry to Result

10's Complement is used at

The 10's Complement of a decimal Number is equal to its 9's Complement + 1.

$$(a) 9$$

$$(b) 46 \rightarrow 0101$$

$$\begin{array}{r} 9 \\ - 9 \\ \hline 0 \end{array}$$

$0 \leftarrow 9\text{'s Complement of } 9$

$$\pm 1$$

$\underline{-1} \leftarrow 10\text{'s Complement of } 9$

$$(b) \begin{array}{r} 99 \\ - 46 \\ \hline \end{array}$$

$$\begin{array}{r} 53 \\ + 1 \\ \hline \end{array}$$

$54 \leftarrow 10\text{'s Complement of } 46$

Subtract

$$9 - 4$$

$$\begin{array}{r} 9 \\ - 4 \\ \hline 5 \end{array}$$

Logic G

A Logic gate makes logic

OR Gate

Truth Table

Input

$$\begin{array}{cc} A & B \\ 0 & 0 \\ 0 & 1 \\ 1 & 0 \\ 1 & 1 \end{array}$$

transistor AND

AND - G

IIP

$$\begin{array}{cc} A & B \\ 0 & 0 \\ 0 & 1 \\ 1 & 0 \\ 1 & 1 \end{array}$$

Subtract 9 - 4 using 10's Complement

$$9 - 4$$

$$\begin{array}{r} 9 \\ - 4 \\ \hline 5 \end{array} \quad \begin{array}{r} 9 \\ + 6 \\ \hline 15 \end{array} \leftarrow \text{10's Complement of 4.}$$

(1) 5 Drop Carry

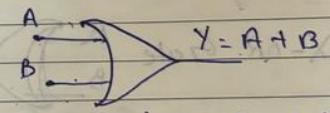
### Logic Gates

A Logic gate is an electronic circuit which makes logical decision

OR Gate : The OR-gate performs logical addition.

Truth Table

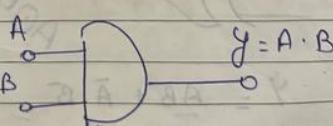
Input	0/P	$Y = A + B$
A 0 B 0	0	0
0 0 B 1	0	0
0 1 B 1	1	1
1 0 B 1	1	1
1 1 B 1	1	1



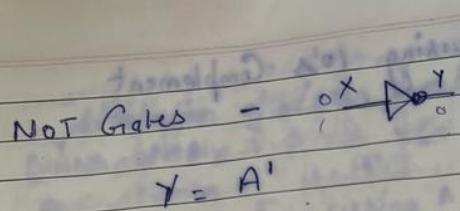
Logic Symbol

AND - Gate : The AND-gate performs logical multiplication.

I/P	O/P
A 0 B 0	$Y = A \cdot B$
0 0 B 0	0
0 1 B 0	0
1 0 B 0	0
1 1 B 1	1



NOT Gates



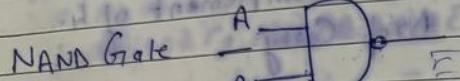
$$Y = A'$$

i/p o/p

A	Y
0	1
1	0

Basics Laws

NAND Gate

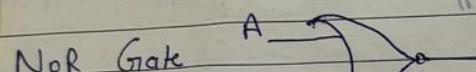


$$Y = \overline{A \cdot B}$$

i/p o/p

A	B	Y
0	0	1
0	1	0
1	0	0

NOR Gate

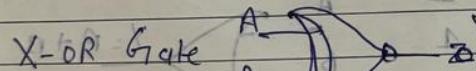


$$Y = \overline{A + B}$$

i/p o/p

A	B	Y
0	0	1
0	1	0
1	0	0

X-NOR Gate

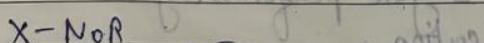


$$Y = \overline{\overline{A}B + A\overline{B}}$$

i/p o/p

A	B	Y
0	0	1
0	1	0
1	0	0

X-NOR



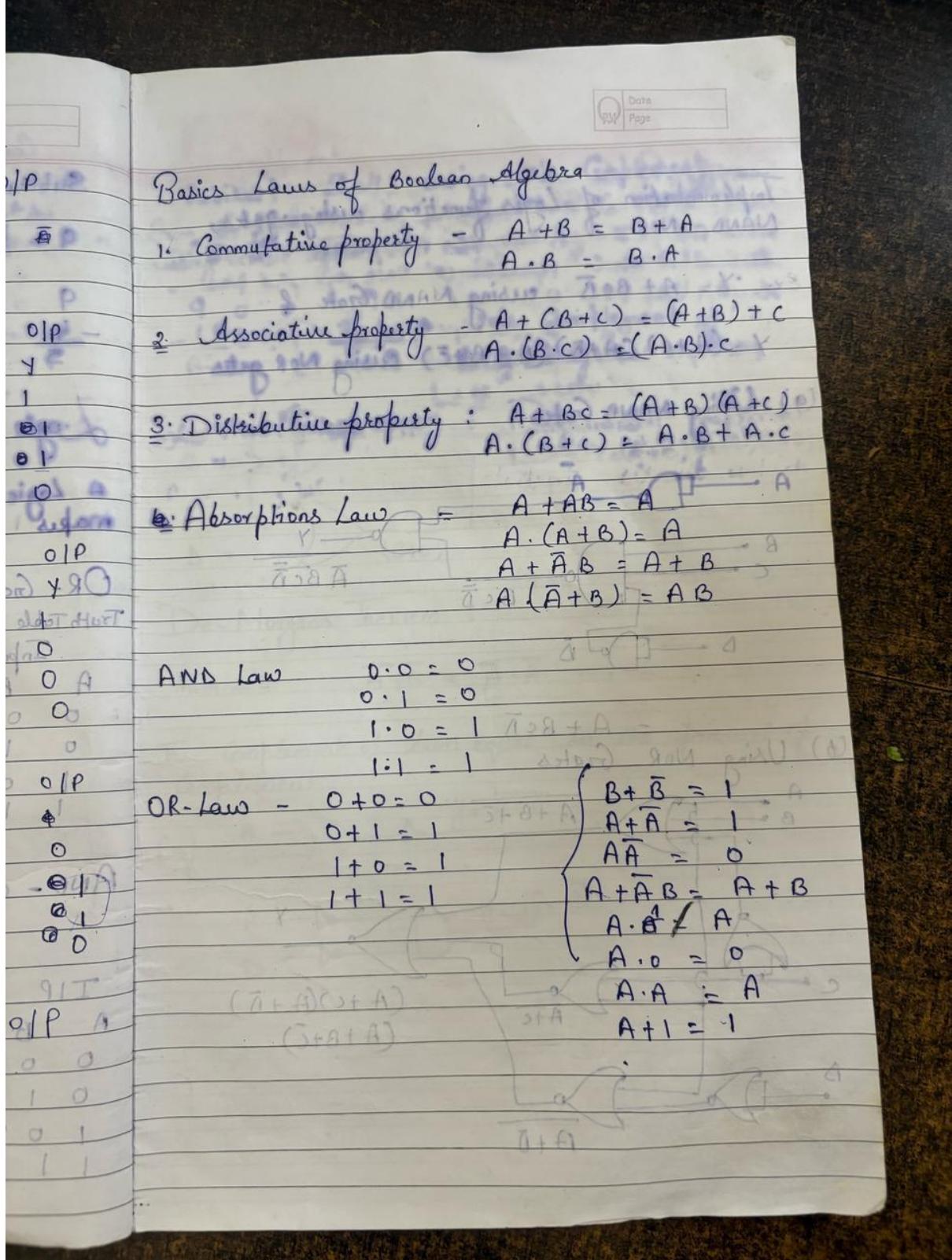
$$Y = \overline{AB} + \overline{\overline{A}B}$$

i/p o/p

A	B	Y
0	0	1
0	1	0
1	0	0
1	1	1

AND Law

OR-Law -

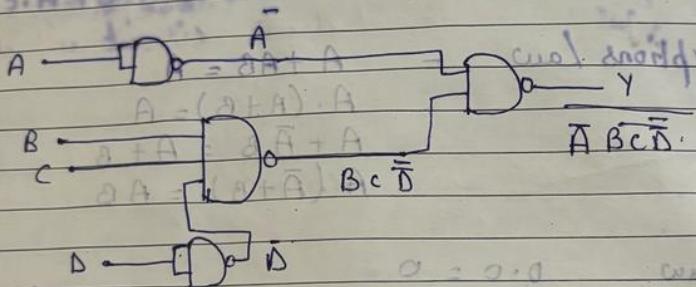


Implementation of Logic functions using gates,  
NAND - NOR

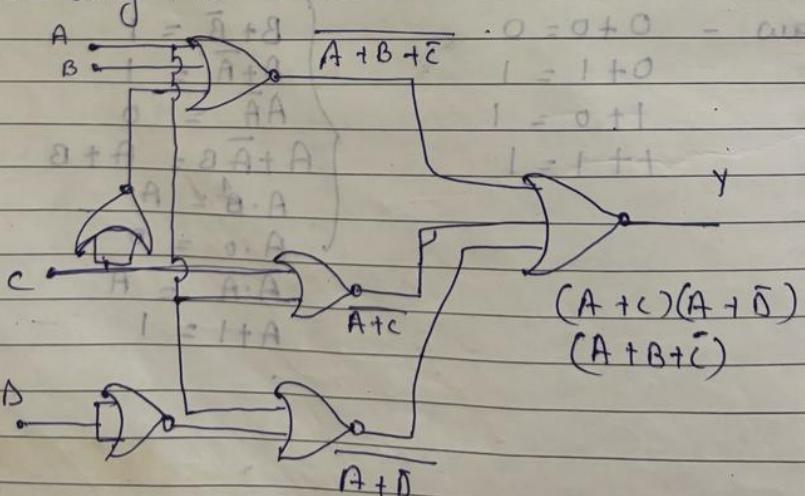
Ex.  $y = A + BC\bar{D}$  using NAND Gate &

$y = (A+C)(A+\bar{B})(A+B+\bar{C})$  using NOR gates

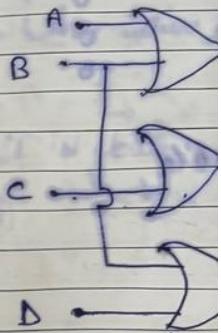
(a) Using NAND Gates



(b) Using NOR Gates



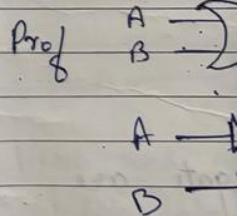
Ex. Realise the  
using basic ga



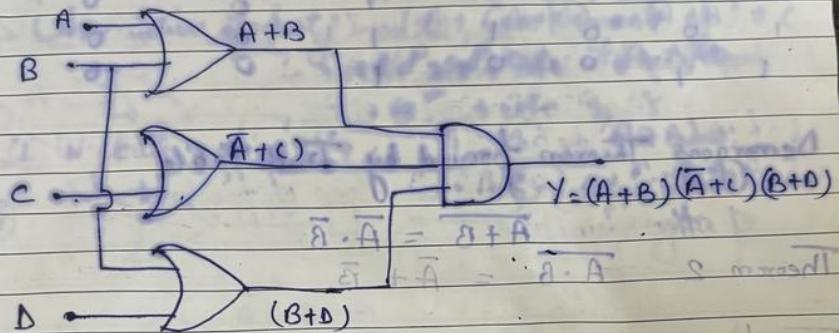
De-Morgan's

Theorem 1

The Complement  
Complement



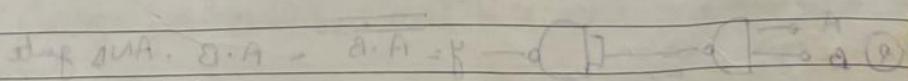
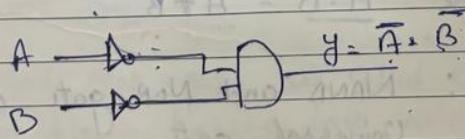
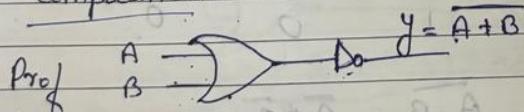
Ex → Realise the logic expression  $Y = (A+B)(\bar{A}+C)(B+D)$  using basic gates.



De-Morgan's Theorem :

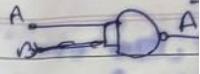
Theorem 1  $\overline{A+B} = \overline{\overline{A} \cdot \overline{B}}$

The Complement of sum equal to the product of Complement

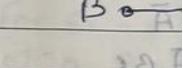
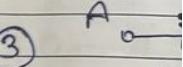
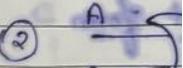
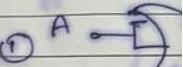


Truth Table

A	B	$\bar{A}$	$\bar{B}$	OR $A + B$	$\overline{A+B}$	$\bar{A} \cdot \bar{B}$
0	0	1	1	0	1	1
0	1	1	0	1	0	0
1	0	0	1	1	0	0
1	1	0	0	1	0	0

 $A + B$  $\overline{A} \cdot \overline{B}$ 

NOR Gate



Demorgans Theorem proved by Truth Table

$$\overline{A+B} = \overline{A} \cdot \overline{B}$$

$$\text{Theorem 2 } \overline{A \cdot B} = \overline{A} + \overline{B}$$

The product of Complement equal to sum of the Complement.

A	B	$\bar{A}$	$\bar{B}$	$A \cdot B$	$\overline{A \cdot B}$	$\overline{\bar{A} + \bar{B}}$
0	0	1	1	0	1	1
0	1	1	0	0	1	1
1	0	0	1	0	1	1
1	1	0	0	1	0	0

proved by Truth table  $\underline{A \cdot B} = \overline{\bar{A} + \bar{B}}$ 

Universal Gate : NAND and NOR gate are

NAND as a Universal gate logic CKT can be built by NAND gate

Universal gate

①  $A \rightarrow \overline{D} \rightarrow y = \bar{A}$  — NOT Gate②  $A \rightarrow \overline{D} \rightarrow \overline{D} \rightarrow y = \overline{\overline{A} \cdot \overline{B}} = A \cdot B$  — AND gateNAND and building b  
any gate  
Combinationby a  
NAND  
gate

for NOR Gate

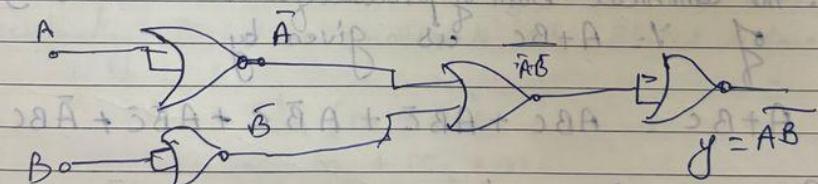
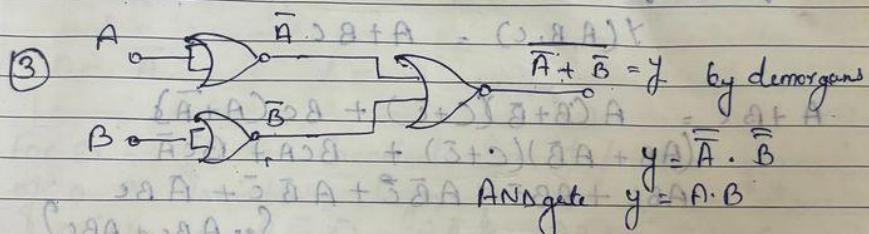
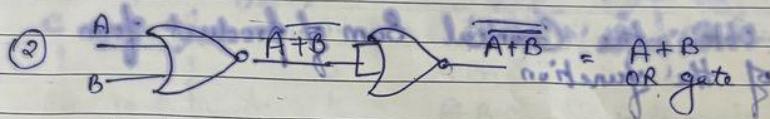
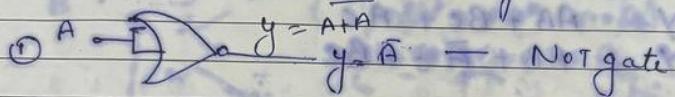


$$A \cdot \bar{B} = \overline{\bar{A} + \bar{B}} = A + B$$

OR Gate  
by DeMorgan's Theorem

$$\overline{A \cdot B} = \overline{A} + \overline{B}$$

NOR Gate as a Universal gate



NAND and NOR gates are called universal gates or universal building blocks because both can be used to implement any gate like AND, OR, and NOT gates or any combination of these basic gates.

How a NAND gate can be used to realize various logic gate



**Sum of Product :** The logical sum of two or more logical terms, is called a sum of products expression. It is basically an OR operation of AND operated variables such as :

$$Y = AB + BC + AC$$

$$Y = AB + \bar{A}C + BC$$

Ex - Obtain the Canonical Sum of product form of the function

$$Y(A, B, C) = A + BC$$

$$A + BC = A(CB + B)(C + \bar{C}) + BC(A + \bar{A})$$

$$(AB + A\bar{B})(C + \bar{C}) + BCA + BC\bar{A}$$

$$ABC + ABC\bar{C} + A\bar{B}C + A\bar{B}\bar{C} + ABC$$

$$\therefore ABC + ABC\bar{C}$$

$\therefore$  the Canonical sum of product form of  $Y = A + BC$  is given by

$$A + BC = ABC + ABC\bar{C} + A\bar{B}C + A\bar{B}\bar{C} + \bar{A}BC$$

### Canonical Sum of product

It is defined as the logical sum of all the minterms derived from the rows of a truth table, for which the value of the function is 1. It is also called a Minterm Canonical form.

The minterm by no. m,  
JU 2

A	B
0	0
0	0
0	1
0	1
1	0
1	0
1	1

For Ex - If  
of a 3- Vo  
of minterms  
be expres  
Corresponding

of two or  
three, is  
n. It is  
related Variables

The minterm of a 3-Variables func<sup>n</sup> can be represented by  $m_0, m_1, m_2, m_3, m_4, m_5, m_6, m_7$

A	B	C	Minterm
0	0	0	$\bar{A}\bar{B}\bar{C}$ $m_0$
0	0	1	$\bar{A}\bar{B}C$ $m_1$
0	1	0	$\bar{A}BC$ $m_2$
0	1	1	$\bar{A}B\bar{C}$ $m_3$
1	0	0	$ABC$ $m_4$
1	0	1	$A\bar{B}C$ $m_5$
1	1	0	$AB\bar{C}$ $m_6$

No P truth table if ABC having  $m_7$  is left  
it will be true at most binary condition w/  
 $0 \leq \bar{A} \leq 1$  ( $1 - A$ ) w/ value of 1st.  
Binary condition 8th in second also is 1. 0

$$\begin{aligned} & \text{SCA} \\ & \bar{A} \\ & \bar{B}C + ABC \\ & = ABC \end{aligned}$$

For Ex - If the Canonical Sum of product form of a 3-Variables logic function Y has three minterms  $\bar{A}\bar{B}\bar{C}$ ,  $\bar{A}\bar{B}C$ ,  $ABC$ , this can be expressed as the sum of the decimal code corresponding to these minterms as stated below:

$$\begin{aligned} Y &= \sum_m (0, 5, 6) \\ &= m_0 + m_5 + m_6 \\ &= \bar{A}\bar{B}\bar{C} + \bar{A}\bar{B}C + ABC \end{aligned}$$

all the  
1. a truth  
tion is 1.  
form.

Product of Sums (Pos) : A product of sums expression is a logical product of two or more logical sum items. It is basically an AND operation of OR operated variables.

$$Y = (A+B)(B+C)(C+\bar{A})$$

$$Y = (A+B+C)(A+\bar{C})$$

Canonical product of Sum expression

This is defined as the logical product of all the minterms derived from the rows of a truth table for which the value of function is 0. It is also known as the Normal Canonical form.

For ex : if the Canonical product of sum of a 3-variable logic function Y, has four minterms  $(A+B+C)$ ,  $(A+\bar{B}+C)$ ,  $(\bar{A}+B+C)$  and  $(\bar{A}+\bar{B}+\bar{C})$ , then it can be expressed as the product of decimal codes as given below:

$$Y = \prod_{M_0, M_2, M_4, M_7} (A+B+C)(A+\bar{B}+C)(\bar{A}+B+C)(\bar{A}+\bar{B}+\bar{C})$$

Sum	A
0	0
0	0
0	0

$$0 - A$$

Ex - Obtain  
 $Y(ABC) =$

Sol - In the  
in the first  
the Variable

$$Y = CAB$$

by using

$$Y = (A+\bar{B}) \cdot (A+\bar{B})$$

$$Y = (A+\bar{B}) \cdot (A+\bar{B})$$

1 Sums

is a

sum

of

Exes	A	B	C	
0	0	0	0	$A+B+C$
0	0	0	1	$A+B+\bar{C}$
0	1	0	0	$A+\bar{B}+C$
0	1	0	1	$A+\bar{B}+\bar{C}$
1	0	0	0	$\bar{A}+B+C$
1	0	0	1	$\bar{A}+B+\bar{C}$
1	1	0	0	$\bar{A}+\bar{B}+C$
1	1	0	1	$\bar{A}+\bar{B}+\bar{C}$
1	1	1	0	$\bar{A}+\bar{B}+\bar{C}$
1	1	1	1	$\bar{A}+\bar{B}+\bar{C}$

of all

truth

is

Canonical

0 - A      1 -  $\bar{A}$

0 0 0 0  
1 1 0 0

Ex → Obtain the Canonical product of sum expression

$$Y(ABC) = (A+\bar{B})(B+C)(A+\bar{C})$$

X 0 0 1

Sol → In the given expression, the Variable C is missing in the first term, the Variable A in the second term, the Variable B in the third term.  $\therefore C \in A\bar{A} \& B\bar{B}$

decimal

$$Y = (ABC) = (A+\bar{B})(B+C)(A+\bar{C})$$
  
$$(A+\bar{B}+0)(B+C+0)(A+\bar{C}+0)$$
  
$$(A+\bar{B}+\bar{C}\bar{C})(B+C+A\bar{A})(A+\bar{C}+\bar{B}\bar{B})$$

by using distributive property.

$$Y = (A+\bar{B}+\bar{C})(A+\bar{B}+\bar{C})(A+\bar{B}+\bar{C})(A+\bar{B}+\bar{C})$$
  
$$(A+\bar{B}+\bar{C})$$

$$Y = (A+\bar{B}+\bar{C})(A+\bar{B}+\bar{C})(A+\bar{B}+\bar{C})(A+\bar{B}+\bar{C})$$
  
$$(A+\bar{B}+\bar{C})$$

$$\therefore (A+\bar{B}+\bar{C})(A+\bar{B}+\bar{C}) =$$
  
$$A+\bar{B}+\bar{C}$$

## K-Map (Karnaugh Map)

A K-Map is a representation of a truth table of a logical function

A K map can be used to obtain a "minimal" expression of a function in the SOP or in POS.

~~the~~ For Ex- 2. Variable k-Map

A	B	C	D
0	0	0	0
0	0	1	1

$$\overline{A} = 1 \quad A = 0$$

$$\begin{matrix} 0 & 0 & 0 & x \\ 0 & 1 & 1 & (A+B)(C+D) = (A+C)(B+D) \end{matrix}$$

## Pos form

(A)

Don't Care

In certain  
never occur  
because they  
never do  
don't ever

## Simplicity

$F(A, B, C)$

(6) POS form Consider the following function in (a) SOP form

(v) -10s form

$$F(A, B, C, D) = \sum m(0, 1, 2, 5, 8, 9, 10)$$

$$\begin{aligned} & (1+\bar{A}+A)(2+A+\bar{A})(3+\bar{A}+A)(1+\bar{A}+A) = \\ & = (3+\bar{A}+A)(5+\bar{A}+A) : \\ & \quad (5+\bar{A}+A) \end{aligned}$$

CD \ AB		00	01	11	10	CD \ AB	00	01	11	10
00	1				1	00	4	12	8	2
01		1			1	01	5	13	9	1
11						11	7	15	11	3
10		1			1	10	6	14	10	2

$$Y = \bar{B}\bar{D} + \bar{A}\bar{C}D + A\bar{B}\bar{C}$$

Pos Form		00	01	11	10
00	1	0	0	1	
01	1	1	0	1	
11	0	0	0	0	1
10	1	0	0	1	1

$$(\bar{A} + \bar{B})(\bar{C} + \bar{D})(\bar{B} + D) \rightarrow Y$$

Don't Care Combinations: up and down next

In certain digital systems, some input combinations never occur during the process of a normal operation because those input conditions are guaranteed never to occur. Such input combinations are don't care combinations.

Simplify the Boolean function

$$F(A, B, C, D) = \sum_m(1, 3, 7, 11, 15) + \sum_d(0, 2, 5)$$

CD \ AB		00	01	11	10
00	d	0	0	0	
01	1	d	0	0	
11	1	1	1	1	
10	d	0	0	0	

$$Y = \bar{A}\bar{B} + CD$$

09/03 7:30  
Simplify the following Boolean function.

(a) SOP (b) POS form

$$Y = \sum_m (0, 2, 3, 6, 7) + \sum_d (8, 10, 11, 15)$$

		CD	AB			
		00	01	11	10	
		00	1			00
		01		0	1	10
		11	1	0	0	01
		10	1	1	0	00

$$Y = \bar{A}c + \bar{a}\bar{B}\bar{D}(\bar{a} + \bar{b})(\bar{b} + \bar{A})$$

By Combining the 1's & d as shown in the k-map  
there are two quads, (quad 1) and (quad 2)

Quine - Mccluskey

Find the minimal expression

$$F(a, b, c, d)$$

$$0 \quad 0 \quad 00$$

$$5 \quad 0 \quad 1$$

$$8 \quad 1 \quad 10$$

$$9 \quad - \quad 1$$

$$10 \quad 0 \quad 11$$

$$11 \quad - \quad 1$$

$$14 \quad 1 \quad 11$$

$$15 \quad - \quad 1$$

Step 1

Groups

Number of

0

1

2

(b) Minimal POS form

		CD	AB			
		00	01	11	10	
		00	0	10	01	0
		01	10	0	0	01
		11		01	d	1
		10		0	d	1

$$Y = A(c + \bar{d})(\bar{B} + c)$$

## Quine - McCluskey Method :

Find the minimal sum of products for the Boolean expression

$$F(a, b, c, d) = \sum(0, 5, 8, 9, 10, 11, 14, 15)$$

0	0 0 0 0	
5	0 1 0 1	
8	1 0 0 0	~ 11, P
9	1 0 0 1	~ 11, 01
10	0 1 1 0	~ 11, 01
11	1 0 1 1	
14	1 1 1 0	~ 11, 11
15	1 1 1 1	+ Binary representation of minterms

Step 1	Group	Minterm	Variable			
			A	B	C	D
	Number of 1's					
00	0	0	✓			
01	1	1		✓		
10	2	2			✓	
11	3	3				✓

Group of minterms for different number of 1's

Step 2

2-cell Combination

Group	Matched Pair	Variables
0	0, 8	A B C D - 0 0 0
1	(8, 9) ✓ 8, 10	1 0 0 - 1 0 0 - 0
2	9, 11 ✓ 10, 11 ✓ 10, 14 ✓	1 0 0 - 1 1 1 1 0 0 1 - P 0 1 1 0 1 1 0 0 1 1 1 0 1 1 1 1 1 1
3	11, 15 ✓ 14, 15 ✓	0 1 - 1 1 1 H1 1 1 1 1 1 - 21

Step 4

P.I

8, 9, 10, 11  
8, 10, 9, 11  
10, 11, 14, 15

0, 8

5

Step 5

f(a, b, c, d)

A B

Step 3 : 4-cell Combination

Group	Matched Pair	Variables
0	8, 9, 10, 11 8, 10, 9, 11	A B C D 1 0 0 - - ] AB
1	10, 11, 14, 15 10, 14, 11, 15	1 - 1 - ] AC 1 - 1 - ] BC
	0, 8 5	- 0 0 0 - 0 1 0 1

$\bar{B}\bar{C}\bar{D}$

Step 4 Prime Implicants Table

P.I.	0	5	8	9	10	11	14	15
8, 9, 10, 11			X	(X)	X	X		
8, 10, 9, 11						X	X	(X) (X)
10, 11, 14, 15								

P.I.	0	8	5	11	14	15
0, 8	(X)	X				
5	✓	(X)	✓	✓	✓	✓

Step 5 Reduced Prime Implicant Table

	0	5	8	9	10	11	14	15
8	X			X				
5			X					

$$f(a, b, c, d) = A'B' + AC + B'c'd' + A'b'c'd$$

ĀB̄

$$\checkmark \quad 1 \quad 0 \quad 1 \quad 1 \quad 1 \quad 0 \quad 1$$

$$\checkmark \quad 1 \quad 0 \quad - \quad 1 \quad 1 \quad 1 \quad 0 \quad 1$$

Ac

$$\checkmark \quad 1 \quad 1 \quad - \quad 1 \quad 1 \quad 1 \quad 0 \quad 1$$

$$\checkmark \quad 1 \quad 1 \quad - \quad 1 \quad 1 \quad 1 \quad 1 \quad 1$$

$$\bar{B}\bar{C}D$$

$$f(A, B, C, D) = \Sigma(4, 6, 9, 10, 11, 13) + \Sigma d(2, 12, 15)$$

Step - 1

Step 2	4	0 1 0 0
	2	0 0 1 0
Step	6	0 1 1 0
	9	1 0 0 1
	10	1 0 1 0
	12	1 1 0 0
	11	1 0 1 1
	13	1 1 0 1
	15	1 1 1 1

Step - 3

4, 6	0 1 - 0.	P
4, 12	- 1 0 0	Q (B0, d, P)
2, 6 + 10 0 A + PA + RA		
2, 10	- 0 1 0	S
9, 11	1 0 - 1	V
9, 13	1 - 0 1	V
10, 11	1 0 1 - - T	
10, 13	1 1 0 - - U	
11, 15	1 - 1 1	V
13, 15	1 1 - 1	V

Step - 3 4 - 0

9, 13, 11, 15

9, 11, 13, 15

Prime implicants

P 0 1

Q - 1

R - 1

S - 0

T - 1

U - 1

V 1

4	X
P	X
Q	X
R	
S	
T*	
U	
V	

(4, 6, 10)

P, S, T

F =

II - 3in

2, 12, 15)

### Step - 3 4-cell Combinations

	A	B	C	D	
9, 13, 11, 15	1	-	-	1	{ - ~
9, 11, 13, 15	1	-	-	1	

Prime implicants are

P	0 1 - 0	A' B A'
Q	- 1 0 0	B C' A'
R	- 1 0 0	A' C A'
S	- 0 1 0	B' C A'
T	1 0 1 0	A B' C
U	1 1 0 -	A B C'
V	1 - 1 1	A A'

(4,6,10) Unticked Columns

P, S, T

$$f = P + T + V$$

$$= \overline{ABD}' + AB'C + AD$$