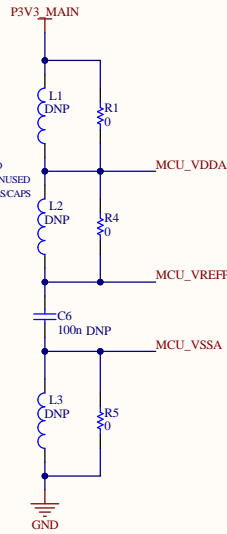


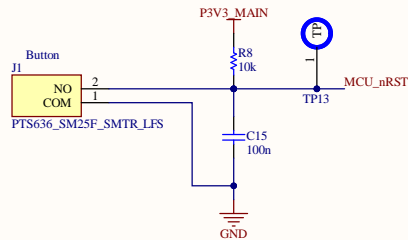
ADC/DAC Analog Supply

ONLY POPULATE 0 OHMS IF ADC/DAC UNUSED
AND DEPOPULATE FERRITE BEADS/CAP IF ADC/DAC UNUSED
0 OHMS SHOULD BE MUTUALLY EXCLUSIVE WITH FERRITE BEADSCAPS

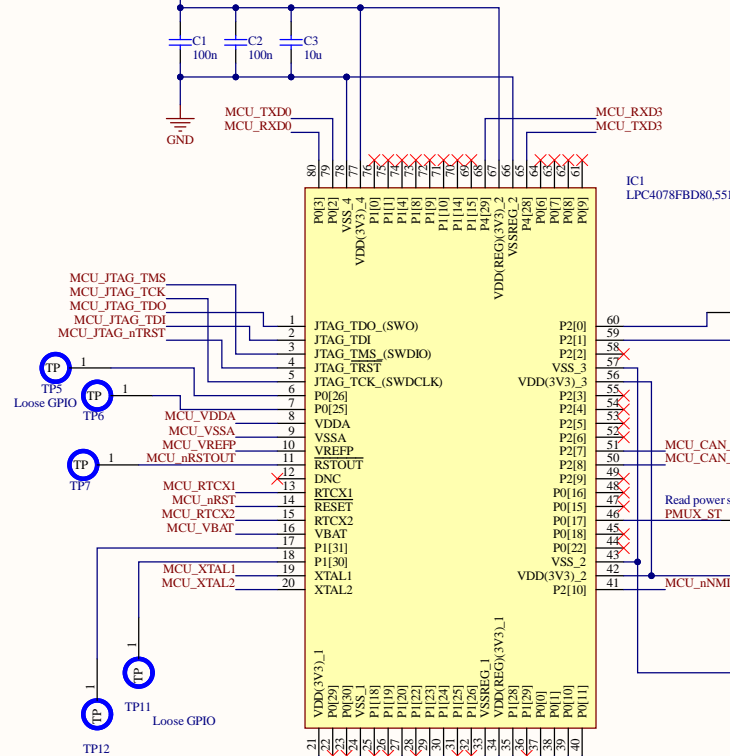


Reset Circuit

Also resettable via USB

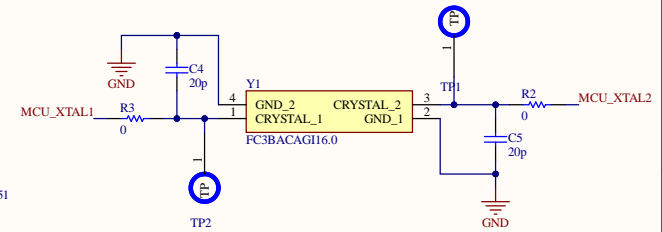


P3V3_MAIN



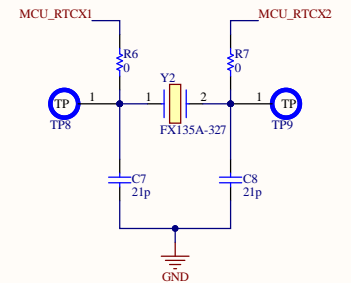
Main Crystal Oscillator

$CL = ((CX1 \times CX1) / (2 \times CX1)) + C_{stray}$
 $CX1 = CX2 = 2(12pF - 2pF) = 20pF$
Crystal $CL=12pF$, assuming $C_{stray}=2pF$

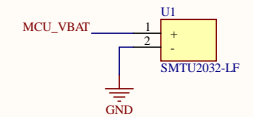


RTC Crystal Oscillator

$CL = ((CX1 \times CX1) / (2 \times CX1)) + C_{stray}$
 $CX1 = CX2 = 2(12.5pF - 2pF) = 21pF$
Crystal $CL=12.5pF$, assuming $C_{stray}=2pF$

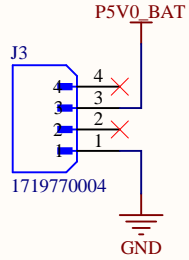


RTC Battery

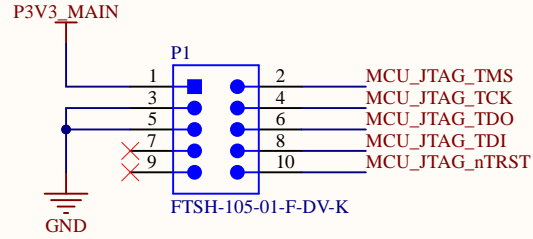


Title Main/MCU Sheet		
Size	Number	Revision
B		
Date:	5/09/2023	Sheet of 1
File:	C:\Users\j.bmsMaster_main\SchDoc	Drawn By: Vincent Saw

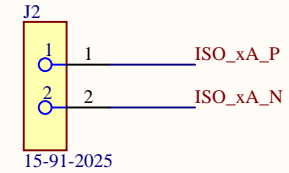
Power Connector



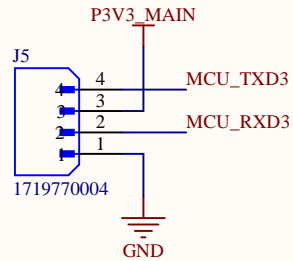
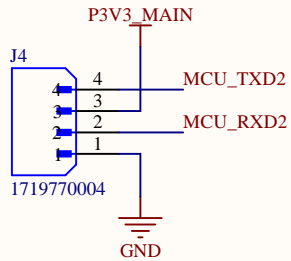
JTAG Debug Connector



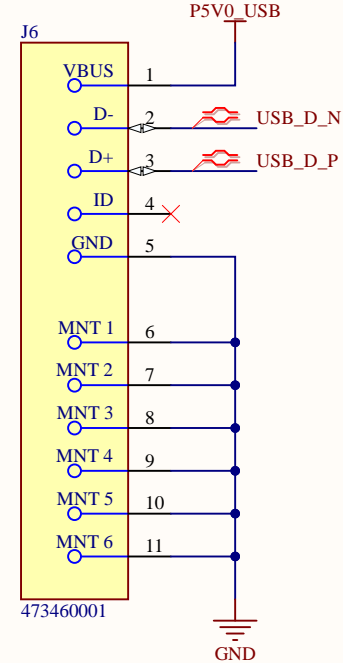
isoSPI Connector (to 1st Slave)



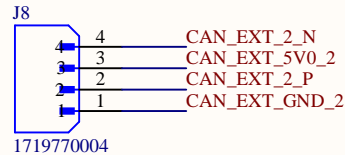
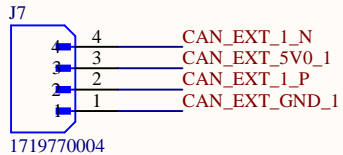
UART/Serial Connectors



microUSB Type B Connector

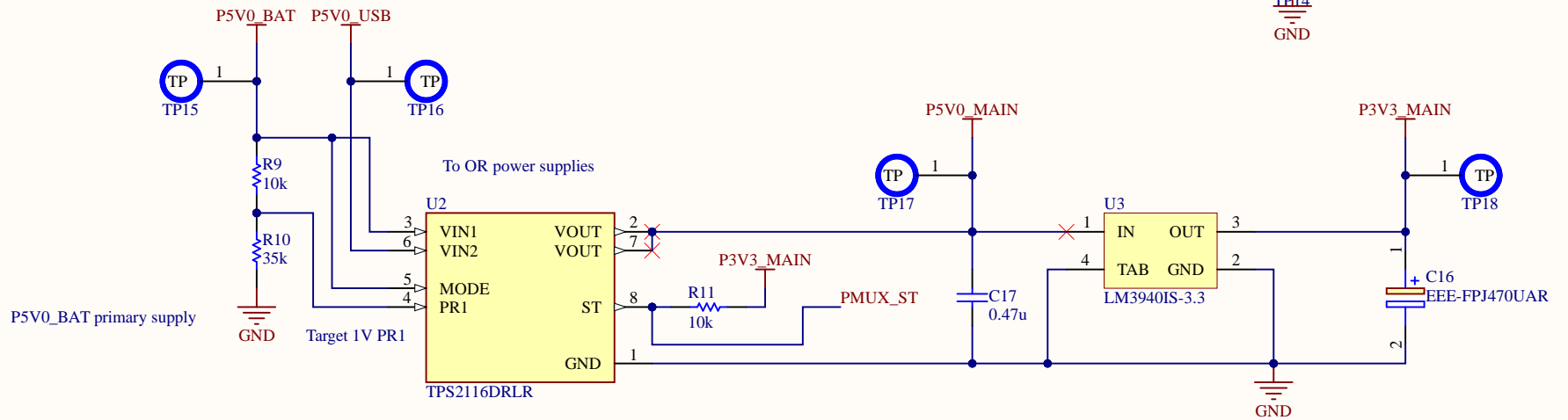


CAN Connectors



Connector Sheet

Size	Number	Revision
A		
Date:	5/09/2023	Sheet of Spartan Racing & SJSU
File:	C:\Users\...\bmsMaster_conn.SchDoc	Drawn By: Vincent Saw



Title Power Sheet		
Size A	Number	Revision
Date: 5/09/2023	Sheet of Spartan Racing & SJSU	
File: C:\Users\...\bmsMaster_power.SchDoc	Drawn By: Vincent Saw	

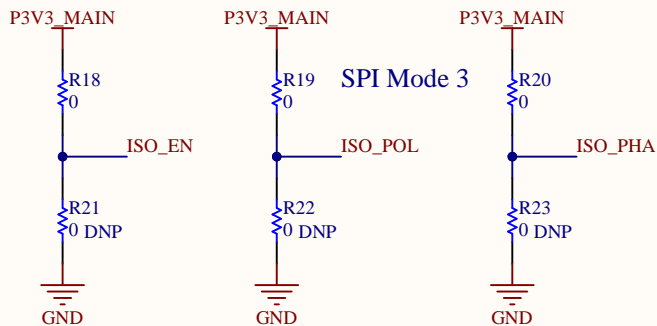
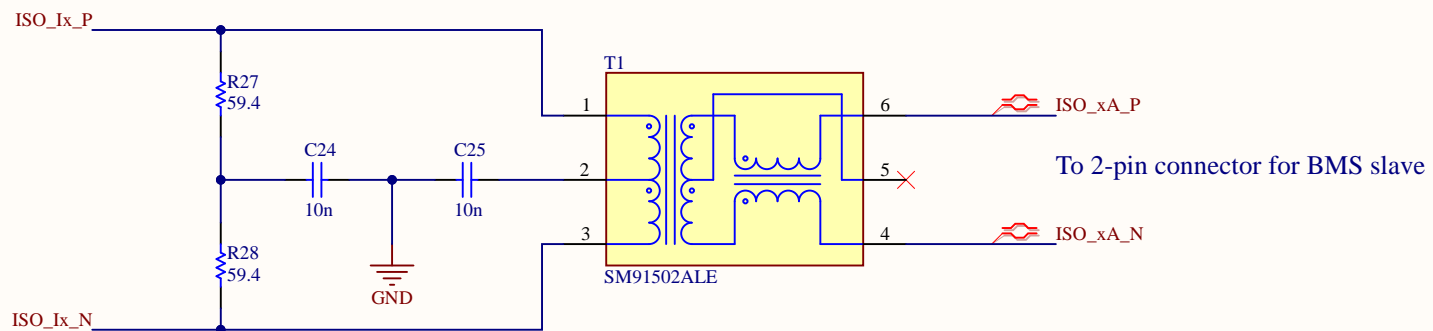
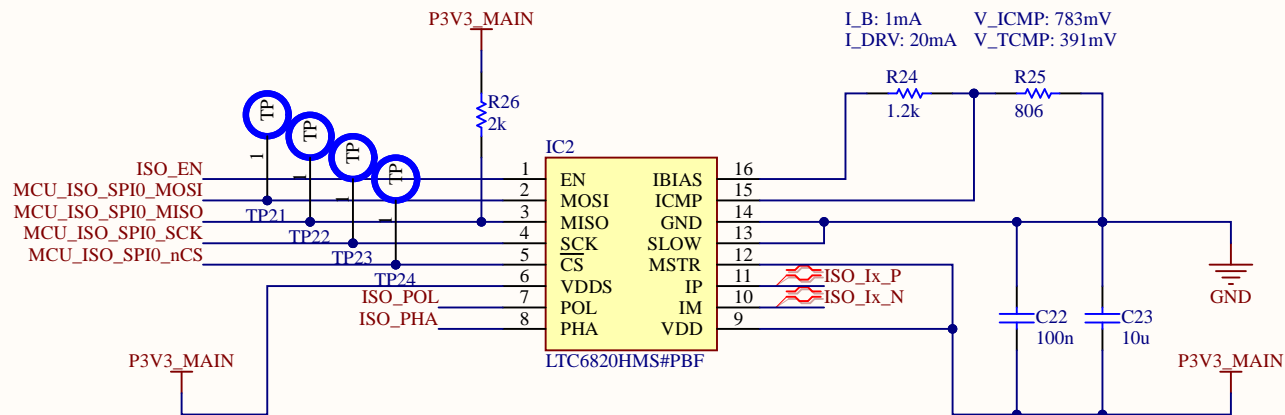


Table 4. SPI Modes

MODE	POL	PHA	DESCRIPTION
0	0	0	SCK Idles Low, Latches on Rising (1st) Edge
1	0	1	SCK Idles Low, Latches on Falling (2nd) Edge
2	1	0	SCK Idles High, Latches on Falling (1st) Edge
3	1	1	SCK Idles High, Latches on Rising (2nd) Edge

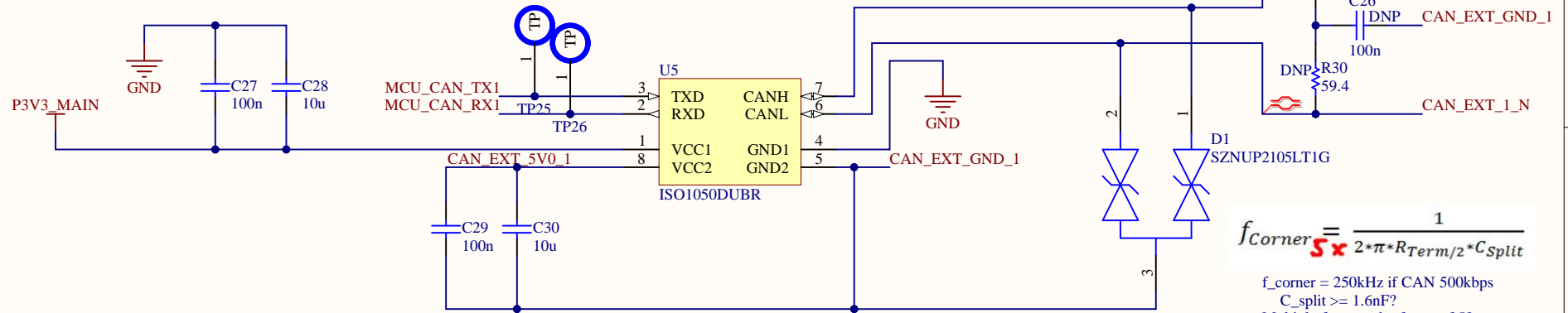


Title IsoSPI Sheet

Size	Number	Revision
A		
Date:	5/09/2023	Sheet of Spartan Racing & SJSU
File:	C:\Users\...\bmsMaster_isospi.SchDoc	Drawn By: Vincent Saw

Isolated CAN Transceiver 1

Requires external 5V and GND for isolation purposes
 TODO: Add on-board isolated power for VCC2/GND2



$$f_{corner} = \frac{1}{2 * \pi * R_{Term} / 2 * C_{split}}$$

$f_{corner} = 250\text{kHz}$ if CAN 500kbps

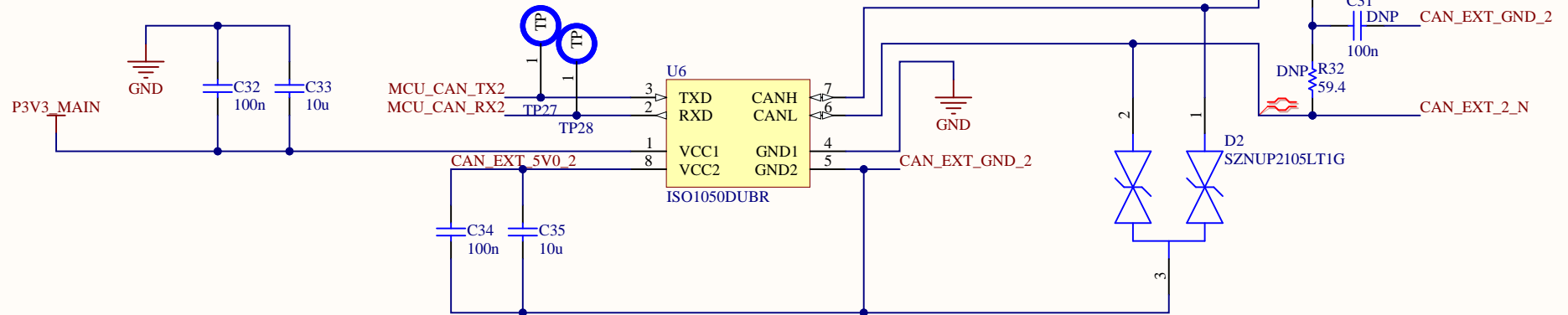
$C_{split} \geq 1.6\text{nF}$?

Multiply f_{corner} by factor of 5?

Using 100n for now, mega low pass, generous threshold

Isolated CAN Transceiver 2

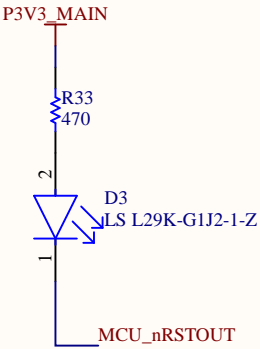
Requires external 5V and GND for isolation purposes
 TODO: Add on-board isolated power for VCC2/GND2



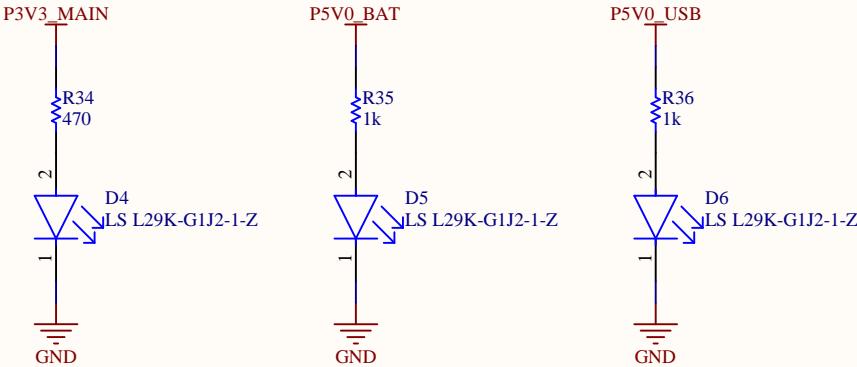
Title **CAN Bus Sheet**

Size	Number	Revision
A		
Date:	5/09/2023	Sheet of Spartan Racing & SJISU
File:	C:\Users\...\bmsMaster_can.SchDoc	Drawn By: Vincent Saw

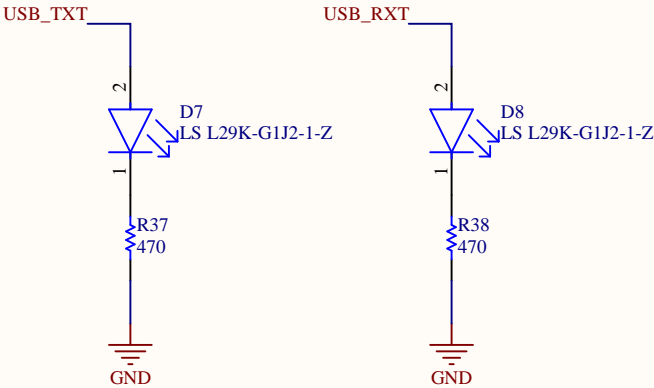
Reset Indicator LED (nRSTOUT)



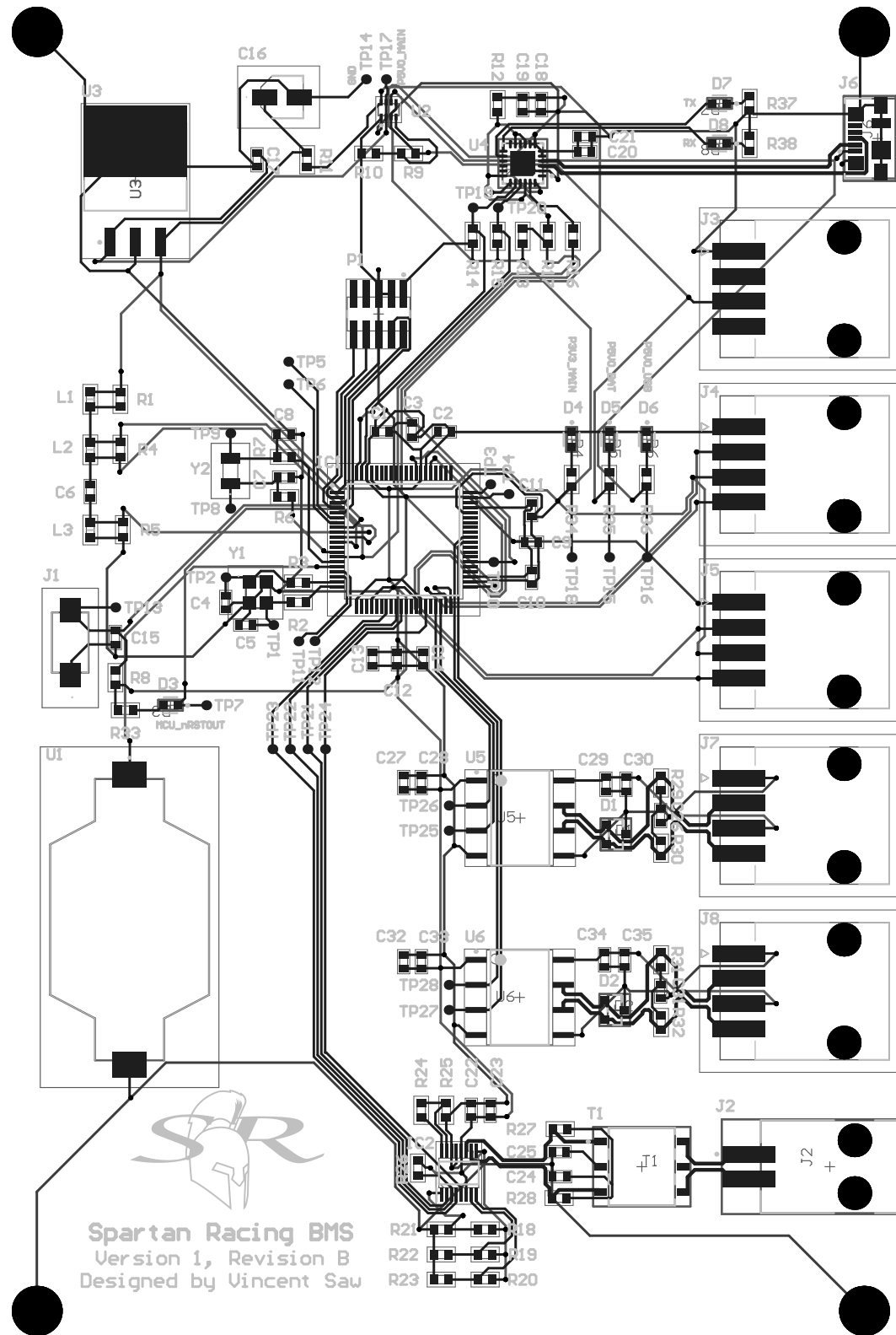
Power Debug LEDs



USB/UART0 Activity Indication LEDs



Title LED Sheet		
Size A	Number	Revision
Date: 5/09/2023	Sheet of Spartan Racing & SJSU	
File: C:\Users\...\bmsMaster_led.SchDoc	Drawn By: Vincent Saw	



NPTH D3.4 +/-0.05 NPTH D3.4 +/-0.05 NPTH D3.4 +/-0.05 NPTH D3.4 +/-0.05

Spartan Racing BMS
Version 1, Revision B
Designed by Vincent Saw

R21 R18
R22 R19
R23 R20

Board Stack Report