









### **C2DaCe** challenges

### Classes Inheritance Contexts Recursions Tail recursion Indirect recursion **Pointers** Unrestrictred arithmetic **Stateful library calls** Automatic assessment **Template programming Library nodes Encapsulation**

### **F2DaCe** challenges

- Generalized views
- Vector operations
- Labels & GoTo's
- Intrinsic function coverage
- Modern Fortran

### DaCe challenges

- Application-level ToGPU transform
  - + Associated transforms

### **Engineering efforts**

**Research efforts** 







## **Application-level ToGPU transform**

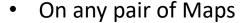
#### **Map Fission**



- On any SDFG
- Must handle
  - Edge assignments
  - Scalars
  - Control flow



#### **Map Fusion**

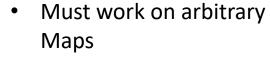






Needs helper Transformations





 Not a state-level transformation



#### **Performance heuristics**

- Guide SDFG transformations
- Must handle
  - Application requirements
  - Hardware capabilities



#### **Data instrumentation**

- Simplifies debugging
- Allows faster heuristics development





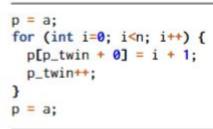


# Work on C and pointer analysis

#### C representation

```
p = a;
for (int i=0; i<n; i++) {
  p[0] = i + 1;
  p++;
}
p = a;</pre>
```

#### twin transformation



#### ASM representation

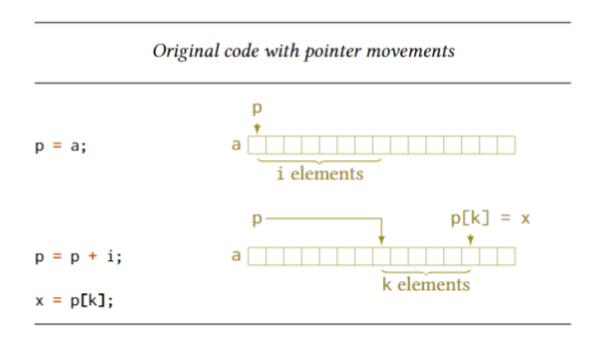
```
loop iterator → rax
. 14:
 add
                    ; i++
         eax, 1
                                                        pointer iterator → rdx
         rdx, 4
 add
                    ; p++
                                                        n \text{ value} \rightarrow r14d
         DWORD PTR [rdx-4], eax
 mov
         eax, r14d ; i < n
          .L4
 jne
No pointer increment
⇒ one less instruction
.L4:
                                                        loop iterator → rax
         DWORD PTR [r12-4+rax*4], eax
 mov
                                                        pointer iterator →
         rax, 1
 add
                    ; i++
                                                        container base address → r12
         rdx, rax ; i < n
 cmp
 jne
          .L4
                                                        n value \rightarrow rdx
```



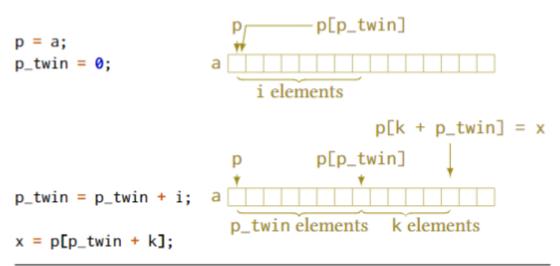




# Work on C and pointer analysis

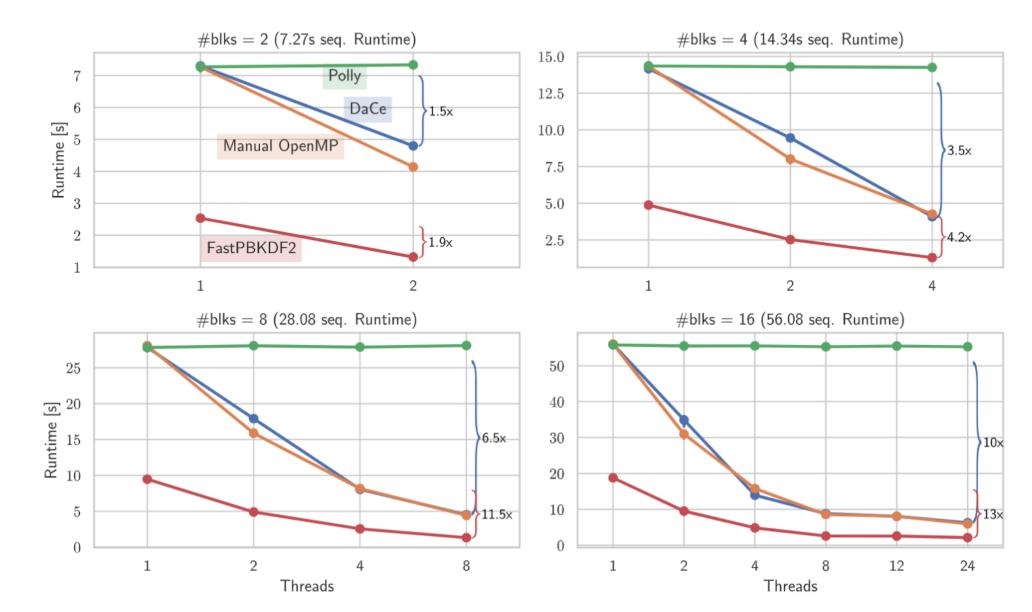


#### Transformed code with twin instead of pointer movements





### PBKDF2







### **HPCCG**

