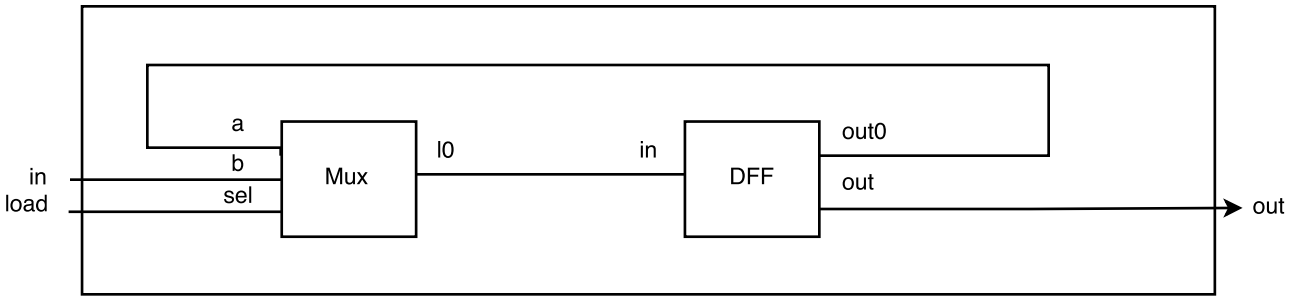
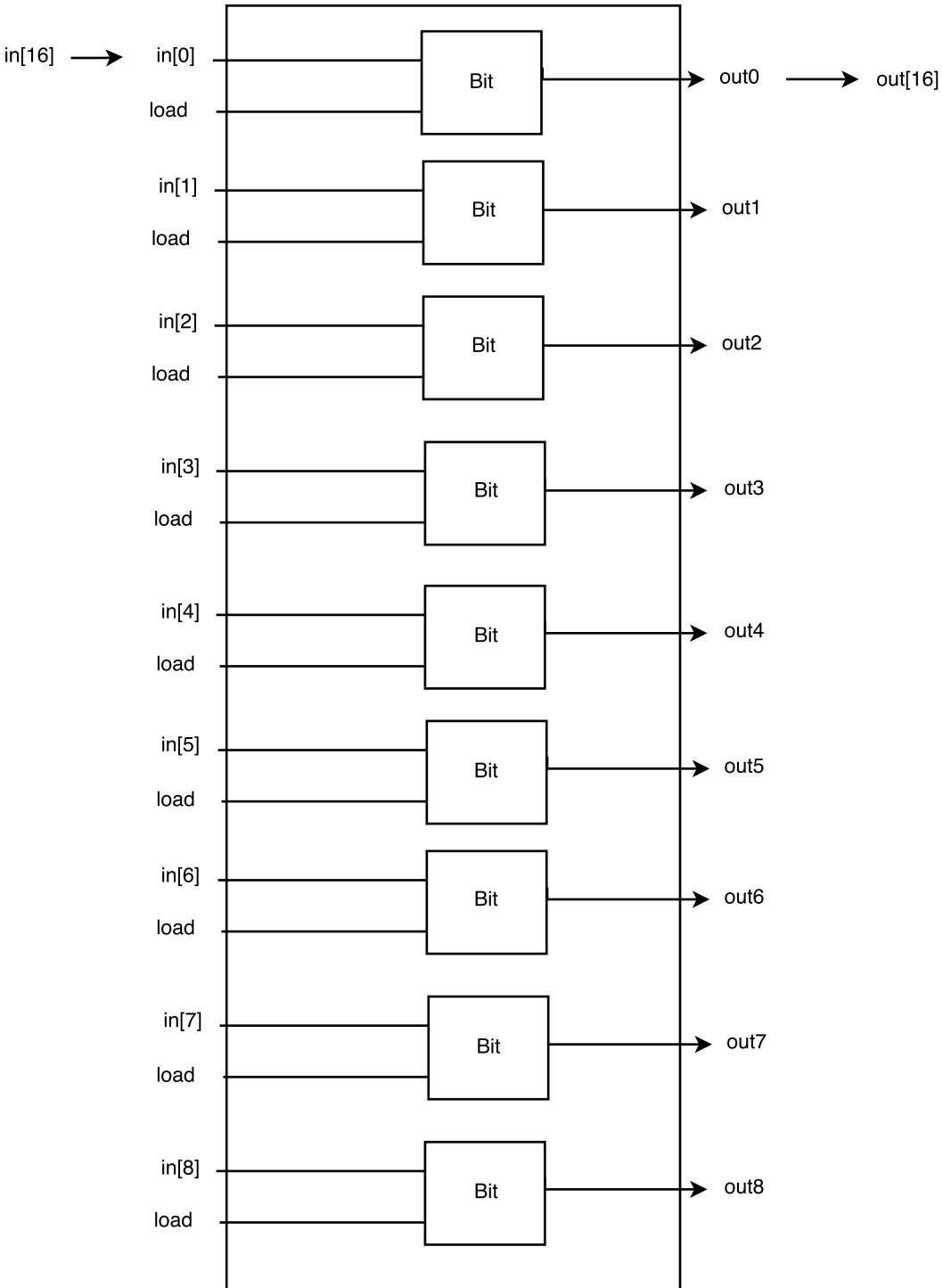
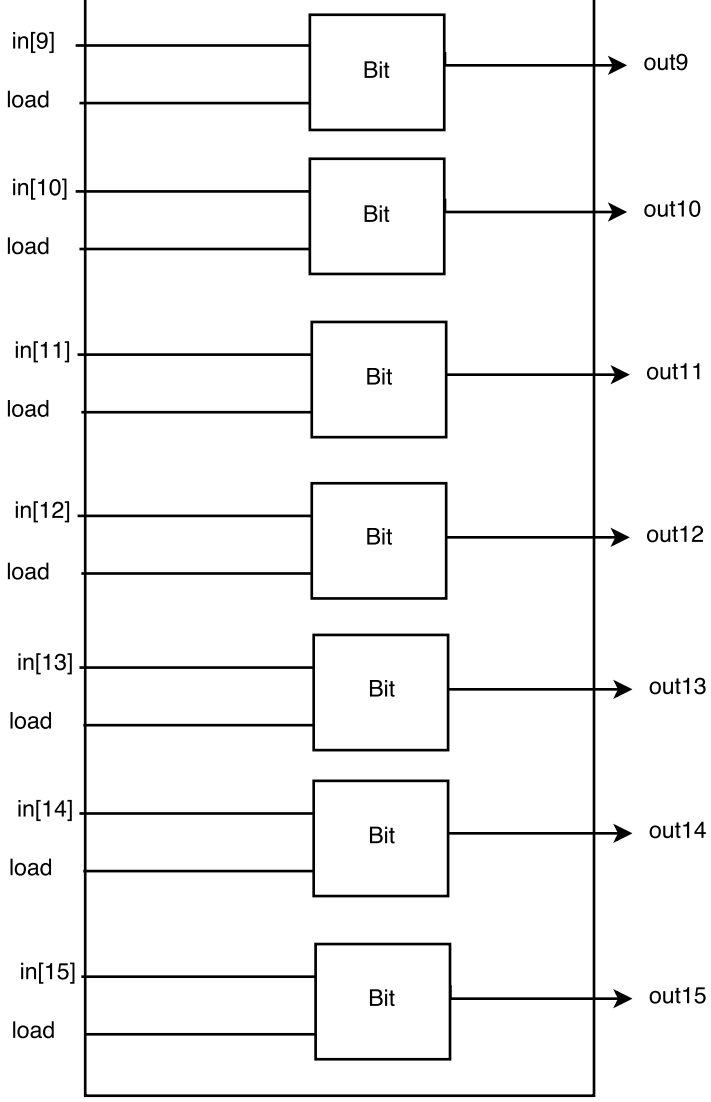
Bit

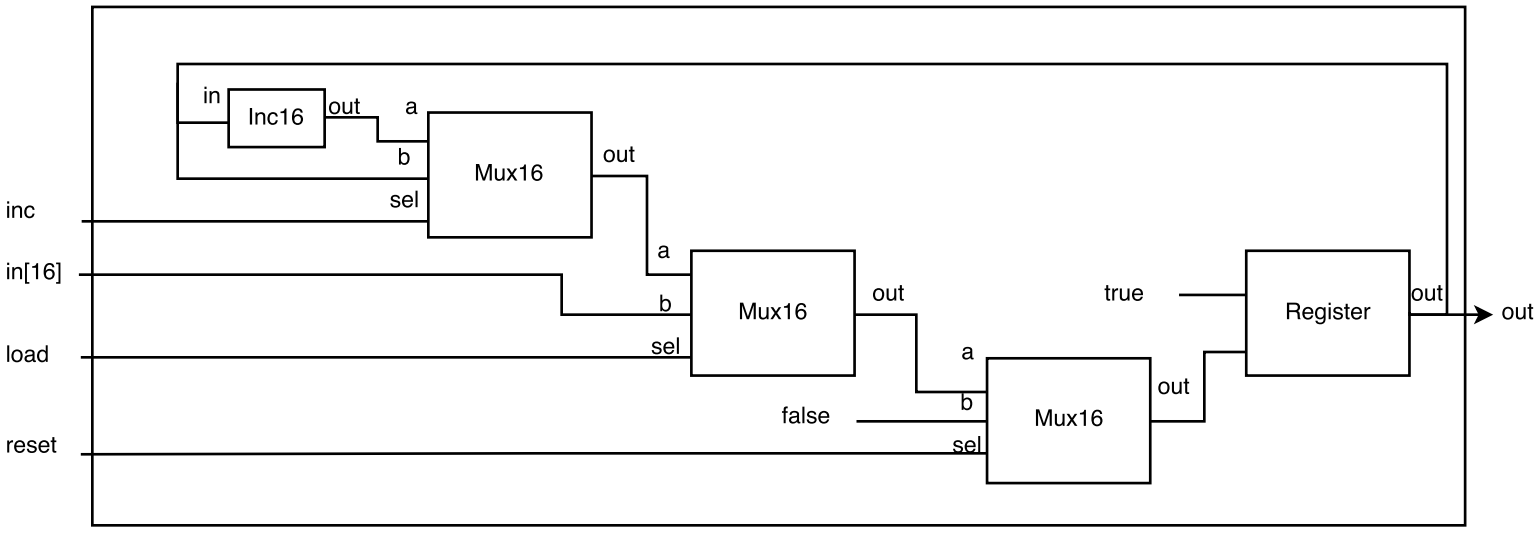


Register

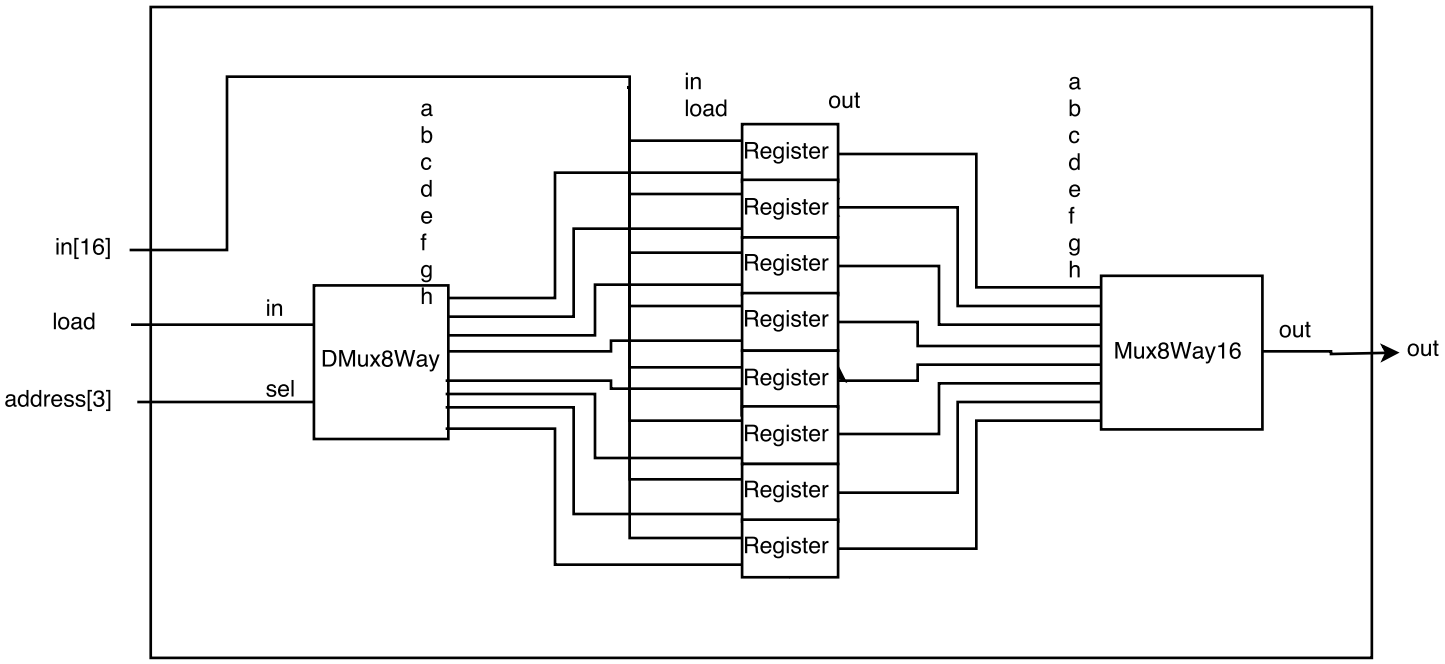



in[9]
load
Bit
out9
in[10]
load
Bit
out10
in[11]
load
Bit
out11
in[12]
load
Bit
out12
in[13]
load
Bit
out13
in[14]
load
Bit
out14
in[15]
load
Bit
out15

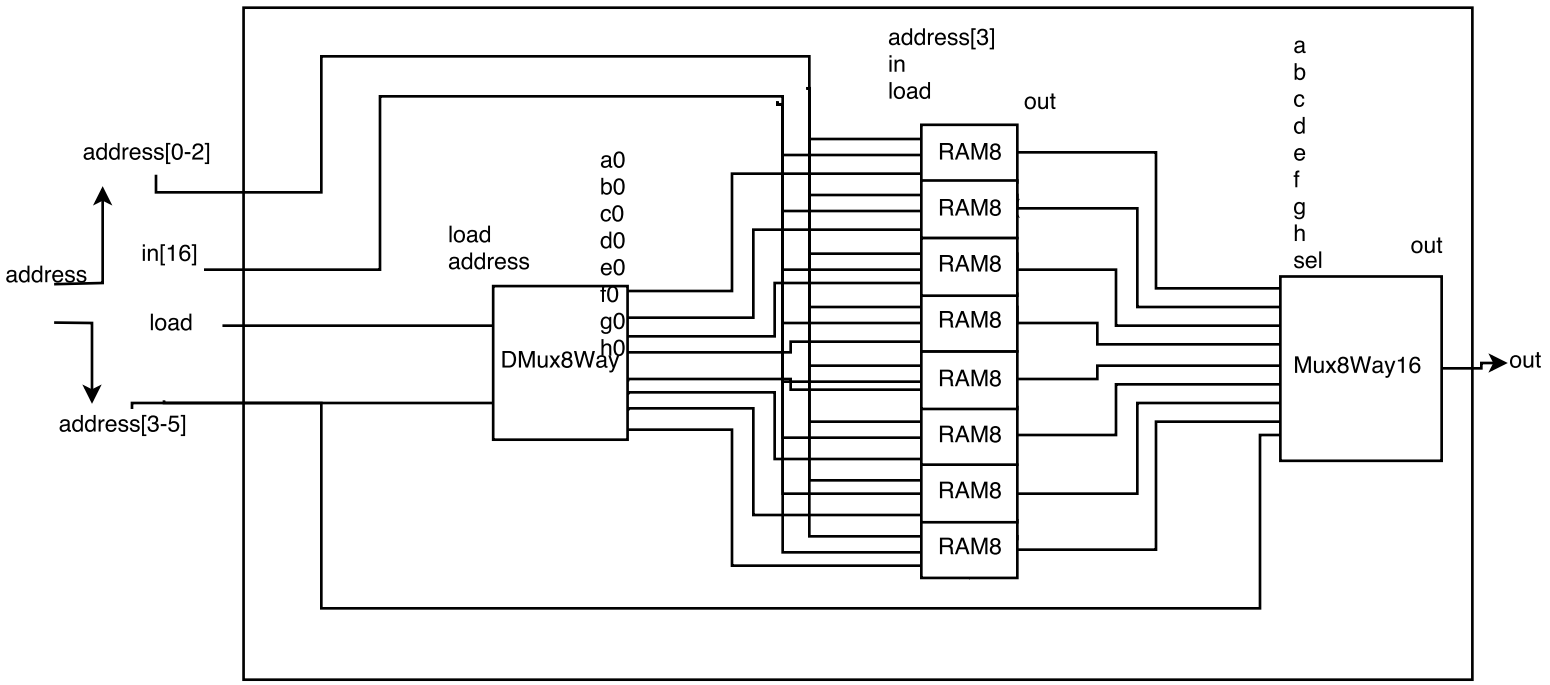
PC


in
Inc16
out
a
b
Mux16
out
sel
inc
in[16]
load
reset
a
b
Mux16
out
sel
a
b
Mux16
out
sel
false
true
Register
out
out

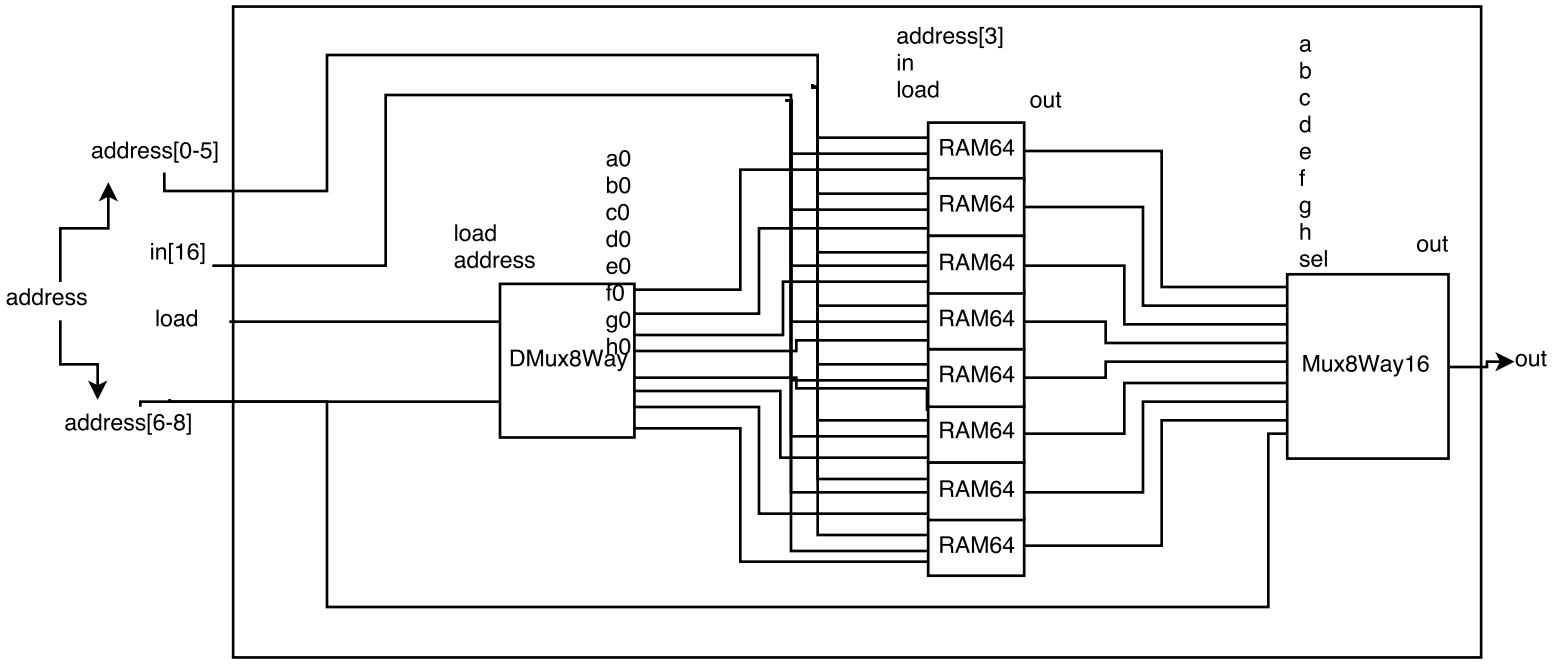
RAM8


in[16]
load
address[3]
in
sel
DMux8Way
a
b
c
d
e
f
g
h
in
load
out
Register
Register
Register
Register
Register
Register
Register
Register
a
b
c
d
e
f
g
h
Mux8Way16
out
out

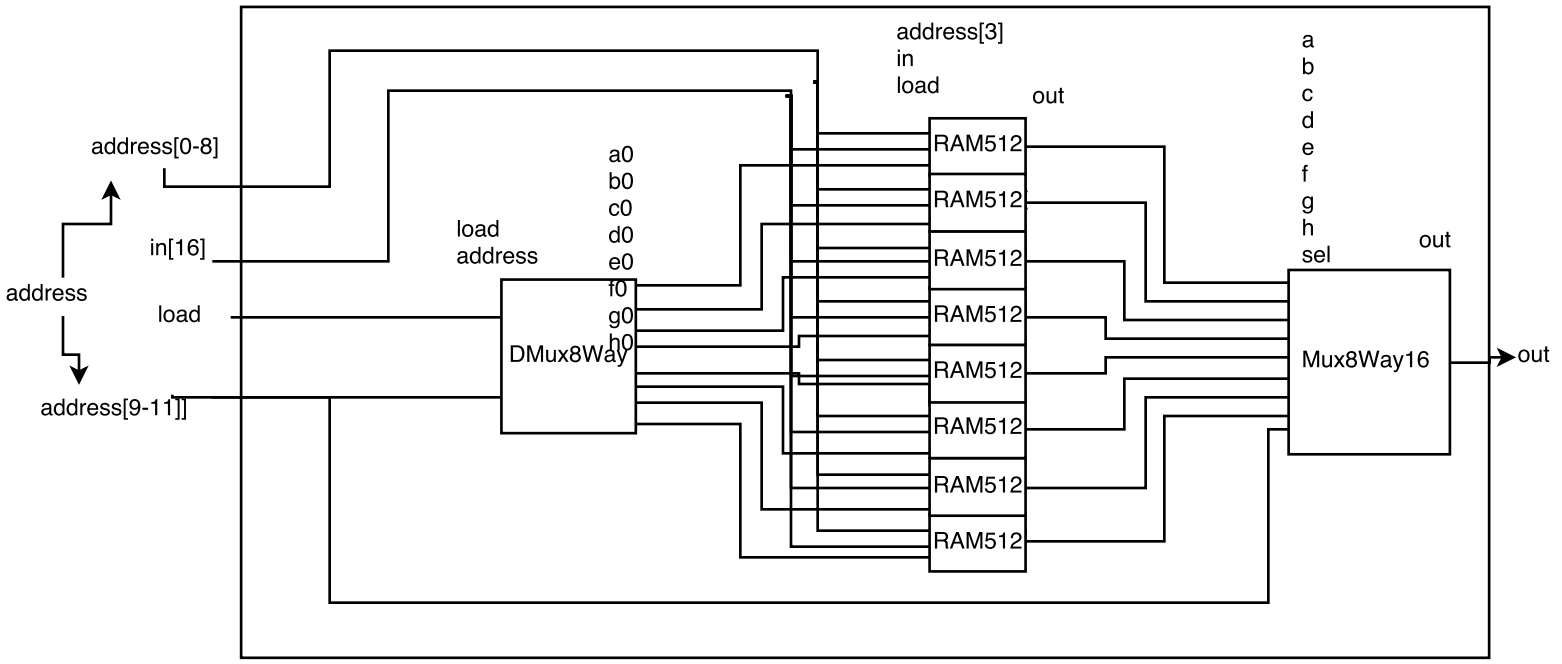
RAM64


address[0-2]
address
in[16]
load
address[3-5]
load
address
DMux8Way
a0
b0
c0
d0
e0
f0
g0
h0
address[3]
in
load
out
RAM8
RAM8
RAM8
RAM8
RAM8
RAM8
RAM8
RAM8
a
b
c
d
e
f
g
h
sel
Mux8Way16
out
out

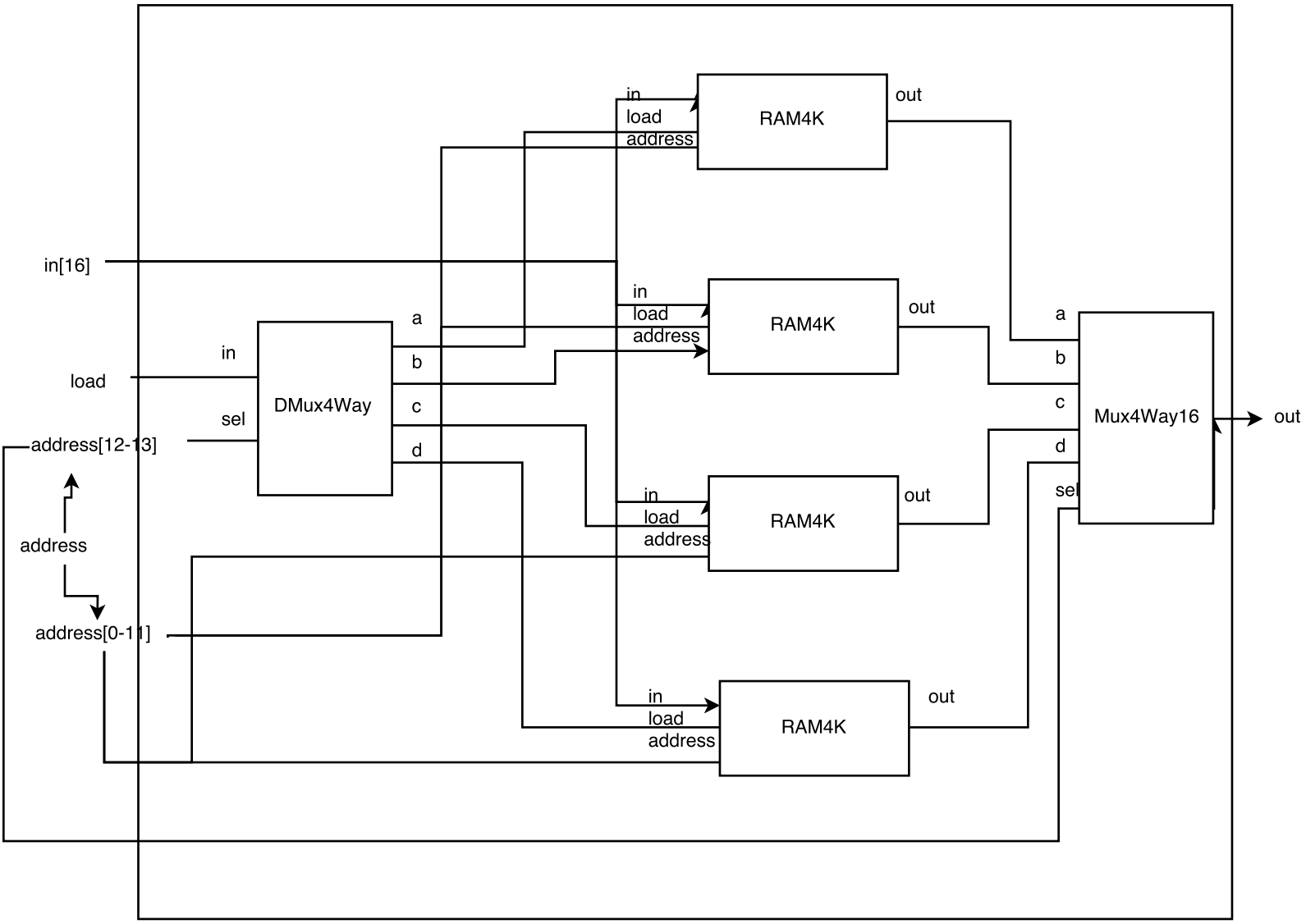
RAM512



RAM4K



RAM16K


in[16]
load
address[12-13]
address
address[0-11]
in
sel
DMux4Way
a
b
c
d
in
load
address
RAM4K
out
in
load
address
RAM4K
out
in
load
address
RAM4K
out
in
load
address
RAM4K
out
a
b
c
d
sel
Mux4Way16
out