# Compatibility of trusted computing

Qi Hu

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## 1 Introduction

## 2 Research Objectives

1. Design a dynamic analysis/translation tool to support legacy software on trusted computing systems. This tool can be implemented in 3 parts:

   - Dynamic analysis of legacy software points out which parts of the code are more vulnerable to attack and which data are important and must be protected.

   - Using the analyzed data to guide the in-place binary translation, insert new code segments, which are used to protect code and migrate data into enclaves.

   - Binary translation is used for cross-instruction architecture programs. The migration instruction segments can be generated and added during the translation process.

2. Migrate the whole dynamic analysis/translation tool into a distributed system.

3. Explore performance bottlenecks and help design/modify the hardware architecture. Achieve efficient operation of the entire system with the help of hardware-software co-optimization.

## 3 Background of Research

### 3.1 Trusted Computing and TEE

Various encryption and authentication methods (e.g., TLS and file disk encryption) are often used to prevent confidential data loss, theft or corruption. However, relying solely on software for confidential data protection has many problems, such as software vulnerabilities and reverse engineering cracking [40]. So, it is helpful to use Trusted Execution Environment (TEE) to protect encryption software and data, which provides an environment shielded from outside interference and the necessary mechanisms to build secure and sensitive applications.

Intel Software Guard Extensions (SGX) is a set of security architecture extensions [20]. It provides the enclave environment, preventing all other software from accessing the code and

data inside an enclave. Also, when data leaves the enclave and is written into the memory, the data will be automatically encrypted.

ARM TrustZone uses a different approach to TEE by introducing a secure world, which is a new execution environment in the processor in addition to the normal world [21]. The secure world has multiple privilege levels, just like a virtual machine (VM), which allows an entire trusted software stack to be implemented.

Due to overly complex operations and unacceptable hardware overhead, Intel begin moving to Trust Domain Extensions (TDX) [27, 28], a new trusted computing architecture introduces a separate trusted hypervisor/VMM. The interaction between trusted virtual machines and external untrusted environments should be checked by the security check module Shim.

Since TrustZone lacks confidentiality support, ARM v9 proposes Confidential Compute Architecture (CCA). CCA [3] differs from TrustZone, directly supports in-memory confidentiality capabilities in hardware, and protects users' confidential data.

## 3.2   Binary rewriting and binary translation

Binary rewriting is a technique for modifying or translating the original binary code without having the source code. According to their characteristics, they can be divided into four categories: static, dynamic, minimal-invasive and full-translation.

Static binary rewriting can use the existing information, such as static data flow analysis and symbol table information, to optimize or enhance existing programs [32, 31]. Dynamic binary rewriting performs alterations during execution, which can be used for performance analysis [18] and hot code patching [5]. Minimal-invasive rewriting is based on branch granularity. It will perform additional instruction at the original location by rewriting into branch instructions. This is often used to add a new function to the original program [8]. Full-translation rewriting can convert binaries at any instruction and usually lift the origin binary code into intermediate translation representations. Some open-source tools, like QEMU [4] and Valgrind [23], use full-translation for binary rewriting.

## 3.3   Distributed system in Trusted computing

With the rise of cloud computing and increase in data sets in recent years, more and more scenarios require the use of distributed systems. While distributed systems, such as Hadoop and Spark, are receiving an increasing number of threats.

In 2015, the first distributed MapReduce system VC3 [30] was proposed, which keeps the code and data confidential, ensures the correctness and completeness of the results. SGX-PySpark [26] was implemented in 2019, and with the help of TEE, it can protect confidential data.

For other systems, such as database, EnclaveDB [25] uses SGX to protect the database engine and ensure high performance. EncDBDB [9] also uses SGX for data security and is optimized for column-oriented in-memory databases.

In recent years, heterogeneous computing systems, such as Computation Storage Architectures (CSA), have also faced data security issues. IronSafe [38] provides a secure processing

system for heterogeneous computing storage architectures using a hardware-assisted trusted execution environment.

# 4 Research Plan and Methodology

## 4.1 Design a tool to analyze legacy programs

In order to complete the migration of a legacy system, the first step is to analyze the program and identify the code and data that needs to be migrated. We need to consider three parts: data analysis, recognition algorithm and recognition accuracy.

The first is **data analysis**. The analysis aims to provide information about binaries so that the subsequent identification process can proceed smoothly. There are many similar works in the field of binary rewriting to explore the accuracy of binary analysis. BIRD [22] uses a combination of static and dynamic identification methods to improve the accuracy of the analysis. However, for dynamic disassembly, some trapped instructions need to be inserted which is not a good choice for us. Other papers point out that static disassembly can also achieve good results [2]. So we prepare to implement the static analysis tool first and then decide whether to add dynamic analysis based on the accuracy of the analysis.

The next is the **recognition algorithm**. Moat [34] is a detection tool designed by Berkeley that uses automatic theorem proving and information flow analysis methods to discover the possibility of application leakage of secret information in the SGX region by analyzing the assembly language level of the program. Our work can be based on Moat, from which we can extract effective identification and verification algorithms and use them in our analysis tool.

To evaluate **recognition accuracy**, we will consider it in two parts. *Coverage of analysis*: We will use different test cases to see how well the overall analysis is covered. Since we have access to the source code of these test programs, the coverage of the analysis can be measured by some tools such as Gcov [10] and QEMU [4]. *Correctness of the analysis*: We also use open-source test cases to verify correctness. We would like to compare the results of our analysis tool with the results of the source code after automatic analysis by Glamdring [16] to obtain an accurate analysis.

## 4.2 Design a binary rewriting tool to protect confidential code and data

With the help of an analysis tool, it is easy to obtain the code segments that need to be protected and the memory areas where vital data is located.

**Code protection**. We can use the Minimal-invasive translation method for code segments that need to be protected. Similar to the rev.ng [8] and pin [18], we can insert the required functions before and after the code segments. We insert the enclave's entry code and enclave's parameters passing code before the segments. Also, enclave's return parameters can be built at the end of the segments. There should be many more details to note and consider here that need to be discovered and resolved during research.

**Data protection**. Data protection is more complicated than code protection, especially

for global variables. For local variables, we can analyze them, get the program boundary, and put the variables as well as code into enclaves for protection. PtrSplit focuses on C/C++ pointers and identifies pointers that block the generation of partition boundaries [17]. But for global variables, there is no good solution for now. However, global variables are often not recommended for a highly cohesive and low-coupling system [1, 35], so dropping this part of the protection when it cannot be solved is generally not a big deal.

## 4.3   Extend the binary rewriting tool into distributed system

Our work will explore two areas related to distributed systems, how to support legacy distributed programs and enable tools to run on distributed systems.

**Support legacy distributed programs**. These distributed legacy programs tend to have more complex features than to ordinary programs. For example, OpenMP [7] and MPI+OpenMP [14], will use mechanisms such as semaphores, message communication, etc., which cause problems for both the identification and transformation of our tools. We will explore and tackle these challenges during our research process.

**Tools run on distributed systems**. How to run our tools on a distributed platform is a complex work. DQEMU [39] achieves a distributed dynamic binary translation system. It discusses the implementation issues and performance optimization, including data coherence protocol, locking mechanism, system calls, and remote thread migration. We can take the idea of DQEMU and modify our tools to run on a distributed system.

## 4.4   Optimize the above system by software-hardware co-optimization

Whether running secret code in enclaves or using rewriting tools for transformation, they both introduce a significant performance overhead. Many studies investigated the overhead of trusted computing and the corresponding optimization method, including avoiding enclave switches [37] and reduceing page swaps [24, 36]. But these optimizations may be challenging to implement in our tools because it is hard to change the program's original behavior. In addition, binary rewriting also faces significant performance overhead, and existing software optimizations are limited in dealing with these issues [13].

Some studies have proposed several ideas for hardware-assisted acceleration, such as shared memory [12]. So our future work will summarize the existing optimization and search the performance bottlenecks through performance analysis tools, such as Perf, VTune, etc. After we obtain the bottlenecks of the performance, we can summarize the common characteristics and design/modify some hardware modules to speed up our tools and programs similar to the PIE [29].

# 5   Expected Outcomes and Significance

To evaluate the effectiveness of our work, as we mentioned in section 4.1, we can use a series of open-source test cases and benchmarks, such as SPEC, UnixBench. We will also consider using SGXGauge [15], a test suite specifically designed to test the performance of trusted systems, to

measure the performance of our tool. Besides that, we are able to compare our tools with other cross-platform TEEs. For example, we can use HyperEnclave [11], TrustVisor [19], Occlum [33] and Graphene [6] as baseline systems for performance analysis.

Although these cross-platform TEEs propose various solutions to address the legacy softwares, most modify libraries and put them into the enclave. This could lead to the expansion of the TCB and cause an attack surface expansion.

Our solution uses binary rewriting to place the analyzed confidential code and data into enclaves, hardening the legacy softwares and reducing the size of TCB. We anticipate our tools for legacy softwares can reach the following goals:

1. The analysis tool can efficiently and accurately identify vulnerable code and data. Using analysis tool alone can help developers find software weaknesses and flaws.

2. The binary rewriting tool can rewrite legacy softwares into enclave-protected programs with the help of analysis results.

3. Our tools are extended to support distributed legacy programs.

4. Based on the performance data, we design or modify the hardware. With hardware-assisted, we expect the performance can exceed other cross-platform TEEs or even approach the performance of native programs.

## 6 Study Schedule

My study mainly focus on the migration of legacy softwares in trusted systems, and my arrangement is as follows:

- 1st Year (2023-2024)

  1. Complete the core courses and choose the elective courses that will help me with my research.

  2. Read the paper and design the whole system, while revising our current ideas and plans.

- 2nd Year (2024-2025)

  1. Start to implement our analysis and rewrite tools based on first-year design.

  2. Publish the first paper about our analysis tools.

- 3rd Year (2025-2026)

  1. Modify and optimize our rewriting tools.

  2. Extend our tools to support distributed programs.

  3. Publish the second papers about our rewriting tools.

- 4th Year (2025-2026)

1. Review our entire system and analyze their performance bottlenecks.

2. Try to improve the performance of migrated programs by modifying the hardware.

3. Publish the third papers about our rewriting tools.

4. Complete the graduation thesis.

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