

Compatibility of trusted computing

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1 Introduction

2 Research Objectives

1. Design a dynamic analysis/translation tool to support running legacy software on trusted computing systems. This tool can be implement into 3 parts:
 - Dynamic analysis of legacy software, point out which parts of the code are more vulnerable to attack and which data are important and need to be protected.
 - Using the analyzed data to guide the in-place binary translation, insert new code segments, which are used to protect code and migrate data into enclave.
 - Binary translation is used for cross-instruction architecture programs. The migration instruction segments can be generated and added during the translation process.
2. Migrate the whole dynamic analysis/translation tool into a distributed system.
3. Explore performance bottlenecks and help design/modify the hardware architecture. Achieve efficient operation of the entire system with the help of hardware-software co-optimization.

3 Background of Research

3.1 Trusted Computing and TEE

Various encryption and authentication methods (e.g. TLS and file disk encryption) are often used to prevent confidential data loss, theft or corruption. However, relying solely on software for confidential data protection has many problems, such as software vulnerabilities, reverse engineering cracking [32], etc. So, it is useful to use Trusted Execution Environment(TEE) to protect encryption software and data, which provides an environment shielded from outside interference and the necessary mechanisms to build secure and sensitive applications.

Intel Software Guard Extensions(SGX) is a set of security architecture extensions [15]. It provides the enclave environment which can prevent all other software accessing the code and

data located inside an enclave. Also when data leaves the enclave and written into the memory, the data will be automatically encrypted.

ARM TrustZone uses a different approach to TEE by introducing a secure world, which is a new execution environment in the processor in addition to the normal world [16]. The secure world has multiple privilege levels just like an virtual machine(VM), which provides the opportunity to implement an entire trusted software stack.

Due to overly complex operations and unacceptable hardware overhead, Intel begin moving to Trust Domain Extensions(TDX) [22, 23], a new trusted computing architecture introduces a separate trusted hypervisor/VMM. The interaction between trusted virtual machines and external untrusted environments need to be checked by the security check module Shim.

Since TrustZone lacks confidentiality support, ARM v9 proposes Confidential Compute Architecture (CCA). CCA [3] differs from TrustZone, which supports in-memory confidentiality capabilities directly in hardware, specifically designed to protect users' confidential data.

3.2 Binary rewriting and binary translation

Binary rewriting is a technique for modifying or translating the original binary code without having the source code. According to their characteristics, they can be divided into four categories: static, dynamic, minimal-invasive and full-translation.

Static binary rewriting can use the existing information, such as static data flow analysis and symbol table information, to optimize or enhance existing programs [26, 25]. Dynamic binary rewriting is performing alterations during execution, which can be used for performance analysis [14] and hot code patching [5]. Minimal-invasive rewriting is based on branch granularity. It will perform additional instruction at the original location by rewriting to branch instructions. This is often used to add new function to the original program [7]. Full-translation rewriting can convert binaries at any instruction, and usually lift origin binary code into intermediate representations for translating. Some open source tools, like QEMU [4] and Valgrind [18], use full-translation for binary rewriting.

3.3 Distributed system in Trusted computing

With the rise of cloud computing and increase in data sets in recent years, more and more scenarios require the use of distributed systems. While distributed systems, such as Hadoop and Spark, are receiving an increasing number of threats.

In 2015, the first distribute MapReduce system VC3 [24] was proposed, which keeps the code and data confidential, ensures the correctness and completeness of the results. SGX-PySpark [21] was implemented in 2019, and with the help of TEE, it can protect the confidential data.

For other systems, such as database, EnclaveDB [20] uses SGX to protect the database engine and ensure high performance. EncDBDB [8] also uses SGX for data security and is optimized for column-oriented in-memory databases.

In recent years, heterogeneous computing systems such as Computation Storage Architectures (CSA) has also faced data security issues. IronSafe [30] provides a secure processing system

for heterogeneous computing storage architectures using a hardware-assisted trusted execution environment.

4 Research Plan and Methodology

4.1 Design a tool to analyze legacy programs

In order to complete the migration of a legacy system, the first step is to analyze the program and identify the code and data that needs to be migrated. There are three parts we need to consider: data analysis, recognition algorithm and recognition accuracy.

The first is **data analysis**. The purpose of the analysis is to provide information about binaries so that the subsequent identification process can proceed smoothly. There are many similar works in the field of binary rewriting to explore the accuracy of binary analysis. BIRD [17] uses a combination of static and dynamic identification methods to improve the accuracy of the analysis. However, for dynamic disassembly, some trapped instruction need to be inserted which is not a good choice for us. Other papers point out that static disassembly can also achieve good results [2]. So we prepare to implement the static analysis tool first and then decide whether to add dynamic analysis based on the accuracy of the analysis.

The next is the **recognition algorithm**. Moat [27] is a detection tool designed by Berkeley that uses automatic theorem proving and information flow analysis methods to discover the possibility of applications leakage of secret information in the SGX region by analyzing the assembly language level of the program. Our work can be based on Moat, from which we can extract effective identification and verification algorithms and use them in our analysis tools.

In order to evaluate **recognition accuracy**, we will consider it in two parts. *Coverage of analysis*: We will use different test cases to see how well the overall analysis is covered. Since we have access to the source code of these test programs, the coverage of the analysis can be measured by some tools such as gcov [9] and QEMU [4]. *Correctness of the analysis*: We also use the test cases available in the source code to verify correctness. We would like to compare the results of our analysis tool with the results of the source code after automatic analysis by Glamdring [12] in order to obtain an accurate analysis.

4.2 Design a binary rewriting tool to protect confidential code and data

With the help of analysis tools, it is easy to obtain the code segments that need to be protected and the memory areas where important data is located.

Code protection. For code segments that need to be protected, we can use the Minimal-invasive translation method. Similar to the rev.ng [7] and pin [14], we can insert the required functions before and after the code segments. We insert the enclave's entry code and enclave's parameters passing code before the segments, also enclave's return parameters can be built at the end of the segments. There should be many more details to note and consider here that need to be discovered and resolved in the course of research.

Data protection. Data protection is more difficult than code protection, especially for

global variables. For local variables, we can analyze them, get the program boundary, and put the variables as well as code into enclave for protection. PtrSplit focuses on C/C++ pointers, identifies pointers that block the generation of partition boundaries [13]. But for global variables, there is no good solution for now. However, for a highly cohesive and low-coupling system, global variables are often not recommended [1], so dropping this part of the protection when it cannot be solved is generally not a big deal.

4.3 Extend the binary rewriting tool into distributed system

Our work will explore two areas related to distributed systems, how to support legacy distributed programs and how to enable tools to run on distributed systems.

Support legacy distributed programs. These distributed legacy programs tend to have more complex features compared to ordinary programs. For example, OpenMP [6] and MPI+OpenMP [11], they will use mechanisms such as semaphores, message communication, etc., which cause problems for both the identification and the transformation of our tools. We will explore and tackle these challenges during our research process.

Tool runs on distributed systems. How to run our tools on a distributed platform is a complex work. DQEMU [31] achieves a distributed dynamic binary translation system. It discusses the implementation issues and performance optimization including data coherence protocol, locking mechanism, system calls and remote thread migration. We can take the idea of DQEMU and modify our tool to run on a distributed system.

4.4 Optimize the above system by software-hardware co-optimization

Whether running secret code in enclave or using rewriting tools for transformation, they both introduce a significant performance overhead. There are many studies investigated the overhead of trusted computing and the corresponding optimization method, including avoids enclave switches [29] and reduces page swap [19, 28]. But these optimization maybe difficult to implement in our tool because it is difficult to change the original behavior of the program. In addition, binary rewriting also faces the significant performance overhead, and existing software optimizations are limited in dealing with these issues [10].

So our future work will summarize the existing optimization and search the performance bottlenecks through performance analysis tools, such as Perf, VTune, etc. After we obtain the bottlenecks of the performance, we can extract common features and design/modify some hardware modules to speed up our tool and programs.

5 Expected Outcomes and Significance

6 Study Schedule

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