# Compatibility of trusted computing

Qi Hu

01/11/2022

Keywords: Trusted Computing, Distribute System, Software-hardware Co-optimization.

#### 1 Introduction

## 2 Research Objectives

- 1. Design a dynamic analysis/translation tool to support running legacy software on trusted computing systems. This tool can be implement into 3 parts:
  - Dynamic analysis of legacy software, point out which parts of the code are more vulnerable to attack and which data are important and need to be protected.
  - Using the analyzed data to guide the in-place binary translation, insert new code segments, which are used to protect code and migrate data into enclave.
  - Binary translation is used for cross-instruction architecture programs. The migration instruction segments can be generated and added during the translation process.
- 2. Migrate the whole dynamic analysis/translation tool into a distribute system.
- 3. Explore performance bottlenecks and help design/modify the hardware architecture. Achieve efficient operation of the entire system with the help of hardware-software co-optimization.

## 3 Background of Research

#### 3.1 Trusted Computing and TEE

Various encryption and authentication methods (e.g. TLS and file disk encryption) are often used to prevent confidential data loss, theft or corruption. However, relying solely on software for confidential data protection has many problems, such as software vulnerabilities, reverse engineering cracking [18], etc. So, it is useful to use Trusted Execution Environment (TEE) to protect encryption software and data, which provides an environment shielded from outside interference and provides the necessary mechanisms to build secure and sensitive applications.

Intel Software Guard Extensions(SGX) is a set of security architecture extensions [7]. It provides the enclave environment which can prevent all other software accessing the code and data located inside an enclave. Also when data leaves the enclave and written into the memory, the data will be automatically encrypted.

ARM TrustZone uses a different approach to TEE by introducing a secure world, which is a new execution environment in the processor in addition to the normal world [8]. The secure world has

multiple privilege levels just like an virtual machine (VM), which provides the opportunity to implement an entire trusted software stack.

Due to overly complex operations and unacceptable hardware overhead, Intel begin moving to Trust Domain Extensions(TDX) [12, 13], a new trusted computing architecture introduces a separate trusted hypervisor/VMM. The interaction between trusted virtual machines and external untrusted environments need to be checked by the security check module Shim.

Since TrustZone lacks confidentiality support, ARM v9 proposes Confidential Compute Architecture (CCA). CCA differs from TrustZone, which supports in-memory confidentiality capabilities directly in hardware, specifically designed to protect users' confidential data [1].

#### 3.2 Binary rewrite and binary translation

Binary rewriting is a technique for modifing or translating the original binary code without having the source code. According to their characteristics, they can be divided into four categories: static, dynamic, minimal-invasive and full-translation.

Static binary rewrite can use the existing information, such as static data flow analysis and symbol table information, to optimize or enhance existing programs [16, 15]. Dynamic binary rewrite is performing alterations during execution, which can be used for performance analysis [6] and hot code patching [3]. Minimal-invasive rewrite is based on branch granularity. It will perform additional instruction at the original location by rewriting to branch instructions. This is often used to add new function to the original program [4]. Full-translation rewrite can convert binaries at any instruction, and usually lift origin binary code into intermediate representations for translating. Some open source tools, like QEMU [2] and Valgrind [9], use full-translation for binary rewriting.

#### 3.3 Distribute system in Trusted computing

With the rise of cloud computing and increase in data sets in recent years, more and more scenarios require the use of distribute systems. While distribute systems, such as Hadoop and Spark, are receiving an increasing number of threats.

In 2015, the first distribute MapReduce system VC3 was proposed, which keeps the code and data confidential, ensures the correctness and completeness of the results [14]. SGX-PySpark was implemented in 2019, and with the help of TEE, it can protect the confidential data [11].

For other systems, such as database, EnclaveDB uses SGX to protect the database engine and ensure high performance [10]. EncDBDB also uses SGX for data security and is optimized for column-oriented in-memory databases [5].

In recent years, heterogeneous computing systems such as Computation Storage Architectures (CSA) has also faced data security issues. IronSafe provides a secure processing system for heterogeneous computing storage architectures using a hardware-assisted trusted execution environment [17].

- 4 Research Plan and Methodology
- 4.1 Design the tool to analyze legacy programs
- 4.2
- 5 Expected Outcomes and Significance
- 6 Study Schedule

### References

- [1] ARM. Arm confidential compute architecture. https://www.arm.com/en/architecture/security-features/arm-confidential-compute-architecture.
- [2] Bellard, F. Qemu, a fast and portable dynamic translator. In *USENIX Annual Technical Conference*, FREENIX Track (2005).
- [3] BRUENING, D., GARNETT, T., AND AMARASINGHE, S. P. An infrastructure for adaptive dynamic optimization. *International Symposium on Code Generation and Optimization*, 2003. CGO 2003. (2003), 265–275.
- [4] FEDERICO, A. D., PAYER, M., AND AGOSTA, G. rev.ng: a unified binary analysis framework to recover cfgs and function boundaries. *Proceedings of the 26th International Conference on Compiler Construction* (2017).
- [5] FUHRY, B., JAYANTHJAINH, A., AND KERSCHBAUM, F. Encdbdb: Searchable encrypted, fast, compressed, in-memory database using enclaves. 2021 51st Annual IEEE/IFIP International Conference on Dependable Systems and Networks (DSN) (2021), 438–450.
- [6] Luk, C. K., Cohn, R. S., Muth, R., Patil, H., Klauser, A., Lowney, P. G., Wallace, S., Reddi, V. J., and Hazelwood, K. M. Pin: building customized program analysis tools with dynamic instrumentation. In *PLDI* '05 (2005).
- [7] McKeen, F. X., Alexandrovich, I., Berenzon, A., Rozas, C. V., Shafi, H., Shanbhogue, V., and Savagaonkar, U. R. Innovative instructions and software model for isolated execution. In *HASP '13* (2013).
- [8] MUKHTAR, M. A., BHATTI, M. K., AND GOGNIAT, G. Architectures for security: A comparative analysis of hardware security features in intel sgx and arm trustzone. 2019 2nd International Conference on Communication, Computing and Digital systems (C-CODE) (2019), 299–304.
- [9] Nethercote, N., and Seward, J. Valgrind: a framework for heavyweight dynamic binary instrumentation. In *PLDI* '07 (2007).
- [10] PRIEBE, C., VASWANI, K., AND COSTA, M. Enclavedb: A secure database using sgx. 2018 IEEE Symposium on Security and Privacy (SP) (2018), 264–278.
- [11] QUOC, D. L., GREGOR, F., SINGH, J., AND FETZER, C. Sgx-pyspark: Secure distributed data analytics. *The World Wide Web Conference* (2019).

- [12] Sahita, R., Caspi, D., Huntley, B. E., Scarlata, V., Chaikin, B., Chhabra, S., Aharon, A., and Ouziel, I. Security analysis of confidential-compute instruction set architecture for virtualized workloads. 2021 International Symposium on Secure and Private Execution Environment Design (SEED) (2021), 121–131.
- [13] SARDAR, M. U., MUSAEV, S., AND FETZER, C. Demystifying attestation in intel trust domain extensions via formal verification. *IEEE Access* 9 (2021), 83067–83079.
- [14] Schuster, F., Costa, M., Fournet, C., Gkantsidis, C., Peinado, M., Mainar-Ruiz, G., and Russinovich, M. Vc3: Trustworthy data analytics in the cloud using sgx. 2015 IEEE Symposium on Security and Privacy (2015), 38–54.
- [15] Schwarz, B., Debray, S. K., Andrews, G. R., and Legendre, M. P. Plto: A link-time optimizer for the intel ia-32 architecture.
- [16] Shen, B.-Y., Hsu, W.-C., and Yang, W. A retargetable static binary translator for the arm architecture. ACM Trans. Archit. Code Optim. 11, 2 (jun 2014).
- [17] UNNIBHAVI, H., CERDEIRA, D., BARBALACE, A., SANTOS, N., AND BHATOTIA, P. Secure and policy-compliant query processing on heterogeneous computational storage architectures. *Proceedings of the 2022 International Conference on Management of Data* (2022).
- [18] ZIMBA, A., CHISHIMBA, M., AND CHIHANA, S. A ransomware classification framework based on file-deletion and file-encryption attack structures. *ArXiv* abs/2102.10632 (2021).