

NO.	Title	Main author	Title of the periodical or the series	Number, date or frequency	Publisher	Place of publication	Year of publication	Relevant pages	Permanent identifiers (if available)	Is/Will open access provided ?
<b>International Workshops 2016</b>										
W1	<i>Compiler Phase Ordering as an Orthogonal Approach for Reducing Energy Consumption</i>	UPORTO	Compilers for Parallel Computing (CPC)	July 6-8, 2016	CPC 2016	Valladolid, Spain	July 6-8, 2016	online	<a href="https://cpc2016.infor.uva.es/wp-content/uploads/2016/06/CPC2016_paper_26.pdf">https://cpc2016.infor.uva.es/wp-content/uploads/2016/06/CPC2016_paper_26.pdf</a>	Yes
W2	<i>Predictive Modeling Methodology for Compiler Phase-Ordering</i>	Amir H. Ashouri, A. Bignoli, G. Palermo, C. Silvano	7th Workshop on Parallel Programming and Run-Time Management Techniques for Many-core Architectures, 5th Workshop on Design Tools and Architectures for Multicore Embedded Computing Platforms	jan. 18, 2016	ACM	Prague, Czech Republic	2016	7-12	DOI 10.1145/2872421.2872424	
W3	<i>Stack Size Estimation on Machine-Independent Intermediate Code for OpenCL Kernels</i>	Stefano Cherubin, Michele Scandale, Giovanni Agosta	7th Workshop on Parallel Programming and Run-Time Management Techniques for Many-core Architectures, 5th Workshop on Design Tools and Architectures for Multicore Embedded Computing Platforms	jan. 18, 2016	ACM	Prague, Czech Republic	2016	1-6	DOI 10.1145/2872421.2872425	
W4	<i>An Evaluation of Autotuning Techniques for the Compiler Optimization Problems</i>	Amir Hossein Ashouri, Gianluca Palermo, Cristina Silvano	RES4ANT Workshop at DATE2016 Conference	março 18, 2016	CEUR	Dresden, Germany	2016	1-6		
W5	<i>Automatic Pruning of Autotuning Parameter Space for OpenCL Applications</i>	Ahmet Erdem, Gianluca Palermo, Cristina Silvano	RES4ANT Workshop at DATE2016 Conference	março 18, 2016	CEUR	Dresden, Germany	2016	1-6		
W6	<i>Application Adaptation at Runtime through Dynamic Knobs Autotuning</i>	Davide Gadioli, Gianluca Palermo, Cristina Silvano	RES4ANT Workshop at DATE2016 Conference	março 18, 2016	CEUR	Dresden, Germany	2016	1-6		
<b>International Workshops 2017</b>										
W7	<i>Benefits in Relaxing the Power Capping Constraint</i>	Daniele Cesarini, Andrea Bartolini, Luca Benini	ANDARE 2017 Workshop @PACT 2017 Conference	setembro 11, 2017	ACM	Portland, Oregon, USA	2017		doi:10.1145/3152821.3152878	<a href="https://dl.acm.org/citation.cfm?id=3152878">https://dl.acm.org/citation.cfm?id=3152878</a>
<b>International Workshops 2018</b>										
W8	<i>Aspect-Driven Mixed-Precision Tuning Targeting GPUs</i>	Ricardo Nobre, Luis Reis, João Bispo, Tiago Carvalho, João M. P. Cardoso, Stefano Cherubin, Giovanni Agosta	PARMA-DITAM@HIPEAC 2018: 26-31		ACM		2018		10.1145/3183767.3183776	<a href="https://repositorio-aberto.up.pt/bitstream/10216/117189/2/297786.pdf">https://repositorio-aberto.up.pt/bitstream/10216/117189/2/297786.pdf</a>
W9	<i>AutoPar-Clava: An Automatic Parallelization source-to-source tool for C code applications</i>	Hamid Arabnejad, João Bispo, Jorge G. Barbosa, João M. P. Cardoso	PARMA-DITAM@HIPEAC 2018: 13-19		ACM		2018		0.1145/3183767.3183770	<a href="https://repositorio-aberto.up.pt/bitstream/10216/111508/2/261002_1.pdf">https://repositorio-aberto.up.pt/bitstream/10216/111508/2/261002_1.pdf</a>
W10	<i>Evaluation of NTP/PTP Fine-Grain Synchronization Performance in HPC Clusters</i>	Antonio Libri, Andrea Bartolini, Daniele Cesarini, Luca Benini	ANDARE 2018 Workshop @PACT 2018 Conference	novembro 4, 2018	ACM	Limassol, Cyprus	2018		10.1145/3295816.3295819	<a href="https://www.research-collection.ethz.ch/handle/20.500.11850/306928">https://www.research-collection.ethz.ch/handle/20.500.11850/306928</a>
W11	<i>DiG: Enabling Out-of-Band Scalable High-Resolution Monitoring for Data-Center Analytics, Automation and Control</i>	Antonio Libri, Andrea Bartolini, Luca Benini	DAAC 2018 Workshop @SC'18 Conference		IEEE		2018			<a href="https://www.research-collection.ethz.ch/handle/20.500.11850/306925">https://www.research-collection.ethz.ch/handle/20.500.11850/306925</a>
W12	<i>COUNTDOWN - A Run-time Library for Application-agnostic Energy Saving in MPI Communication Primitives</i>	Daniele Cesarini, Andrea Bartolini, Luca Benini	ANDARE 2018 Workshop @PACT 2018 Conference	novembro 4, 2018	ACM	Limassol, Cyprus	2018		To appear	To appear

W13	<i>The D.A.V.I.D.E. Big-data-powered Fine-grain Power and Performance Monitoring Support</i>	Andrea Bartolini, Andrea Borghesi, Antonio Libri, Francesco Beneventi, Daniele Gregori, Simone Tinti, Cosimo Gianfreda, Piero Altoè	BigDAW 2018 : International Workshop on Big Data Analytics - Proceedings of the 15th ACM International Conference on Computing Frontiers	15th, Annual	ACM	Ischia, Italy	2018		10.1145/3203217. 3205863	To appear
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