

NO.	Title	Main author	Title of the periodical or the series	Number, date or frequency	Publisher	Place of publication	Year of publication	Relevant pages	Permanent identifiers (if available)	Is/Will open access provided ?
<i>International Journals (in chronological order)</i>										
J1	COBAYN: Compiler Autotuning Framework using Bayesian Networks	Amir Hossein Ashouri, Giovanni Mariani, Gianluca Palermo, Eunjung Park, John Cavazos, Cristina Silvano	ACM Transactions on Architecture and Code Optimization	Volume 13 Issue 2, June 2016	ACM	ACM DL	2016		DOI 10.1145/2928270	
J2	Thermal Analysis and Interpolation Techniques for a Logic + WideIO Stacked DRAM Test Chip	Francesco Beneventi, Andrea Bartolini, Pascal Vivet, and Luca Benini	IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS	vol. 35, no. 4, pp. 623-636	IEEE	IEEE	2016	623-636	10.1109/TCAD.2015.2474362	
J3	Self-Aware Thermal Management for High-Performance Computing Processors	Andrea Bartolini, Roberto Diversi, Daniele Cesarini, Francesco Beneventi	IEEE Design & Test	vol. 35, no. 5, pp. 28-35	IEEE	IEEE	2018	28-35	10.1109/MDAT.2017.2774774	https://ieeexplore.ieee.org/stamp/stamp.jsp?arnumber=8113477
J4	COUNTDOWN: a Run-time Library for Performance-Neutral Energy Saving in MPI Applications	Daniele Cesarini, Andrea Bartolini, Pietro Bonfà, Carlo Cavazzoni, Luca Benini	IEEE Transactions on Parallel and Distributed Systems	-	IEEE	IEEE	2019	-	Under review	https://arxiv.org/abs/1806.07258
J5	Quantifying the Impact of Variability and Heterogeneity on the Energy Efficiency for a Next-Generation Ultra-Green Supercomputer	Francesco Fraternali, Andrea Bartolini, Carlo Cavazzoni, Luca Benini	IEEE Transactions on Parallel and Distributed Systems	vol. 29, no. 7, pp. 1575-1588	IEEE	IEEE	2018	1575-1588	10.1109/TPDS.2017.2766151	
J6	Robust identification of thermal models for in-production High-Performance-Computing clusters with machine learning-based data selection	Federico Pittino, Roberto Diversi, Luca Benini, Andrea Bartolini	IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS	-	IEEE	IEEE	2019	-	Under review	
J7	Aspect composition for multiple target languages using LARA	Pedro Pinto, Tiago Carvalho, João Bispo, Miguel António Ramalho, João M. P. Cardoso	Computer Languages Systems & Structures	53: 1-26	Elsevier	Elsevier	2018		https://doi.org/10.1016/j.cl.2017.12.003	https://repositorio-aberto.up.pt/bitstream/10216/117169/2/260268.pdf
J8	libVersioningCompiler: An easy-to-use library for dynamic generation and invocation of multiple code versions	Stefano Cherubin, Giovanni Agosta	SoftwareX	vol 7	Elsevier	Online	2018	95-100	https://doi.org/10.1016/j.softx.2018.03.006	https://www.sciencedirect.com/science/article/pii/S2352711018300426
J9	A Framework for Automatic and Parameterizable Memoization	Loïc Besnard, Pedro Pinto, Imane Lasri, João Bispo, Erven Rohou, João M.P. Cardoso	SoftwareX	Under review	Elsevier					
J10			ACM TACO	to be submitted (final preparation)	ACM					
J11			IEEE Trans. SE	to be submitted (final preparation)	IEEE					
J12	Clava		SoftwareX	to be submitted (final preparation)	Elsevier					