

SWVSM's
Tatyasaheb Kore Institute of Engineering and Technology, Warananagar
Department of Computer Science and Engineering
T.E. Division (Academic Year: 2018-19)
Domain Specific Mini Project (DSMP)

DSMP TIMELINE 2018-19

Sr.No.	Project Schedule	Start Date	End Date	No. of Days
1	Domain Knowledge (5%)	10/12/2018	22/12/2018	12
2	Problem Definition & Scope (5%)	24/12/2018	29/12/2018	6
3	Requirement Gathering (5%)	31/12/2018	05/01/2019	6
4	System Architecture (5%)	07/01/2019	12/01/2019	6
5	Synopsis Report Submission (5%)	12/01/2019	12/01/2019	1
6	Synopsis Presentation (5%)	14/01/2019	19/01/2019	5
7	Design Methodology (20%)	21/01/2019	26/01/2019	6
8	Module Implementation (5%)	28/01/2019	09/02/2019	12
9	DSMP Presentation I (10%)	11/02/2019	16/02/2019	6
10	Module Implementation (15%)	18/02/2019	16/03/2019	24
11	Test Specification (5%)	18/03/2019	19/03/2019	2
12	Project Report (10%)	20/03/2019	23/03/2019	6
13	Final Presentation (10%)	25/03/2019	31/03/2019	6
				98

DSMP Coordinator

(Prof. N.J.Ghatge)

HOD

(Prof. A.G.Patil)