# **CMS12-A4**

# **Development Board**

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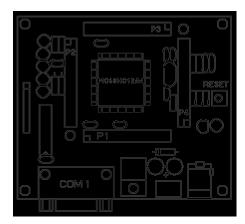
# **GETTING STARTED**

The Axiom CMS12-A4 board is a fully assembled, single chip development board for the Motorola 68HC12A4 Microcontroller, complete with wall plug style power supply and serial cable. To get started quickly, perform the following test now to make sure everything is working correctly:

- 1. Install the software on your PC:
  - Create a directory on your PC hard drive for the utility software and copy the contents of the UTL12 disk to that directory. NOTE: the AX12.EXE utility on this disk requires DOS and will not run under NT.
  - If your board came with the AX12 for Windows software, run the SETUP.EXE program on the setup disk. This software will work with any 32-bit version of windows (95+), including NT.
  - If you have the Background Debug Module, install the BDM software from the floppy disk by running the SETUP.EXE program.
- 2. Connect one end of the supplied serial cable to a free COM port on your PC. Connect the other end of the cable to the COM1 port on the CMS12-A4 board.
- 3. Apply power to the board by plugging in the wall plug power supply that came with the system.
- 4. If you have AX12 for Windows:
  - Run the AX12W program. From Edit/Options menu, select the COM port you're using on your PC.
  - Press the Utilities button and select the CMS12-A4 board.
  - Follow the instructions onscreen to send the utilities software to the board.
- 5. If you do not have AX12 for Windows:
  - Change to the directory containing the utility software and execute the program: AX12.EXE.
  - Select the PC COM port that you're using and when asked for board type select CMS12-A4.
  - From the AX12 menu select HC12 Utilities.
  - Follow the instructions onscreen to send the utilities software to the board.
- 6. If everything is working properly, the bootload software should be sent to the CMS12 board after which you can read and write to the 68HC12 Internal EEPROM memory.

If you receive an error message, see the **TROUBLESHOOTING** section of this manual.

## CMS12 Board



# **Development Philosophy**

Software development on the CMS12-A4 can be performed using a Background Debug Module (BDM) connected to the DEBUG connector on the PB module to assist in creating and debugging your program stored in RAM or Internal EEPROM (see the Memory Map).

After satisfactory operation running under a debugger, your program can be written to Internal EEPROM thru the BDM or using the AX12 (or AX12W) utility program. When programming is complete your program will run automatically when the CMS12-A4 is powered on or RESET is applied.

## **SOFTWARE**

There are many useful programs on the UTL12 disk included with the CMS12-A4 that can make developing projects easier. You can also download the latest version of this disk free at any time from our web page at: http://www.axman.com.

All of the utilities on the UTL12 disk require DOS, or a dos window running under MS Windows™. The main programming interface to the CMS12-A4 board is the AX12 program. This program also provides an interface to the assembler and compiler software provided by Karl Lunt. The DOS version is included on the UTL12 disk and there is also a Windows version available from the manufacturer. The DOS version will not work with Windows NT.

Both versions of AX12 communicate with the board via its COM1 port and include a Terminal window for interfacing with other programs running on the CMS12-A4.

In addition to the AX12 terminal, most communications programs will work with the CMS12-A4. Even the Terminal program in Microsoft Windows™ will do an adequate job. Communications settings should be set to 9600 baud, 1 start, 1 stop, 8 data, no parity.

See the **README** file on the utility disk for a complete listing of all programs and files on the UTL12 disk.

Axiom also manufacturers a Background Debug Module for this board, which is a powerful real-time source level debugging tool. Contact Axiom for more information.

# **DOCUMENTATION**

All of the documentation for the CMS12-A4 hardware and software is available in electronic form. See the **MANUAL.TXT** file on the UTL12 disk or select HELP from the menu for the AX12 for DOS documentation.

Complete documentation for the AX12 for Windows and BDM Software products are available from the Help menu of these programs.

Owners manuals and Users guides for the M68HC12-A4 micro and programming interface is available in Adobe Acrobat (.pdf) on the Axiom web page **(www.axman.com)**. Here you will also find updates to this document as well as supporting part and schematic documentation. You can also download a free viewer for Acrobat (.pdf) files.

# **MEMORY MAP**

FFFF FFFE	RESET Vector CSP0 Chip Select	2 Bytes
	Unused	
1FFF 1F80	Axiom Bootload Kernel	
1F7F	HC12 Internal EEPROM Program or Data	4K
1000 0FFF		
0111	CMS12 RAM Space CSD	1K
0C00		
0BFF	HC12 Internal RAM	1K
0800		
07FF	CMS12 RAM Space CS3 EPAGE if used	1K
0400	OOO EI MOE II doed	
03FF	CS0, CS1, CS2, CS3 if used	512
0200	CMS12 RAM SPACE	Bytes
01FF	68HC12A4 Internal Registers	540
0000	See HC12A4 Technical Summary (MC68HC812A4) for complete listing and usage information	512 Bytes

# **Bootloader Firmware**

The MC68HC812A4 is pre-programmed with bootload firmware that operates in conjunction with the AX12 Utility software to provide a low cost debugging and programming environment for the M68HC12. The 64 Byte firmware is programmed into the internal HC812A4 EEPROM and becomes operational when the HC812A4 is in Single Chip Mode to support AX12 utility operations.

#### **Firmware Memory**

Single Chip Mode (normal) \$FF80 - \$FFBF and \$FFFE/FFFF = Reset Vector Expanded Modes (default) \$1F80 - \$1FBF and \$1FFE/1FFF = \$FF80

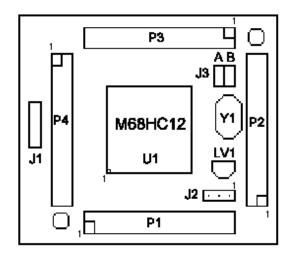
Caution should be used when programming or erasing the internal 68HC812A4 EEPROM to assure the bootload firmware is not erased or corrupted. The EEPROT Register (Location \$00F1 default) bits 0 and 1 should be set high during the users software initialization sequence to protect the bootload firmware.

# **HARDWARE**

# PB68HC12A4 Controller Module

The PB68HC12A4 is installed on the CMS12 board as a standard to allow replacement if necessary. The CMS12 will mount the MC68HC812A4 Microcontroller directly, please contact the factory if this option is preferred.

The Options Jumpers on the PB68HC12A4 should not be used while installed on the CMS12 board.



# J1 SCI0 Header

The J1 header provides access to the HC12 SCI0 serial channel.

PIN 1	HC12 TxD0
PIN 2	HC12 RxD0
PIN 3	+Vdd (5 volts)
PIN 4	Vss (Ground)

# J1 Debug Header

The J2 header is an auxiliary Background Debug Port.

PIN 1	RESET active low
PIN 2	Vss Ground
PIN 3	BGND debug pin

# J3 Mode Option

J3 should be left open while the PB68HC12A4 is installed on the CMS12 board.

# Y1 Crystal Oscillator

Y1 is 16.00MHz standard. This provides an instruction clock / bus speed of 8mhz to the CMS12.

#### LV1 Reset Generator

LV1 is a voltage detector that will generate an active low RESET state if Vdd is below +4.4 VDC. This duplicates the operation of the CMS12 RESET Generator.

# **Ports and Connectors**

# PB68HC12A4 - I/O Connectors

The Motorola M68HC12 Microcontroller is attached to four dual row 14 pin connectors (28 pins each) which are configured as follows:

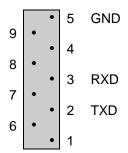
P1				P2							
Vss	1	1	2	2	Vdd	D9/PC1	29	1	2	30	PC2/D10
PJ0	3	3	4	4	PJ1	D11/PC3	31	3	4	32	PC4/D12
PJ2	5	5	6	6	PJ3	D13/PC5	33	5	6	34	PC6/D14
PJ4	7	7	8	8	PJ5	D15/PC7	35	7	8	36	PE0/XIRQ
PJ6	9	9	10	10	PJ7	IRQ/PE1	37	9	10	38	PE2/R/W
A16/PG0	11	11	12	12	PG1/A17	LSTR/PE3	39	11	12	40	/RESET
A18/PG2	13	13	14	14	Vdd	Vss	41	13	14	42	Vdd
Vss	15	15	16	16	PG3/A19	Vddpll	43	15	16	44	XFC
A20/PG4	17	17	18	18	PG5/A21	Vsspll	45	17	18	46	EXTAL
BKGD	19	19	20	20	PD0/D0	XTAL	47	19	20	48	PE4/ECLK
D1/PD1	21	21	22	22	PD2/D2	MODA/PE5	49	21	22	50	PE6/MODB
D3/PD3	23	23	24	24	PD4/D4	ARST/PE7	51	23	24	52	PB0/A0
D5/PD5	25	25	26	26	PD6/D6	A1/PB1	53	25	26	54	PB2/A2
D7/PD7	27	27	28	28	PC0/D8	A3/PB3	55	27	28	56	PB4/A4
P3											
		P3	3						94		
A5/PB5	57	P3	2	58	PB6/A6	$V_RH$	85	4		86	$V_{RL}$
A5/PB5 A7/PB7	57 59			58 60	PB6/A6 PA0/A8	V <sub>RH</sub> PAD0	85 87	1	2	86	V <sub>RL</sub> PAD1
		1	2					3	2		
A7/PB7	59	1	2	60	PA0/A8	PAD0	87	1 3 5	2 4 6	88	PAD1
A7/PB7 A9/PA1	59 61	1 3 5	2 4 6	60	PA0/A8 PA2/A10	PAD0 PAD2	87 89	1 3 5 7	2 4 6 8	88	PAD1 PAD3
A7/PB7 A9/PA1 A11/PA3	59 61 63	1 3 5 7	2 4 6 8	60 62 64	PA0/A8 PA2/A10 PA4/A12	PAD0 PAD2 PAD4	87 89 91	1 3 5 7	2 4 6 8 10	88 90 92	PAD1 PAD3 PAD5
A7/PB7 A9/PA1 A11/PA3 A13/PA5	59 61 63 65	1 3 5 7 9	2 4 6 8 10	60 62 64 66	PA0/A8 PA2/A10 PA4/A12 PA6/A14	PAD0 PAD2 PAD4 PAD6	87 89 91 93	1 3 5 7 9 11	2 4 6 8 10 12	88 90 92 94	PAD1 PAD3 PAD5 PAD7
A7/PB7 A9/PA1 A11/PA3 A13/PA5 A15/PA7	59 61 63 65 67	1 3 5 7 9 11	2 4 6 8 10 12	60 62 64 66 68	PA0/A8 PA2/A10 PA4/A12 PA6/A14 PF0/CS0	PAD0 PAD2 PAD4 PAD6 V <sub>DDA</sub>	87 89 91 93 95	1 3 5 7 9 11 13	2 4 6 8 10 12 14	88 90 92 94 96	PAD1 PAD3 PAD5 PAD7 V <sub>SSA</sub> PS1/TxD0
A7/PB7 A9/PA1 A11/PA3 A13/PA5 A15/PA7 CS1/PF1	59 61 63 65 67 69	1 3 5 7 9 11 13	2 4 6 8 10 12 14	60 62 64 66 68 70	PA0/A8 PA2/A10 PA4/A12 PA6/A14 PF0/CS0 PF2/CS2	PAD0 PAD2 PAD4 PAD6 V <sub>DDA</sub> RxD0/PS0	87 89 91 93 95	1 3 5 7 9 11 13	2 4 6 8 10 12 14 16	88 90 92 94 96 98	PAD1 PAD3 PAD5 PAD7 V <sub>SSA</sub> PS1/TxD0 PS3/TxD1
A7/PB7 A9/PA1 A11/PA3 A13/PA5 A15/PA7 CS1/PF1 CS3/PF3	59 61 63 65 67 69 71	1 3 5 7 9 11 13	2 4 6 8 10 12 14 16	60 62 64 66 68 70 72	PA0/A8 PA2/A10 PA4/A12 PA6/A14 PF0/CS0 PF2/CS2 PF4/CSD	PAD0 PAD2 PAD4 PAD6 V <sub>DDA</sub> RxD0/PS0 RxD1/PS2	87 89 91 93 95 97	1 3 5 7 9 11 13 15	2 4 6 8 10 12 14 16 18	88 90 92 94 96 98	PAD1 PAD3 PAD5 PAD7 V <sub>SSA</sub> PS1/TxD0 PS3/TxD1 PS5/SD0
A7/PB7 A9/PA1 A11/PA3 A13/PA5 A15/PA7 CS1/PF1 CS3/PF3 CSP0/PF5	59 61 63 65 67 69 71 73	1 3 5 7 9 11 13 15	2 4 6 8 10 12 14 16 18	60 62 64 66 68 70 72 74	PA0/A8 PA2/A10 PA4/A12 PA6/A14 PF0/CS0 PF2/CS2 PF4/CSD PF6/CSP1	PAD0 PAD2 PAD4 PAD6 V <sub>DDA</sub> RxD0/PS0 RxD1/PS2 SDI/PS4	87 89 91 93 95 97 99	1 3 5 7 9 11 13 15 17	2 4 6 8 10 12 14 16 18	88 90 92 94 96 98 100	PAD1 PAD3 PAD5 PAD7 V <sub>SSA</sub> PS1/TxD0 PS3/TxD1 PS5/SD0 PS7/SCK
A7/PB7 A9/PA1 A11/PA3 A13/PA5 A15/PA7 CS1/PF1 CS3/PF3 CSP0/PF5 PH0	59 61 63 65 67 69 71 73	1 3 5 7 9 11 13 15 17	2 4 6 8 10 12 14 16 18 20	60 62 64 66 68 70 72 74 76	PA0/A8 PA2/A10 PA4/A12 PA6/A14 PF0/CS0 PF2/CS2 PF4/CSD PF6/CSP1 PH1	PAD0 PAD2 PAD4 PAD6 V <sub>DDA</sub> RxD0/PS0 RxD1/PS2 SDI/PS4 MOSI/PS6	87 89 91 93 95 97 99 101	1 3 5 7 9 11 13 15 17 19 21	2 4 6 8 10 12 14 16 18 20 22	88 90 92 94 96 98 100 102	PAD1 PAD3 PAD5 PAD7 V <sub>SSA</sub> PS1/TxD0 PS3/TxD1 PS5/SD0 PS7/SCK PT1
A7/PB7 A9/PA1 A11/PA3 A13/PA5 A15/PA7 CS1/PF1 CS3/PF3 CSP0/PF5 PH0 PH2	59 61 63 65 67 69 71 73 75	1 3 5 7 9 11 13 15 17 19 21	2 4 6 8 10 12 14 16 18 20 22	60 62 64 66 68 70 72 74 76	PA0/A8 PA2/A10 PA4/A12 PA6/A14 PF0/CS0 PF2/CS2 PF4/CSD PF6/CSP1 PH1 PH3	PAD0 PAD2 PAD4 PAD6 V <sub>DDA</sub> RxD0/PS0 RxD1/PS2 SDI/PS4 MOSI/PS6 PT0	87 89 91 93 95 97 99 101 103	1 3 5 7 9 111 133 155 177 199 211 233	2 4 6 8 10 12 14 16 18 20 22 24	88 90 92 94 96 98 100 102	PAD1 PAD3 PAD5 PAD7 V <sub>SSA</sub> PS1/TxD0 PS3/TxD1 PS5/SD0 PS7/SCK PT1 PT3

- Small numbers next to Connector pin Numbers are MC68HC812A4 package pin number for reference.
- The PB68HC12A4 contains the crystal oscillator and an additional RESET generator.
- See the M68HC812A4 Technical Manual or PB68HC12A4 Manual for more information.

#### SERIAL COM PORT

**COM1** interfaces the HC12 internal SCI0 serial port (I/O PS0 and PS1) and is a simple three wire asynchronous serial interface with hard wired Clear to Send (CTS) and Data Terminal Ready (DTR). These two logic level signals are coupled through a RS232 level shifter to the COM1 connector.

## COM1 DB9S Style Connector



- Permanent jumpers between following pins:
  - 4 → 1 and 6 (DTR/DSR/DCD)
  - $7 \rightarrow 8 (RTS/CTS)$
- COM1 is set to connect directly to a PC serial port with a straight thru type of cable (supplied).

# A/D Reference

The  $V_{RH}$  and  $V_{RL}$  lines from the HC12 are connected to +5v through R2 and to ground through R1 respectively. These two surface mount resistors are on the bottom (solder) side of the CMS12 board. The resistors are identified on the silk screen by their reference designators. The appropriate resistor(s) need to be removed in order to apply an external reference to the  $V_{RH}$  and/or  $V_{RL}$  inputs.

## CMS12 Other Features

#### **RESET GENERATION**

The CMS12 has a voltage level detector (U7) that generates an active low Reset when the supply voltage is below ~4.5VDC. The RESET Switch will apply a ground to the Reset line as long as the switch is depressed.

#### **POWER SUPPLY**

The CMS12 has a regulated power supply that accepts +7 to +25VDC and outputs +5VDC at up to 500ma. Normally input voltages are supplied by the provided wall plug installed into J6. TB1 provides access to output or optional input to the +V input voltage, Ground and +5VDC system supply. Caution should be used if supplying an external +5VDC system voltage so that a voltage level of +6VDC is not exceeded or permanent damage to components on the CMS12 board may occur.

#### PHASE-LOCKED LOOP

CMS12 HC12 PLL loop filter components RX1, CX1 and CX2 are not installed. If PLL operation is desired, the user should review the Motorola Application Note and determine the component values to be used for the frequency of desired operation.

# **TROUBLESHOOTING**

The CMS12 board is fully tested and operational before shipping. If it fails to function properly, inspect the board for obvious physical damage first. Ensure that all socket IC devices are properly seated in the their sockets.

The most common problems are improperly configured communications parameters and attempting to use the wrong COM port on the PC. Verify that your communications port is working by substituting a known good serial device, or by doing a loop back diagnostic. Verify that no other devices are conflicting with the port (such as a mouse, modem, etc.).

Check your hardware configuration jumpers and switches. Verify the power source. You should measure 9 volts between GND and +9V test point pads on the board near J1. If no voltage is found, verify wall plug connections to 115VAC-outlet and power connector. Disconnect all external connections to the board except for COM1 to the PC and the wall plug. Follow these steps in the order given:

# **Troubleshooting Steps**

- 1. Visual Inspection
- 2. Verify power by checking for +9 volts between GND and +9V test point pads.
- 3. Re-Check the communications parameters.
- 4. Make sure that the RESET line is not being held low.
- 5. Verify presence of 16MHz sine wave on the crystal if possible.
- 6. Please check off these steps and any others you may have performed before calling so we can better help you.

# Tips and Suggestions

Following are a number of tips, suggestions, and answers to common questions that will solve most problems that users have with the AX12 development system. This information is also available in the AX12 program under Troubleshooting, which will have the most complete, updated information. There also may be a newer version of the AX12 utility software available. You can download the latest version for FREE on our web page at: WWW.AXMAN.COM

# **AX12 Program**

- If you are using AX12 you must access the board in Single-Chip mode only.
- Be certain that the data cable you are using is bi-directional and is connected securely to both the PC and the board. Also, make sure you are using the correct serial port.
- Make sure the correct power is supplied to the board. You should only use a 9-volt, 300-ma adapter or power supply. If you are using a power strip, make sure it is turned on.
- Make sure you load your code to an address space that actually exists. See the Memory Map if you are not sure.
- If you are running in a multitasking environment (such as Windows) close all programs in the background to be certain no serial conflict occurs.
- If the Assembler menu option does not work properly on your system you can modify it is operation
  by editing the file DO\_ASM.BAT in the AX12 directory. This can also be done from the Options
  menu.
- You can reset all AX12 configuration options to their original state by deleting the file named AX12.CFG. This file will be re-created the next time you run AX12.

#### **Code Execution**

 Make sure ALL jumpers are set correctly according to your board's configuration. Read the hardware manual section on jumpers carefully if you are not sure.