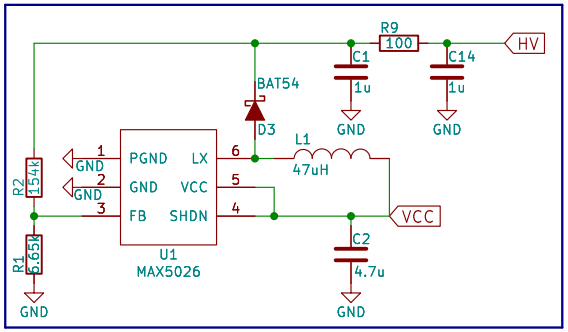
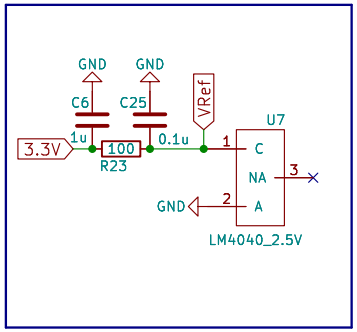
DC–DC Booster

This circuit takes the VCC line, and increases the voltage to +30V.
This HV line is used to provide the reverse bias to the SiPM.

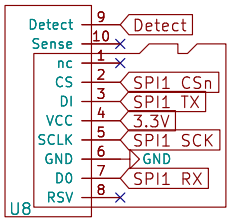


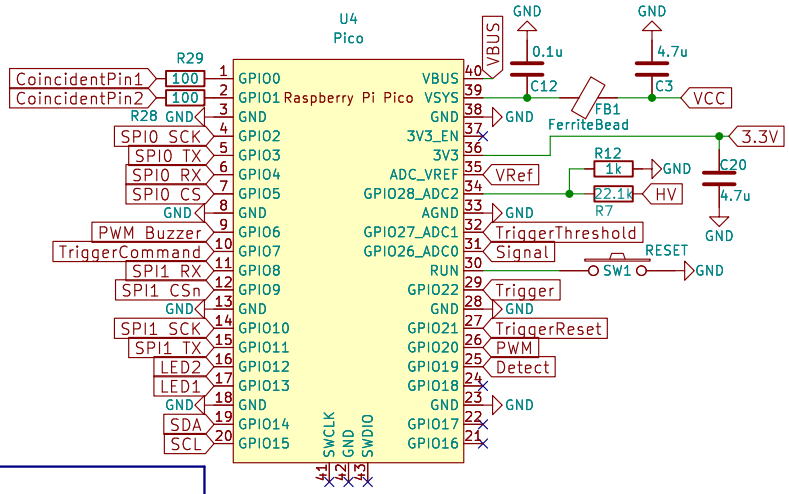
Biasing and reference circuit

Voltage bias for ADC reference and op amp inputs.



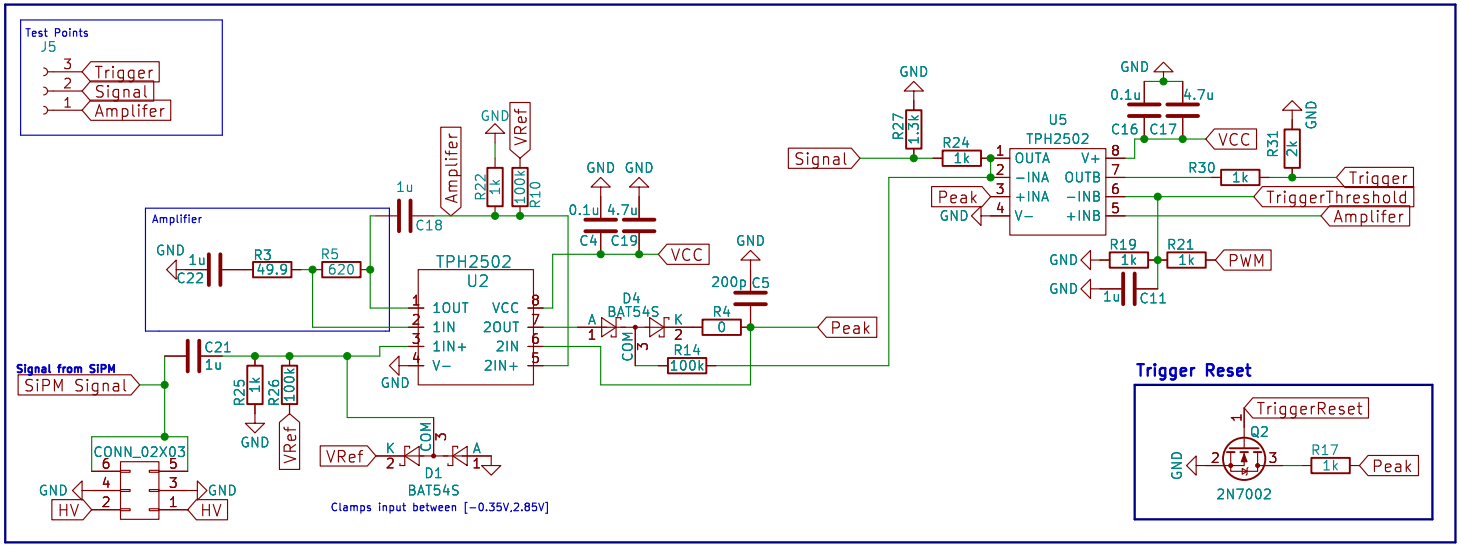
SD Card Socket





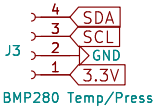
Amplifying, Triggering, and Peak Detecting circuits

The SiPM pulse is amplified and then peak detected. The amplified pulse also feeds a comparator circuit to trigger the detector readout.

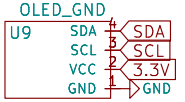


Trigger Reset

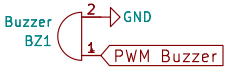
Temp/Press



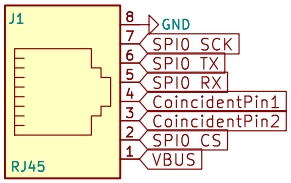
OLED



Buzzer

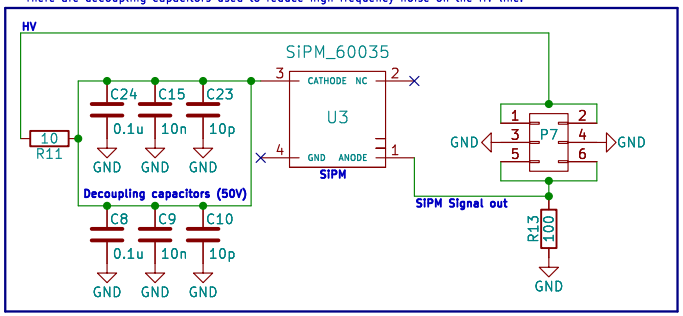


Coincidence Connection

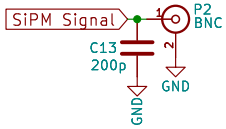


SiPM Circuit

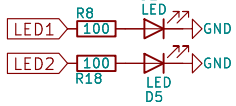
Here, we are biasing the SiPM with the HV voltage (30V).
There are decoupling capacitors used to reduce high frequency noise on the HV line.



BNC Output



LEDs



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