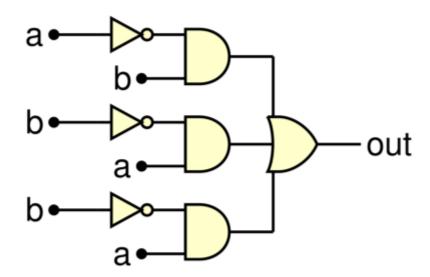
```
In [36]: import wavedrom
         import cairosvg
          from PIL import Image
         import matplotlib.pyplot as plt
          def make_logic(circuit):
              svg = wavedrom.render(f"""{circuit}
              """)
              svg["width"] ="2500px"
              svg["height"] ="2500px"
              svg.saveas("/tmp/output.svg")
              svg.saveas("/tmp/demo2.svg")
              cairosvg.svg2png(url='/tmp/output.svg', write_to='/tmp/output.png')
              image = Image.open("/tmp/output.png")
              plt.figure(figsize=(8, 8)) # Adjust the width and height values as desired
              plt.imshow(image)
              plt.axis('off')
              plt.show()
In [39]: circuit = { "assign":[
                ["out",
                  ["|",
                    ["&", ["~", "a"], "b"],
["&", ["~", "b"], "a"],
                    ["&", ["~", "b"], "a"]
```



]}

make_logic(circuit)