

Sheet: input

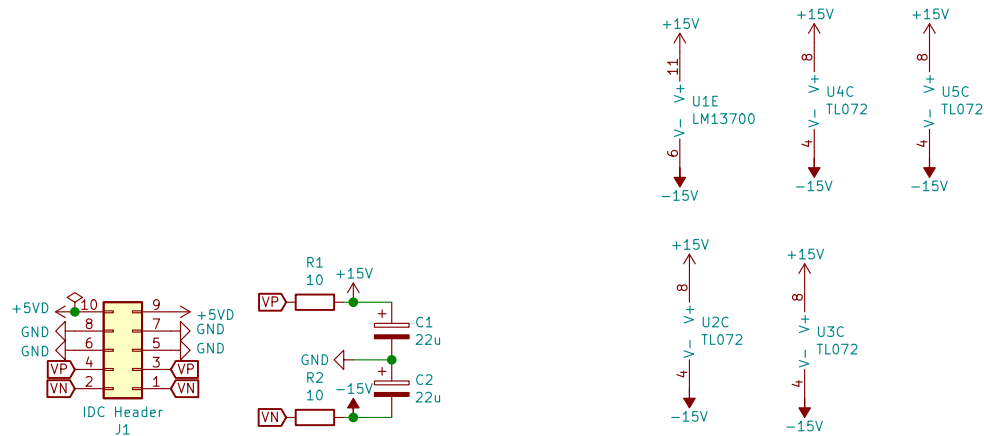
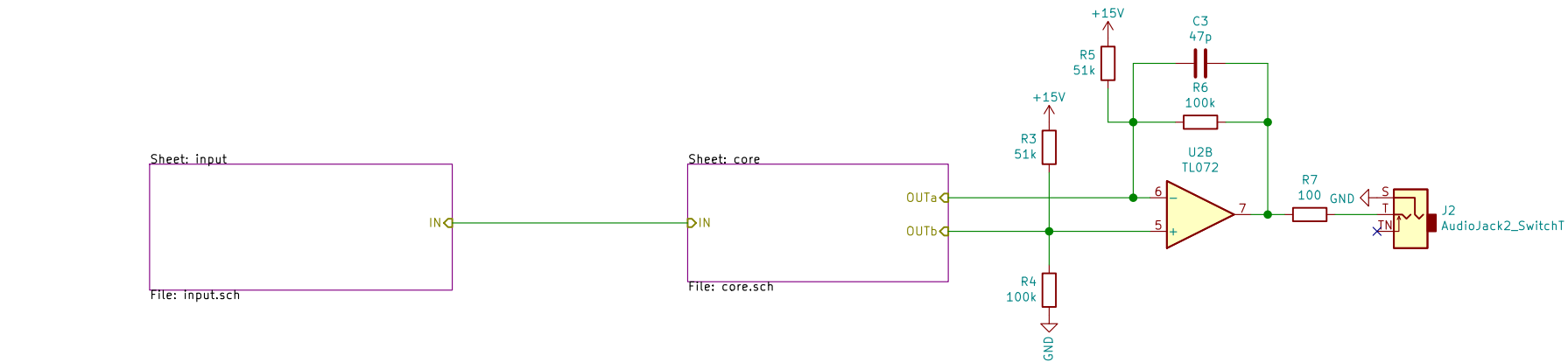
File: input.sch

Sheet: core

File: core.sch

Sheet: Voltage_Reference

File: Voltage_Reference.sch



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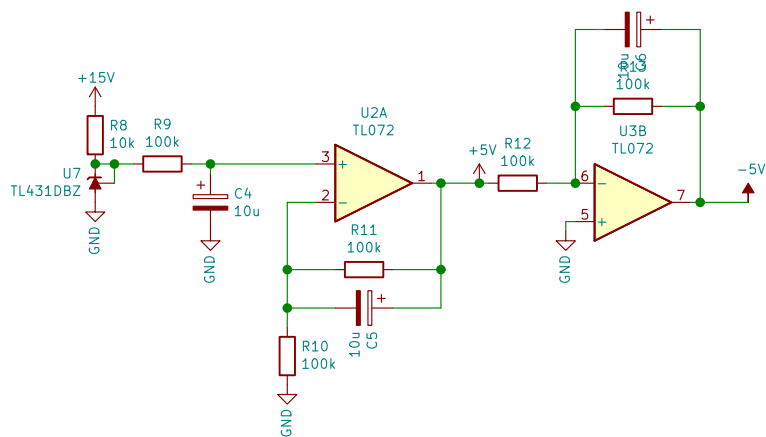
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schema for pcb

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File: main.sch

Title: (title)

Size: A4 Date:
KiCad E.D.A. eeschema 5.1.9+dfsg1-1

Rev: R01
Id: 1/4



Sheet: /Voltage_Reference/
File: Voltage_Reference.sch

Title:

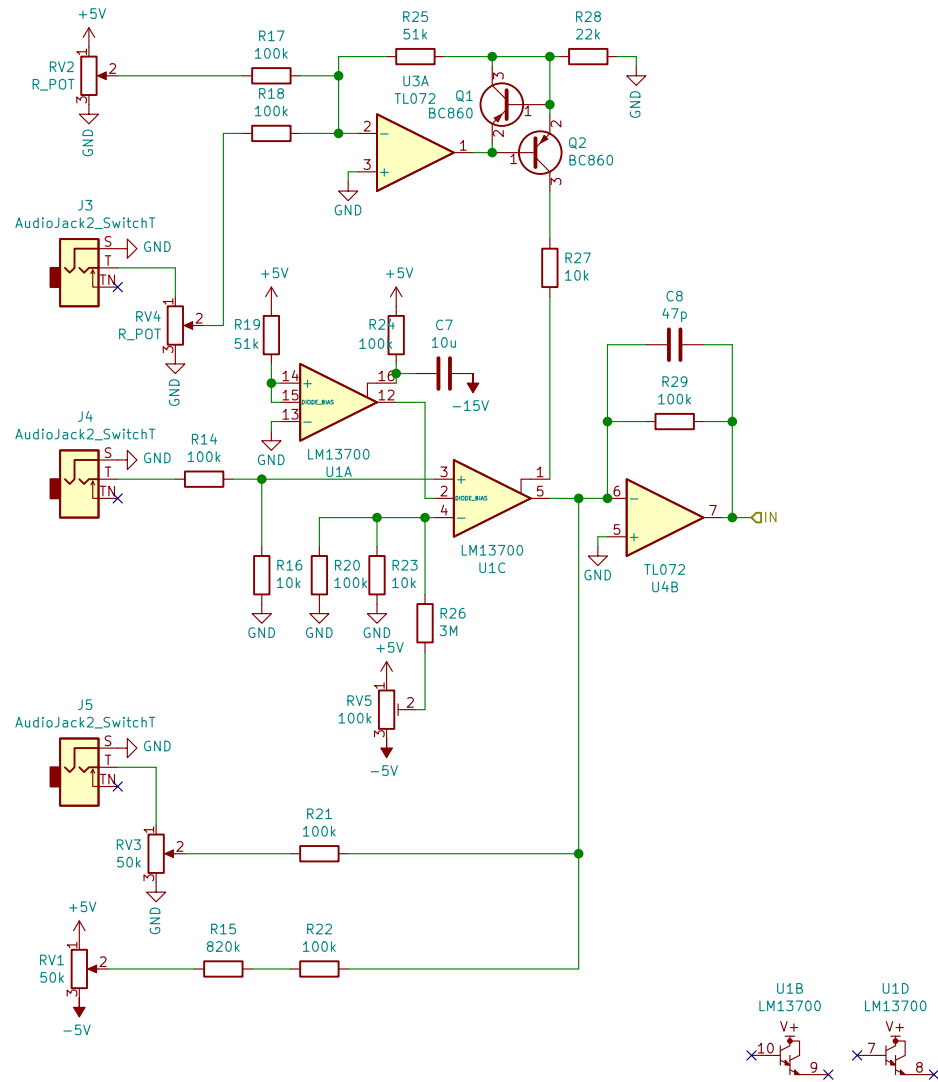
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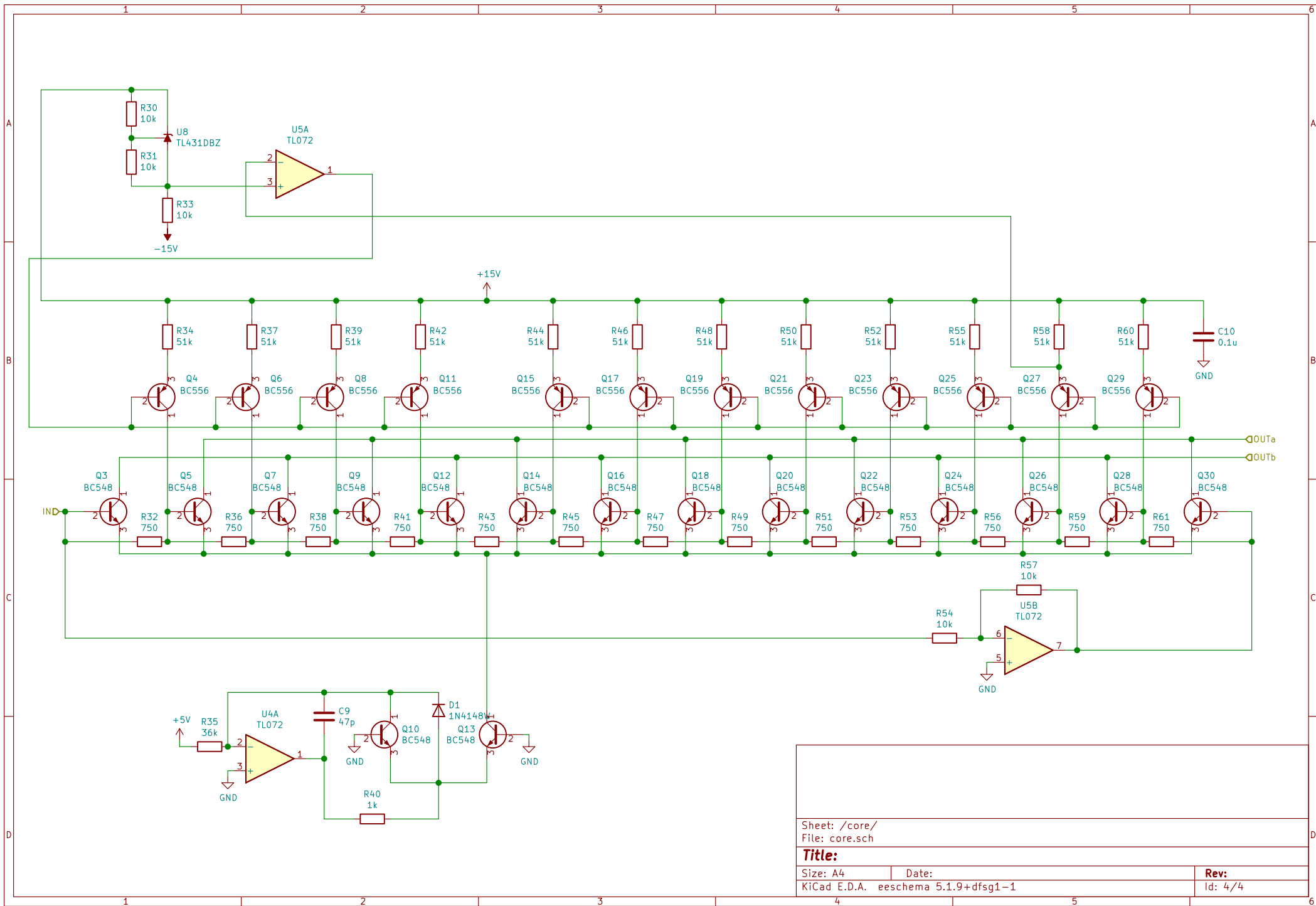
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Id: 2/4



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Size: A4	Date:	Rev:
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Sheet: /core/ File: core.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. eeschema 5.1.9+dfsg1-1		Id: 4/4