

KLS GOGTE INSTITUTE OF TECHNOLOGY, BELAGAVI
DEPT OF COMPUTER SCIENCE AND ENGINEERING
FASTRACK INTERNAL ASSESSMENT TEST-1

SUB: Computer Organization

Sem: III

Date : 24-06-2019

Time : 9am to 10.15 am

Duration : 75 min

Note: Answer any five questions. Each carry 5 marks.

1.	Explain communication between processor and memory unit with diagram.
2.	Calculate the time required to execute the program which consists of 100 lines of code, and each line is executed in a sequence of 7 steps and a clock rate of 5 GHZ.
3.	Explain overflow condition. Check whether overflow occurs or not for the following examples which uses four bits for the representation. a. 15 -11 b. 13 + 14
4.	Explain Bus structure with diagram. Discuss different types of Bus.
5.	Discuss byte addressability. Explain Big endian and Little endian address assignments with example.
6.	What is an addressing mode? Discuss any five addressing modes with an example.
7.	Discuss instruction sequencing with diagram.

Quiz-1

1.	_____ unit of the CPU controls and provides coordination between different parts of computer system .	
2.	ROM stores _____ instructions	
3.	Memory operations are _____ and _____	
4.	_____ is a nonprofitable organization which provides and publishes a suit of programs for computer system performance measurement.	
5.	Performance of a computer is measured using _____ parameter. a. Hardware c. Machine Instruction set b. Compiler d. All of the above	
6.	_____ computers are used for engineering applications a. Desktop b. Mainframe c. Supercomputers d. None	
7.	Add A, B, C is an example of _____ address instruction. a. 1-address b. 2-address c. 3-address d. none	
8.	_____ points to the address of the next instruction to be fetched for execution. a. MAR b. MDR c. IR d. PC	
9.	Memory hierarchy is provided based on _____ a. Size b. Cost c. Accessing time d. All	
10.	Different ways in which the addresses of an operand are specified as part of the instruction is called as _____	

