

Third Semester B.E. Semester End Examination, Dec/Jan 2018-19
ANALOG ELECTRONICS

Time: 3 Hours

Max. Marks: 100

- Instructions:**
1. Each Full Question Carries 20 Marks.
 2. UNIT-II and UNIT-IV are compulsory.
 3. Answer any one full question from each remaining units.

UNIT - I

L CO PO M

- 1 a. Analyse the clipper circuit shown in Fig. Q1(a) and sketch the transfer characteristics. Further sketch the output waveform assuming $V_1 = 3V$ and $V_2 = 5V$ and $V_i = 10\sin\omega t$. Assume ideal diodes.

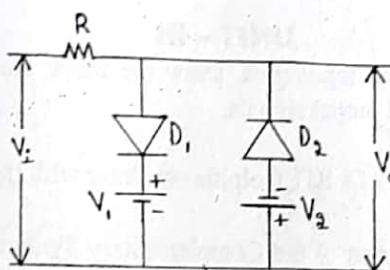


Fig.Q1(a)

(3) (1,2) (1,4) (10)

- b. Explain the following terms as applied to a semiconductor diode:
 i) Transition or Depletion capacitance
 ii) Diffusion capacitance
- (2) (1) (1) (05)
- c. For the emitter bias network shown below in Fig.Q1(c), with $V_{BE} = 0.7V$ and $\beta = 60$, determine
 i) I_B and I_C
 ii) V_{CE}
 iii) V_C , V_E and V_B

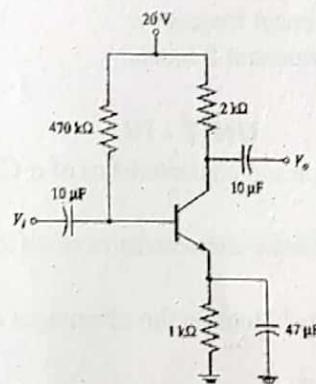


Fig.Q1(c)

(3) (3) (1,4) (05)

OR

- 2 a. For the voltage divider bias circuit, derive an equation for base current (I_B), collector current (I_C) and collector-emitter voltage (V_{CE}) using exact analysis.
- (3) (2,3) (1) (10)
- b. Explain Fixed bias circuit, with the help of Base-Emitter loop and Collector Emitter loop equations.
- (2) (2,3) (1) (05)

Note: L (Level), CO (Course Outcome), PO (Programme Outcome), M (Marks)

- c. For the clammer circuit shown below Fig Q2(c), sketch the output waveform assuming ideal diodes.

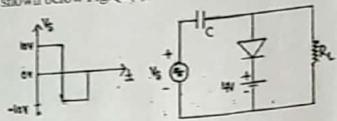


Fig Q2 (c)

(3) (1,2) (1,4) (05)
L CO PO M

UNIT - II

- 3 a. Derive the expressions for Miller Effect input capacitance and output capacitance. (2) (3,4) (1) (10)
b. Develop r_e model of a transistor in common-base configuration. (2) (4) (1) (05)
c. Write a short note on current mirror circuits. (2) (4) (1) (05)
L CO PO M

UNIT - III

- 4 a. List the four feedback topologies. Draw the block diagram of each feedback topology type and indicate the input and output signals. (2) (5) (1) (10)
b. Explain the operation of a BJT Colpitts oscillator with the help of a circuit diagram. (2) (5) (1) (05)
c. Draw the circuit diagram of the Complementary Symmetry Class B Push-pull power amplifier and explain the working principle. (2) (6) (1) (05)

OR

- 5 a. Draw the circuit diagram of series-fed Class A power amplifier. Give the expression for DC power input and AC power output and show that efficiency of the amplifier is 25%. (3) (6) (1) (10)
b. Explain the feedback concept with the help of a block diagram. (2) (5) (1) (05)
c. A crystal has the following parameters:
 $L=0.4H$, $C=0.085pF$, $C_M=1pF$, $R=5k\Omega$
i) Calculate the series resonant frequency.
ii) Calculate the parallel resonant frequency. (3) (5) (1) (05)

UNIT - IV

- 6 a. Explain the basic operation and characteristics of n-Channel Depletion type MOSFET. (2) (2) (1) (10)
b. Sketch and explain the transfer characteristics of n-Channel JFET. (2) (2,3) (1) (05)
c. What is CMOS technology? Mention the advantages of CMOS technology. (2) (2) (1) (05)

UNIT - V

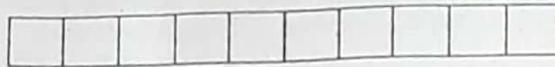
- 7 a. Derive the FET small signal model with necessary equations. (2) (3,4) (1) (10)
b. Draw and explain Enhancement-MOSFET small signal ac equivalent model. (2) (4) (1) (05)
c. Draw the circuit of a JFET common-source amplifier and the ac equivalent using small-signal model. Explain in brief. (3) (4) (1) (05)

Note: L [Level], CO [Course Outcome], PO [Programme Outcome], M [Marks]

Note: L [Level], CO [Course Outcome], PO [Programme Outcome], M [Marks]

OR

- 8 a. For a JFET common-source amplifier draw the small-signal ac model and derive the expressions for input impedance (Z_i) and output impedance (Z_o). (3) (4) (1) (10)
b. List the advantages of FET. (2) (2) (1) (05)
c. Derive the relation between transconductance (gm) and drain current (I_D). (3) (4) (1) (05)



Third Semester B.E. Makeup Examination, January 2019
ANALOG ELECTRONICS

Max. Marks: 100

Time: 3 Hours

- Instructions:**
1. Each full question carries 20 Marks.
 2. UNIT-II and UNIT-IV are compulsory.
 3. Answer any one full question from each remaining unit.

UNIT - I

L CO PO M

1. a. Sketch and explain the working of a parallel biased negative clipper circuit. Draw the transfer characteristics and input-output waveforms. Assume ideal diode. (3) (1) (1,4) (10)
- b. Discuss the DC equivalent circuits of a semiconductor diode under forward and reverse bias conditions. (2) (1) (1) (05)
- c. For the fixed bias circuit shown in Fig.Q1(c), determine
- i) Collector Current (I_C)
 - ii) Collector Resistance (R_C)
 - iii) Base Resistance (R_B)
 - iv) Collector to Emitter voltage (V_{CE})

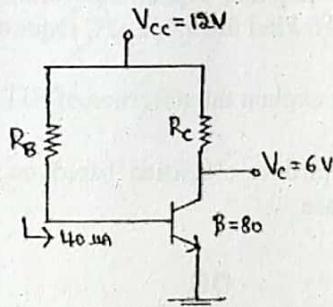
Assume $\beta=80$, $V_{BE}=0.7V$ 

Fig.Q1(c)

(3) (2,3) (1,4) (05)

OR

2. a. For the circuit shown below in Fig.Q2 (a), using silicon transistor and $\beta=80$.
- i) Find the quiescent base current, collector current and V_{CE} .
 - ii) Determine the values of collector, emitter and base voltages with respect to ground.
 - iii) Draw DC load line and locate the Q-point corresponding to $\beta=80$.

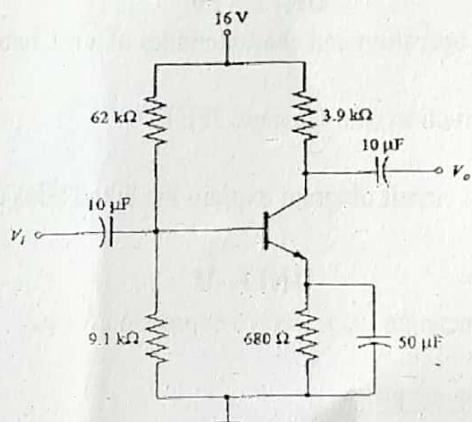


Fig.Q2(a)

(3) (2,3) (1,4) (10)

Note: L (Level), CO (Course Outcome), PO (Programme Outcome), M (Marks)

- b. Explain Emitter bias circuit, with the help of Base-Emitter loop and Collector Emitter loop equations. (2) (2,3) (1) (05)
- c. For the clamping circuit shown below Fig.Q2(c), sketch the output waveform for $V_i = 10\sin\omega_t$. Assume ideal diode.

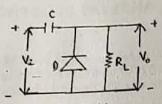


Fig.Q2(c)

(3) (1,2) (1,2) (05)

L CO PO M

UNIT - II

3. a. Starting from a two-port system derive the expressions for the hybrid (h) parameters and sketch the complete hybrid equivalent circuit. (2) (3,4) (1) (10)
- b. What is Darlington connection? Explain in brief the biasing arrangement used for the Darlington connection. (2) (2,3) (1) (05)
- c. Sketch the frequency response of a RC coupled amplifier and explain the nature of the waveform in the different frequency regions. (2) (3) (1) (05)

UNIT - III

4. a. With the help of a circuit diagram, explain the working of Hartley oscillator. In a transistor Hartley oscillator, $L_1 = L_2 = 10\mu H$. Find the value of C required for an oscillating frequency of 150KHz. (3) (5) (1) (10)
- b. With neat circuit diagram explain the operation of BJT RC phase shift Oscillator. (2) (5) (1) (05)
- c. How are the power amplifiers classified based on the location of Q-point? Also indicate the operating cycle in each case. (2) (6) (1) (05)

OR

5. a. Draw the circuit diagram and explain the operation of Class B push pull power amplifier. Also show that maximum conversion efficiency of class B push pull amplifier is 78.5%. (3) (6) (1) (10)
- b. For a Transformer-coupled Class A power amplifier draw the DC and AC load line and explain. (2) (6) (1) (05)

- c. Explain Barkhausen criteria for sustained oscillations. (2) (5) (1) (05)

UNIT - IV

6. a. Explain the basic operation and characteristics of a n-Channel depletion-type MOSFET. (2) (2) (1) (10)
- b. Explain the construction of n-Channel JFET. (2) (2) (1) (05)
- c. With the help of a circuit diagram explain the Fixed-Bias configuration of JFET. (2) (2,3) (1) (05)

UNIT - V

7. a. Define transconductance (g_m). Derive expression for g_m . (3) (4) (1) (10)
- b. Compare BJT with FET. (2) (2) (1) (05)

Note: L (Level), CO (Course Outcome), PO (Programme Outcome), M (Marks)

Note: L (Level), CO (Course Outcome), PO (Programme Outcome), M (Marks)

- c. Draw small signal FET model and explain in brief. (2) (3,4) (1) (05)

OR

8. a. For a JFET common-source amplifier draw the small-signal ac model and derive the expressions for voltage gain (A_v). (3) (4) (1) (10)
- b. Draw and explain Depletion-MOSFET small signal ac equivalent model. (2) (4) (1) (05)
- c. With the help of transfer characteristics, obtain the expression for transconductance (g_m) using graphical method. (3) (4) (1) (05)

Instructions: 1. Questions in UNIT I and UNIT II are compulsory.
2. Solve any one question each from UNIT III to V

UNIT - I

- 1 a. Draw and Explain working of a series negative clipper. (Level [2], CO [1], PO [1]) 06 M
 b. Explain the operation of positive clamper. (Level [2], CO [1], PO [1]) 06 M
 c. Determine the Q point and S for a voltage divider bias circuit with $R_{B1} = 70\text{ k}\Omega$, $R_{B2} = 10\text{ k}\Omega$, $R_C = 10\text{ k}\Omega$, $R_E = 1\text{ k}\Omega$. with $V_{CC} = 15\text{ V}$, using approximate analysis method. (Level [3], CO [1], PO [4]) 08 M

UNIT - II

- 2 a. Using r_e model, derive an expression for voltage gain, input and output impedances for voltage divider bias amplifier with emitter bypass capacitor. (Level [2], CO [2], PO [1]) 06 M
 b. Determine voltage gain, input and output impedances of a fixed bias amplifier with $h_{ie} = 1.2\text{ k}\Omega$, $h_{fe} = 200$, $h_{oe} = 10\mu\text{S}$, $R_B = 1\text{ M}\Omega$, $R_C = 10\text{ k}\Omega$. (Level [3], CO [2], PO [4]) 06 M
 c. Derive an equation for Miller input and output capacitance. (Level [2], CO [2], PO [4]) 08 M

UNIT - III

- 3 a. Calculate gain, input and output impedances of voltage series negative feedback amplifier having $A = 1000$, $Z_i = 1.5\text{k}\Omega$ and $Z_o = 50\text{ k}\Omega$ for feedback factor $\beta = 0.01$. (Level [3], CO [5], PO [4]) 06 M
 b. With neat diagram, explain BJT Colpitts oscillator. Calculate the operating frequency if $C_1 = 0.001\mu\text{F}$ and $C_2 = 0.01\mu\text{F}$ and $L = 10\mu\text{H}$. (Level [3], CO [5], PO [4]) 06 M
 c. Derive an expression for gain, input impedance and output impedance of voltage series negative feedback. Mention the advantages and disadvantages of negative feedback. (Level [2], CO [5], PO [4]) 08 M

OR

- 4 a. Explain the classification of power amplifiers. Also comment on the power dissipation in the transistor. (Level [2], CO [6], PO [1]) 06 M
 b. Explain with neat circuit diagram, Class A power amplifier and derive the expression for efficiency. (Level [2], CO [6], PO [1]) 06 M
 c. Explain with neat circuit diagram, Class B complementary symmetry power amplifier and derive the expression for efficiency. (Level [2], CO [6], PO [1]) 08 M

UNIT - IV

- 5 a. Explain the working of FET and draw the characteristics. (Level [2], CO [4], PO [1]) 06 M
 b. Determine the value of g_{in} for a JFET ($I_{DSS} = 8\text{mA}$ and $V_p = -6\text{V}$) when biased at $V_{GS} = V_p/4$. (Level [3], CO [3], PO [4]) 06 M
 c. For a self biased JFET, calculate R_D and R_S with $V_{DD} = 20\text{ V}$, $I_{DSS} = 10\text{mA}$, $V_{GS} = -3\text{V}$ at $I_D = 4\text{mA}$. Assume the Q point is at the center of the operating region. (Level [3], CO [3], PO [4]) 08 M

- OR**
6. a. Explain the working principle of n-channel JFET and draw drain and transfer characteristics. (Level [2], CO [3], PO [1]) 10 M
- b. Explain the working principle of n-channel depletion type MOSFET and draw drain and transfer characteristics. (Level [2], CO [3], PO [1]) 10 M
- UNIT - V** 06 M
7. a. Draw the JFET small signal model for given $g_s = 2.8 \text{ mS}$ and $g_{os} = 25 \mu\text{S}$. (Level [3], CO [5], PO [4]) 06 M
- b. The fixed-bias configuration with an operating point defined by $V_{GSQ} = -2.2 \text{ V}$ and $I_{DQ} = 0.6 \text{ mA}$, with $I_{DS} = 10 \text{ mA}$ and $V_D = -8 \text{ V}$. The value of y_{os} is provided as $30 \mu\text{S}$.
- a. Determine g_m .
 - b. Find r_e .
 - c. Determine Z_i .
 - d. Calculate Z_o .
 - e. Determine the voltage gain A_v .
 - f. Determine A_v ignoring the effects of r_d .
- (Level [3], CO [5], PO [4]) 08 M
- c. Determine Z_i , Z_o , and A_v for JFET self bias configuration. (Level [2], CO [5], PO [4]) 08 M
- OR** 06 M
8. a. Differentiate between BJT and FET. (Level [2], CO [4], PO [1]) 06 M
- b. Draw the small signal equivalent circuit of a common gate FET amplifier. Derive expressions for input impedance, output impedance and voltage gain. (Level [2], CO [4], PO [1]) 08 M
- c. Draw the small signal equivalent circuit of a common drain FET amplifier. Derive expressions for input impedance, output impedance and voltage gain. (Level [2], CO [4], PO [1]) 08 M

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16EC32/EC35

Third Semester B.E. Semester End Examination, Dec/Jan 2017-18

ANALOG ELECTRONICS

Time: 3 Hours

Max. Marks: 100

Instructions: 1. Questions in UNIT I and UNIT II are compulsory.
2. Solve any one question each from remaining Units

UNIT - I

- a. Draw and explain the diode equivalent circuit. (Level [1], CO [1], PO [1]) 05 M

- b. Explain the operation of positive clammer. Determine V_o for the network of Fig. 1 for the input indicated.

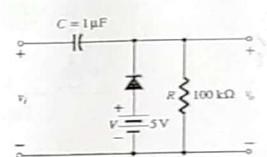
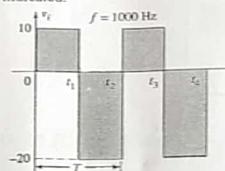


Fig.1

(Level [2], CO [1], PO [1])

- c. Derive the expression for stability factor (S) for fixed bias configuration. (Level [2], CO [1], PO [1]) 05 M

(Level [2], CO [1], PO [1])

UNIT - II

- a. Draw r_e and h-parameter models of a transistor in CE mode. Give relation between r_e and h-parameters. (Level [2], CO [2], PO [1]) 05 M

- b. Using hybrid model, derive equations for voltage gain, input and output impedances of approximate voltage divider network with emitter bypass capacitor. (Level [2], CO [2], PO [1]) 07 M

- c. Derive an equation for Miller input and output capacitance. (Level [2], CO [2], PO [1]) 08 M

UNIT - III

- a. Derive an expression for gain, input impedance and output impedance of voltage shunt negative feedback. Mention the advantages and disadvantages of negative feedback. (Level [2], CO [5], PO [1]) 10 M

(Level [2], CO [5], PO [1])

- b. Calculate gain, input and output impedances of voltage series feedback amplifier having $A = -500$, $Z_i = 1.5 \text{ k}\Omega$ and $Z_o = 50 \text{ k}\Omega$ for feedback factor $\beta = -1/10$. (Level [3], CO [5], PO [4]) 05 M

(Level [3], CO [5], PO [4])

- c. With neat diagram, explain BJT Hartley oscillator. Calculate the operating frequency of BJT RC phase shift oscillator for $R = 3 \text{ k}\Omega$, $C = 1500 \text{ pF}$ and $R_C = 10 \text{ k}\Omega$. (Level [2], CO [5], PO [4]) 05 M

(Level [2], CO [5], PO [4])

OR

- a. For distortion readings of $D_2 = 0.15$, $D_3 = 0.01$, and $D_4 = 0.05$, calculate the total harmonic distortion. (Level [2], CO [6], PO [1]) 05 M

(Level [2], CO [6], PO [1])

- b. Explain with neat circuit diagram, Class A transformer coupled power amplifier and derive the expression for efficiency. (Level [2], CO [6], PO [1]) 07 M

(Level [2], CO [6], PO [1])

- c. Explain with neat circuit diagram, Class B push-pull power amplifier and derive the expression for efficiency. (Level [2], CO [6], PO [1]) 08 M

(Level [2], CO [6], PO [1])

UNIT - IV

- 5 a. Draw and explain CMOS inverter with neat sketch. (Level [1], CO [4], PO [1])
 b. Determine the value of g_m for a JFET ($I_{DSS} = 8 \text{ mA}$ and $V_p = -6 \text{ V}$) when biased at $V_{GS} = V_p/4$. (Level [3], CO [3], PO [4])
 c. For the fixed-bias configuration of Fig.2.
 a. Sketch the transfer characteristics of the device.
 b. Determine I_{DQ} and V_{DSQ} .

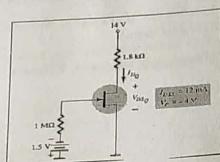


Fig.2

(Level [3], CO [3], PO [4])

OR

- 6 a. Explain the working principle of n-channel JFET and draw drain and transfer characteristics. (Level [2], CO [3], PO [1])
 b. Explain the working principle of n-channel depletion type MOSFET and draw drain and transfer characteristics. (Level [2], CO [3], PO [1])

UNIT - V

- 7 a. Draw the JFET small signal model for given $g_s = 3.8 \text{ mS}$ and $g_{os} = 20 \mu\text{S}$. (Level [3], CO [5], PO [4])
 b. Determine Z_i , Z_o , and A_v for JFET fixed bias configuration. (Level [2], CO [5], PO [4])
 c. The fixed-bias configuration with an operating point defined by $V_{GSQ} = -2 \text{ V}$ and $I_{DQ} = 5.625 \text{ mA}$, with $I_{DSS} = 10 \text{ mA}$ and $V_p = -8 \text{ V}$. The network is as shown in Fig. 3. The value of $y_{os} = 40 \mu\text{S}$.

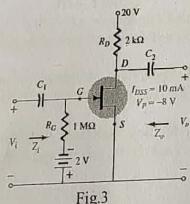


Fig.3

Find the following:

- a. Determine g_m .
 b. Find r_d .
 c. Determine Z_i .
 d. Calculate Z_o .
 e. Determine the voltage gain A_v .
 f. Determine A_v ignoring the effects of r_d .
 (Level [2], CO [5], PO [4])

OR

- 8 a. Give the difference between BJT and FET. (Level [1], CO [4], PO [1])
 b. Draw the small signal equivalent circuit of a common gate FET amplifier. Derive expressions for input impedance, output impedance and voltage gain. (Level [2], CO [4], PO [1])
 c. Draw the small signal equivalent circuit of a common drain FET amplifier. Derive expressions for input impedance, output impedance and voltage gain. (Level [2], CO [4], PO [1])

05 M

07 M

08 M

Third Semester B.E. Semester End Examination, Nov. / Dec. 2016-17
ANALOG ELECTRONIC CIRCUITS

Time: 3 Hours

Max. Marks: 100

- Instructions:** 1. Answer five full questions
2. Unit I and Unit II compulsory
3. Answer any one question from each of the remaining units

UNIT - I

UNIT - II

- UNIT - II**

2 a. Draw and Explain h-parameter and r-parameter model for Common Emitter configuration amplifier (Level[L2], CO[2], PO[1,9]) 10 M

b. Design a Common Emitter Amplifier for a voltage gain of 15, handling a maximum output signal swing of 15 V with a stability factor of 20 with lowest frequency of interest of 250 Hz. Calculate the input and output impedance of the designed amplifier. Assume $\beta=100$, $h_{ie} = 1 \text{ k}\Omega$. (Level[L3], CO[3], PO[1,9]) 10 M

UNIT - III

OR

- OR**

4 a. Calculate the operating point for a self biased JFET having the supply voltage of $V_{DD}=20V$, maximum value of drain current $I_{DSS} = 10 \text{ mA}$ and $V_{GS} = -3V$ at $I_D = 4\text{mA}$. Also determine values of R_S and R_D (Level[L3], CO[3], PO[1,9]) 10 M

b. Explain the construction and working of different modes of operation in MOSFET with relevant characteristics plots. (Level[L2], CO[3], PO[1,9]) 10 M

UNIT - IV

- UNIT - IV**

5 a. Draw the small signal equivalent circuit of a Common Source FET Amplifier and derive expressions of Gain, Input Impedance and Output Impedance (Level[L2], CO[4], PO[1,9]) **10 M**

b. Draw the small signal equivalent circuit of a Common Gate FET Amplifier and derive expressions of Gain, Input Impedance and Output Impedance (Level[L2], CO[4], PO[1,9]) **10 M**

OR

- 6 a. Draw the frequency response for a RC Coupled Amplifier and discuss the different regions of operation. Also define the term bandwidth. (Level[L2],CO[4],PO[1,9])

- b. Derive an equation for the Miller input and output capacitance.
 (Level [L2], CO[4], PO[1,9])

UNIT - V

- 7 a. Explain with a block diagram voltage shunt feedback system and derive expressions for Gain, Input Impedance and Output Impedance
 (Level [L2], CO[5], PO[1,9])
 b. Discuss Barkhausen criterion for oscillation. Explain the working of a phase shift oscillator.
 (Level [L2], CO[5], PO[1,9])

- 8 a. Explain working of a Series Fed Class A Power Amplifier and derive expression for the power efficiency
 (Level [L2], CO[6], PO[1,9])
 b. Explain working of a Class B Complementary Symmetry Push-Pull Power Amplifier and derive expression for the power efficiency
 (Level [L2], CO[6], PO[1,9])

OR

10 M SN

16EC33

Third Semester B.E. Semester End Examination, Dec/Jan 2018-19
DIGITAL ELECTRONICS

Max. Marks: 100

- Instructions: 1. Units III and V are compulsory
 2. Answer any one full question from each of the units I, II, and IV

UNIT - I

- a. Translate the Boolean function $f(a,b,c) = a(b+c') + c$ into minterms and maxterms.
 (2) (1) (1) (06)
 b. Simplify $f(a,b,c,d) = \Sigma m(0,3,4,7,8,10,12,14) + d\bar{c}(2,6)$ by applying K map method to obtain minimal SOP equation and implement using only NOR gates.
 (3) (1) (1) (08)
 c. Design a logic circuit that has 4 inputs, the output will be high, when the majority of the inputs are low, use K map to simplify.
 (3) (1) (1) (06)

OR

- a. Apply Quine McCluskey method to the given Boolean function $f(a,b,c,d) = \Sigma m(1,2,3,5,6,7,8,9,12,13,15)$ and obtain prime implicants, essential prime implicants and minimal SOP equation.
 (3) (1) (1) (12)
 b. Apply MEV technique to the Boolean function $f(a,b,c,d) = \Sigma m(2,3,4,5,13,15) + d\bar{c}(8,9,10,11)$ to obtain minimal SOP equation taking least significant bit as MEV.
 (3) (1) (1) (08)

UNIT - II

- 3 a. Explain the concept of BCD addition and hence design the logic circuit using 7483 ICs and suitable gates that adds two single digit BCD and produces BCD sum and carry.
 (2) (2) (1) (10)
 b. In certain application, four inputs A,B,C,D are fed to the logic circuit, producing an output F which operates a relay. The relay turns ON when F=1 for the inputs (ABCD): 0000, 0010, 0101, 0110, 1101 and 1110. Design an optimal logic circuit using two 74138 ICs and suitable gate.
 (3) (2) (1,3) (10)

OR

- 4 a. What is the need for look-ahead adder? Explain.
 (2) (2) (1) (05)
 b. Write a note on priority encoder.
 (1) (2) (1) (05)
 c. Design an optimal logic circuit using a 4x1 MUX that should produce logic-1 output when it detects illegal single digit BCD code. Use most significant variables as select inputs for the MUX.
 (3) (2) (1,3) (10)

UNIT - III

- 5 a. Implement a gated SR latch and write the truth table. Explain its disadvantage.
 (2) (3) (1) (07)
 b. Explain the working of a master slave JK Flip flop.
 (2) (3) (1) (07)
 c. In an SR latch, output Q is connected to R input while output Q' is connected to S input. Analyze the circuit and draw the timing diagram with respect to clock and identify the functionality of the circuit. Assume SR latch to be cleared initially.
 (2) (3) (1) (07)

Note: L (Level), CO (Course Outcome), PO (Programme Outcome), M (Marks)

UNIT - IV

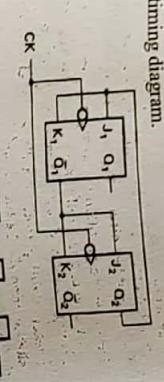
L CO
M PO

- a. Explain, with logic diagram, the working of a parallel-in unidirectional shift register.

- b. Design a sequential circuit to count the sequence $3 \rightarrow 5 \rightarrow 6 \rightarrow 9 \rightarrow 10 \rightarrow 12$ and repeat. Use POSITIVE edge triggered D flip-flops.

- 7 a. Analyze the operation of a 4-bit ripple counter with the help of logic diagram and timing diagram. List its advantages and limitations.

- b. Analyze, and complete the timing diagram.



- c. Design a mod-6 counter (block diagram only) that counts non-BCD values from 1010 to 1111.

- repeat, using IC 74193 (IC74193: 4-bit synchronous binary up/down counter).

UNIT - V

L CO
M PO

- 8 a. Compare Mealy and Moore models of sequential machines

(2) (5) (1)

- b. Find the equivalence partition for the sequential machine defined by the following state table.

Show the standard form of the corresponding reduced machine.

Present State	Next State, z	
	x=0	x=1
A	B,1	H,1
B	F,1	D,1
C	D,0	E,1
D	C,0	F,1
E	D,1	C,1
F	C,1	D,1
G	C,1	A,1
H	C,0	A,1

(3) (4) (1)

- c. Design a mod-6 counter (block diagram only) that counts non-BCD values from 1010 to 1111.

- repeat, using IC 74193 (IC74193: 4-bit synchronous binary up/down counter).

OR

L CO
M PO

- a. Apply Quine-McClusky method to simplify the following Boolean function for optimality and realize using minimum universal NAND gates only. $F(w,x,y,z) = \Sigma(1,3,4,5,7,8,9,11,15)$

b. Apply VEM method and simplify the Boolean function $f(w,x,y,z) = \overline{\Sigma}(1,4,5,6,14)$

L CO
M PO

- a. Design & implement a 4 bit carry look ahead adder and estimate its propagation delay.

(3) (1) (10)

- b. Design and implement a Full adder using IC 74139(2:4 line decoder).

(3) (2) (12)

OR

L CO
M PO

- a. Design and implement 16:1 Multiplexer using 4:1 Multiplexers.

(3) (2) (1,12) (05)

- b. Design & implement 2 bit digital magnitude comparator.

(3) (2) (1) (07)

- c. Design & implement 4:2 line Priority Encoder with highest priority assigned to higher order bit. The circuit shall have a validity indicator which should be 1 if the inputs are valid otherwise 0.

(3) (2) (1) (08)

UNIT - III

L CO
M PO

- a. What is the limitation of basic bi-stable element? Explain SR latch with logic diagram and function table.

(2) (3) (1) (1)

- b. Explain, with logic diagram and function table, the asynchronous inputs PRESET and CLEAR edge triggered D flip-flop.

(2) (3) (1)

Third Semester B.E. Makeup Examination, January 2019

DIGITAL ELECTRONICS

Max. Marks: 100

Instructions: 1. Units III and V are compulsory
2. Answer any one full question from remaining Units

UNIT - I

L CO
M PO

- a. List the steps in converting verbal description of the problem statement into truth table and hence convert the following problem statement into truth table:
An electric motor powering a conveyor used to move material is to be turned ON when atleast one interlock switch is in position, if the material to be moved is present, and if the protective switch not open as logic-1. The motor will be ON when circuit outputs logic-1. Write both the Canonical expressions in m-notation.

- b. A problem statement resulted into the following Boolean function. Simplify using map method for optimality and realize using minimum universal NOR gates only.
 $F(a,b,c,d) = \overline{\Sigma}(0,6,8,13,14,15) + \overline{dc}(4,5,10,12)$

(3) (1) (1) (10)

- c. Apply Quine-McClusky method to simplify the following Boolean function for optimality and realize using minimum universal NAND gates only. $F(w,x,y,z) = \Sigma(1,3,4,5,7,8,9,11,15)$

(3) (0) (1) (10)

- b. Apply VEM method and simplify the Boolean function $f(w,x,y,z) = \overline{\Sigma}(1,4,5,6,14)$

(3) (1) (1) (10)

UNIT - II

L CO
M PO

- a. Design & implement a 4 bit carry look ahead adder and estimate its propagation delay.

(3) (2) (1) (12)

- b. Design and implement a Full adder using IC 74139(2:4 line decoder).

(3) (2) (1,12) (08)

OR

L CO
M PO

- a. Design and implement 16:1 Multiplexer using 4:1 Multiplexers.

(3) (2) (1,12) (05)

- b. Design & implement 2 bit digital magnitude comparator.

(3) (2) (1) (07)

- c. Design & implement 4:2 line Priority Encoder with highest priority assigned to higher order bit. The circuit shall have a validity indicator which should be 1 if the inputs are valid otherwise 0.

(3) (2) (1) (08)

UNIT - III

L CO
M PO

- a. What is the limitation of basic bi-stable element? Explain SR latch with logic diagram and function table.

(2) (3) (1) (1)

- b. Explain, with logic diagram and function table, the asynchronous inputs PRESET and CLEAR edge triggered D flip-flop.

(2) (3) (1)

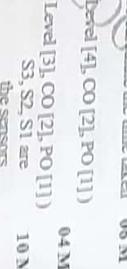
- b. Design a synchronous mod-5 counter using JK flip-flops that counts the sequence 0→2→3→6→5→1 and repeat. (Level [3], CO [4], PO [1])
- UNIT - V** (Level [3], CO [5], PO [1]) 0.4 M
- a. Explain K-distinguishable and K-equivalent states.
- b. Derive the reduced (minimal) machine and express in standard form for a completely specified sequential machine defined by the following state table.

Present State	Next State, Z
X=0	Z=1
E,0	G,0
A,0	A,0
B,0	G,0
C,0	A,0
D,0	B,0
E,1	D,0
F,0	E,0
G,0	D,0

(Level [3], CO [5], PO [1])

- UNIT - II** Time: 3 Hours Max. Marks: 100
- Instructions:** 1. Unit-I and Unit-V are compulsory.
2. Answer any one full question from each of the units II, III, and IV
- UNIT - I**
1. a. Applying VEM method, simplify the Boolean expression $f(w,x,y,z) = \Sigma m(0,1,2,3,5,7,8,9,10,11,13,14,15)$ and realize using NAND gates. (Level [3], CO [1], PO [1]) 10 M
- b. Apply QM method and obtain the minimal SOP expression for the Boolean function $f(a,b,c,d) = \Sigma m(0,2,8,10,12,14,15) + \text{dc}(1,7,9)$ (Level [3], CO [1], PO [1]) 10 M
- UNIT - III**
2. a. Draw the block-diagram of a n-bit ripple adder. Analyze and hence calculate the time taken by the n-bit ripple adder to produce the output.
- b. Design a 8x1 multiplexer using two 4x1 multiplexers. (Level [4], CO [2], PO [1]) 0.6 M
- UNIT - IV**
- c. A conveyor system brings raw material in from three different sources. The three sources converge in to a single output conveyor. Sensors mounted adjacent to each source conveyor indicate the presence of raw material. All four conveyors have separate motors so they can be individually controlled. Each source conveyor can have different speed. The output product rate is fixed; it can be only turned on or off. The output product flow rate must match the source flow rates. To accomplish this, the following conditions must be met.
- If source 1 has the product, then sources 2 and 3 must be turned off; if source 1 is empty, then either 2 or 3 or both can be turned on. In the event that no product is available from the sources, the output conveyor must be turned off. If no product is available, the respective source conveyor must be turned off.
- Construct the truth table and design the logic circuit to control the motors using suitable decoder IC and gates.
- OR** (Level [6], CO [2], PO [1,4,12])
- UNIT - V** 16EC33
3. a. Design a 4-bit look-ahead adder and estimate its propagation delay. (Level [3], CO [2], PO [1]) 12 M
- b. Two motors M_2 and M_1 are controlled by three sensors S_1, S_2, S_3 . One motor, M_2 is to run any time all three sensors are ON. The other motor, M_1 , is to run whenever sensors S_2 or S_3 , but not both, are ON and S_1 is OFF. For all sensor combinations where M_1 is ON, M_2 is to be OFF, except when all three sensors are OFF then both motors must remain OFF. Design the logic network for motors control using IC 741S3 and suitable gates. (Level [6], CO [2], PO [1,4,12])

KLS GOYTE INSTITUTE OF TECHNOLOGY, BELAGAVI



M1,M2,M3,M4
are the motors

Third Semester B.E. Semester End Examination, Nov.

c. 2016-17

UNIT-IV

- a. Distinguish between asynchronous and synchronous counters (Level flip-flops). Explain how to count the numbers 10, 11, 12, 13, 14, 15 using a synchronous counter.

b. Design a synchronous counter using positive edge-triggered T flip-flops to count the numbers 10, 11, 12, 13, 14, 15 and repeat.

c. Implement the logic function $f(w,x,y,z) = \sum m(0,2,4,5,8,13,15) + \sum d(1,10,14)$ using only NOR gates.

n the

- c. Design a decade counter using 4-bit ring counter
 OR
 Draw the block diagram of a 4-bit ring counter and explain its working with a counting sequence (Level [6], CO[7], CS[5,4,3,2], Q[15,14,13,12,11,10,9,8,7,6,5,4,3,2,1,0])
 OR
 function f(w,x,y,z) = $\Sigma(4,7,9,11,12,13,14,15)$
 OR
 J01LQ (Level [1,3], CO[2], PO[1])

Other

- b. Design a universal shift register using multiplexers and D flip-flops, and explain its operation.

(Level [3], CO [4], PO [1])

b. Apply Karnaugh map method and obtain the minimal SOP expression for the Boolean function $f(w,x,y,z) = \sum m(1,3,8,9,10,11,12,14,15)$.

(Level [3], CO[5], PO[1])

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- explain.

sequential r

- Explain Mealy and Moore machines. (Level-12, CSE-375)
Derive the reduced machine for a sequential machine defined by the following state table

olear

- b. Use 8x1 MUX and implement the Boolean function
 $f(w,x,y,z) = \sum m(1,2,4,6,7,9,11,12,15)$

25

- c. What is the limitation of ripple adder? Estimate the overall delay of an n-bit ripple adder. 08 M
Mention the remedy. Mention the overall delay of remedial circuit?

INT - III

- (Level [3], CO [5], PO [1])

4 a. Explain, with logic diagram (using gates) and truth table, the operation of MSJK flip-flop.

Present State		Next State, z
x=0		x=1
A	E,0	D,1
B	F,0	D,0
C	E,0	B,1
D	F,0	B,0
E	C,0	F,1
F	B,0	C,0

(Level [3], c0 [5], p0 [1])

- b. Derive the characteristic equation of JK and T flip-flops. (Level[1,2], CO[1], PO[1]) 08 M

c. What is the function of register? Explain the classification of registers. (Level[1,3], CO[1], PO[1]) 04 M

- 5 a. Complete the logic diagram shown in Fig.6(a) for a NAND latch

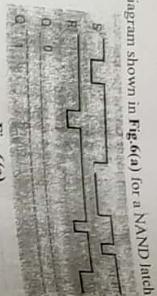


Fig. 6(a)

- b. Explain race-around condition that occurs in 1K flip-flop. Mention the remedies.

- c. Explain the operation of universal shift register with neat diagram and mode controllable

UNIT - IV

- 6 a. Compare ripple and synchronous counters (Obtaining at least five different points).
- b. Explain Johnson counter.
- c. Design a synchronous mod-6 counter using T flip-flops that counts sequence $0 \rightarrow 1 \rightarrow 3 \rightarrow 6 \rightarrow 7 \rightarrow 4$ and repeat.

UNIT - V

- 7 a. Draw the circuits of CMOS inverter, CMOS N AND and CMOS NOR gates.
- (Level[L1], CO[1], PO[1])
(Level[L2], CO[1], PO[1])
(Level[L3], CO[1], PO[1])
- b. Explain, briefly, the terms fan-in, fan-out, noise margin and propagation delay.
- (Level[L1], CO[1], PO[1])
(Level[L2], CO[1], PO[1])
(Level[L3], CO[1], PO[1])

- c. Find the system properties namely, memory, stability, causality, linearity, and time invariance for the following systems.
i) $n x(n)$ ii) $e^{x(t)}$
- Realize $g(t)$ and $\frac{d}{dt} g(3t)$.



Figure Q1 (a)

L

CO

PO

M

1

0

-1

-2

1

0

5

4

3

2

1

0

-1

-2

3

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0.4 M

ISN []

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Third Semester B.E. Semester End Examination, Dec/Jan 2018-19

SIGNALS AND SYSTEMS

Max. Marks: 160

Instructions: 1. Unit-I and Unit-IV are compulsory.

2. Answer any one full Question from remaining units.

UNIT - I

- a. Determine the odd and even parts of the signal $x(t)$ shown in figure Q1(a).

L

CO

PO

M

1

0

-1

-2

1

0

-1

-2

3

2

1

0

-1

-2

4

3

2

1

0

-1

-2

5

4

3

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-2

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Third Semester B.E. Makeup Examination, January 2019						Max. Marks: 100
	SN	Time: 3 Hours	Instructions:	L CO PO	UNIT - I	UNIT - II
5	a.	Check for stability, causality and memory of the LTI system impulse responses. i) $h(n) = n \left(\frac{1}{2}\right)^n u(n)$ ii) $h(t) = e^{-t} u(t+1)$	(2)	(3)	(1,2)	(2)
b.	Find the total response of $\frac{d^2y(t)}{dt^2} + 3 \frac{dy(t)}{dt} + 2y(t) = 4e^{-3t}$	(3)	(3)	(1,2)	(2)	(3)
c.	Realize direct form I and II for $0.77y_{II}(t) - 3y_I(t) + y(t) = x(t) + 2x'(t)$	(3)	(3)	(1,2)	(2)	(3)
y(0) = 3 y'(0) = 4						
OR						
a.	List properties of ROC with reference to Z-transforms.					
b.	Determine Z-transforms of following i) $x(n) = \left(\frac{1}{2}\right)^n u(-n)$ ii) $x(n) = \cos(\omega_0 n) u(n)$	(3)	(4)	(1,2)	(2)	(3)
c.	A causal LTI system is described by $y(n) - y'(n) + \frac{5}{6}y(n-1) + \frac{1}{6}y(n-2) = x(n)$, determine i) system function $H(z)$ and ii) impulse response.	(3)	(4)	(1,2)	(2)	(3)
UNIT - V						
State and prove time convolution and frequency convolution properties of Fourier transform.						
	(1)	(5)	(1,2)	(1,2)	(2)	(1)
	(2)	(1)	(1)	(1)	(2)	(1)
	(3)	(5)	(1,2)	(1,2)	(2)	(1)
	(4)	(1)	(1)	(1)	(2)	(1)
	(5)	(1)	(1)	(1)	(2)	(1)
UNIT - VI						
Find the inverse Fourier transform of i) $X(j\omega) = \frac{3 - \frac{1}{2}e^{-j\omega}}{\frac{1}{16}e^{-j2\omega} + 1}$ ii) $X(j\omega) = \frac{6}{e^{-j2\omega} - 5e^{-j\omega} + 6}$						
OR						
State and prove time shifting and time scaling properties of Fourier transform.						
	(1)	(5)	(1,2)	(1,2)	(2)	(1)
	(2)	(1)	(1)	(1)	(2)	(1)
	(3)	(5)	(1,2)	(1,2)	(2)	(1)
	(4)	(1)	(1)	(1)	(2)	(1)
	(5)	(1)	(1)	(1)	(2)	(1)
UNIT - VII						
Find the Fourier transform of $f(t) = e^{-at} \cos \omega t$ using the convolution property.						
	(3)	(5)	(1,2)	(1,2)	(2)	(1)
	(4)	(1)	(1)	(1)	(2)	(1)
	(5)	(1)	(1)	(1)	(2)	(1)
Find the inverse Fourier transform of $X(j\omega) = \frac{j\omega}{(2+j\omega)^2}$ and the inverse Fourier transform of $X(j\omega) = \frac{1}{(1-j\omega)^2}$						
	(2)	(5)	(1,2)	(1,2)	(2)	(1)
	(3)	(5)	(1,2)	(1,2)	(2)	(1)
	(4)	(1)	(1)	(1)	(2)	(1)
	(5)	(1)	(1)	(1)	(2)	(1)
UNIT - III						
List properties of impulse response representation of LTI systems. Assess i) Causality ii) Memory and iii) Stability of the LTI systems using impulse response.						
b.	Ascertain i) Causality, ii) Stability of a LTI system with $h(t) = e^{-4 t }$	(3)	(3)	(4)	(3)	(4)
c.	Draw the direct form-I and II implementation of the system represented by differential equation $\frac{d^2y(t)}{dt^2} + 5 \frac{dy(t)}{dt} + 4y(t) = \frac{dx(t)}{dt}$	(3)	(3)	(4)	(3)	(4)

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- 5 a. A system is modeled by the linear differential equation
 $0.77 \frac{d^3y(t)}{dt^3} - 3 \frac{dy(t)}{dt} + y(t) = x(t) + 2 \frac{dx(t)}{dt}$. Draw the block diagram for the system in direct forms I and II.

- b. Determine the overall response of the system shown in fig.5b, given
 $H_1(n) = (1/3)^n u(n)$ $H_2(n) = (1/2)^n u(n)$ $H_3(n) = (1/5)^n u(n)$

OR

$$0.77 \frac{d^3y(t)}{dt^3} - 3 \frac{dy(t)}{dt} + y(t) = x(t) + 2 \frac{dx(t)}{dt}$$

- (Level [3], CO[3], PO[1])

16EC34

- 1 Time: 3 Hours
Third Semester B.E. Makeup Examination, January 2018
SIGNALS AND SYSTEMS
Max. Marks: 100

Instructions:

1. Unit II and Unit V are to be attempted compulsorily.
2. Answer any three questions, choosing one question each from units I, III and IV.

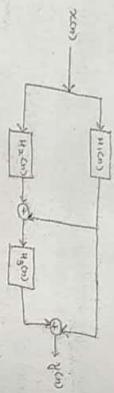


Figure 5b.

(Level[3], CO[3], PO[1,4])

UNIT - IV

- 6 a. Compute the Fourier transform of the following signals: i) $x(t) = e^{-at} \cos wt$
ii) $x(t) = t \quad 0 < t < 1$

- b. State and prove the convolution property of FT.

- c. Find the DTFT of unit step sequence

(Level [2], CO[4], PO[1,4])

OR

- 7 a. Explain Fourier Transform. State and prove the following properties of Fourier Transform: i) Time shifting ii) Time scaling iii) Time reversal

(Level [2], CO[4], PO[1,4])

10

- b. Find the Fourier Transform of i) $x(t) = 1 \quad 0 < t < 1$
ii) $x(t) = 2 \quad 1 < t < 2$
iii) $x(n) = n(0.5)^n u(n)$

(Level [3], CO[4], PO[1,4])

10

- 8 a. Find the Z-transform of the following functions and indicate the ROC. i) $x(n) = (0.1)^n u(n) + (0.3)^n u(-n-1)$
ii) $x(t) = e^{-at} \sin(\omega t)$
(Level [3], CO[5], PO[1,4])

- b. A discrete LTI system is characterized by $H(z) = \frac{z(3z-4)}{(z-\frac{1}{2})(z-3)}$. Specify the ROC and determine $h(n)$ if the system is i) causal ii) stable

(Level [4], CO[5], PO[1,4])

UNIT - V

- 9 a. Sketch the output $y(n)$ of an LTI system is characterized by the equation

$$y(n) = x(n+1) + 5x(n) - 8x(n-1) + 5x(n-2), \text{ input } x(n) = u(n).$$

(Level[3], CO[5], PO[1,4])

10

- b. Convolute $h_1(t)$ and $h_2(t)$ given $h_1(t) = e^{-2t}u(t)$, $h_2(t) = u(t)$. Determine whether $h(t)$ is stable.

(Level [3], CO[2], PO[1,4])

10

- c. Draw the block diagram of the system given by $y(t) = [u(t-1) + r(t-3)] * x(t)$.

(Level[3], CO[2], PO[1,4])

4 a. Find the total response for

$$\frac{dy(t)}{dt} + 0.5y(t) = x(t), t \geq 0; y(0) = 0, x(t) = u(t)$$

10 M

(Level[3], CO[3], PO[1,2,4])

b. A system is modeled by the linear differential equation $0.77 \frac{d^3y(t)}{dt^3} + 3 \frac{dy(t)}{dt} + y(t) = x(t) + 2 \frac{dx(t)}{dt}$. Draw the block diagram for the system in direct forms I and II. (Level[3], CO[3], PO[1,9])

OR

5. a. The impulse response of a system is $h(t) = e^{-4t} u(t-2)$. Check whether the system is stable, causal and memoryless. (Level[4], CO[3], PO[1,9])

b. Find the natural response of following system. $\frac{d^2y(t)}{dt^2} + 3 \frac{dy(t)}{dt} + 2y(t) = 4e^{-3t}, t \geq 0; y(0) = 3, y'(0) = 4$ (Level[3], CO[3], PO[1,9])

c. Determine the overall response of the system shown in fig.5c, given $h_1(t) = e^{-6t}u(t), h_2(t) = e^{2t}u(t), h_3(t) = tu(t)$.

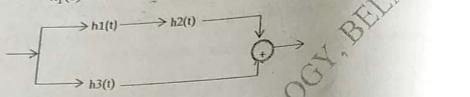


Fig. 5c.

(Level[3], CO[3], PO[1,9])

UNIT - IV

6. a. Define Fourier Transform. State and prove the following properties of Fourier Transform: i) Time shifting ii) Time scaling iii) Time reversal (Level[2], CO[4], PO[1,9])

b. Determine the Fourier Transform of the rectangular pulse defined as $x(t) = \begin{cases} 1, & |t| < a \\ 0, & |t| > a \end{cases}$ Plot the magnitude and phase spectrum. (Level[3], CO[4], PO[1,9])

OR

7. a. Define Fourier Transform. Find the Fourier Transform of the following signals: i) unit impulse ii) decaying exponential iii) The rect function. (Level[2,3], CO[4], PO[1,9])

b. State and prove Parseval's theorem. Compute the energy of the signal $x(t) = e^{-at}u(t)$. (Level[2,3], CO[4], PO[1,9])

UNIT - V

8. a. Find the Z-transform of the following functions and indicate the ROC. i) $x(n) = (0.1)^n u(n) + (0.3)^n u(-n-1)$ ii) $x(t) = e^{-at} \sin(\omega t)$ (Level[3], CO[5], PO[1,9])

b. A discrete LTI system is characterized by $H(z) = \frac{z(3z-4)}{(z-\frac{1}{2})(z-3)}$. Specify the ROC and determine $h(n)$ if the system is i) causal ii) stable (Level[3], CO[5], PO[1,9])

Library - 21/12/18 - 09:30 to 12:50

ISN [REDACTED]

16EC35

Third Semester B.E. Semester End Examination, DEC/JAN 2018-19

NETWORK ANALYSIS

Max. Marks: 100

- Instructions: 1. Units I and III are compulsory
2. Answer one question each from the remaining units
3. Assume suitable data if necessary

UNIT - I

- L CO PO M
1. Calculate the resistance between a and b terminals of the network shown in Fig.1(a)

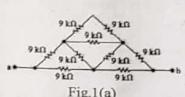


Fig. 1(a)

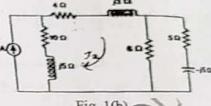


Fig. 1(b)

2. Using mesh analysis find the current flowing through $(4+3j)\Omega$ and power dissipated in 5Ω resistor for the network given in Fig.1(b). (3) (1) (1) (06)

3. Determine the node voltages V_1, V_2 and V_3 for the circuit shown in Fig.1(c) by applying Node analysis. (3) (1) (1) (07)

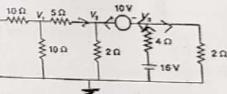


Fig. 1(c)

UNIT - II

L CO PO M
(3) (1) (1) (07)

2. a. Using Millman's theorem, find the current through the load resistance $R_L=10\Omega$ in the circuit shown in Fig. 2(a)

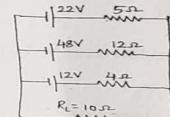


Fig. 2(a)

- b. In the circuit shown in Fig. 2(b), find the value of R that receives maximum power and calculate the maximum power. (2) (2) (1,2) (06)

Note: L (Level), CO (Course Outcome), PO (Programme Outcome), M (Marks)

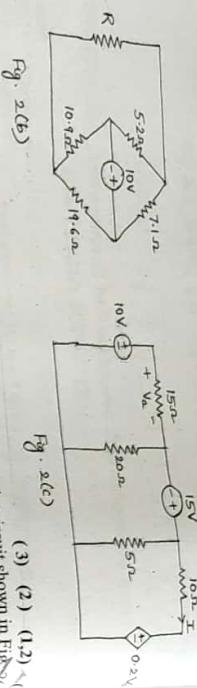


Fig. 2(c)

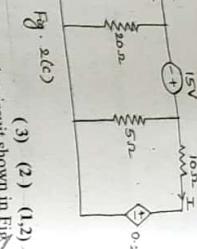


Fig. 2(e)

- c. Using superposition theorem, find the current in 10Ω resistance for the circuit shown in Fig. 2(e)

OR

3. a. Obtain Norton's equivalent circuit for the network shown in Fig. 3(a)

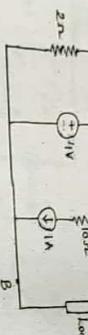


Fig. 3(a)

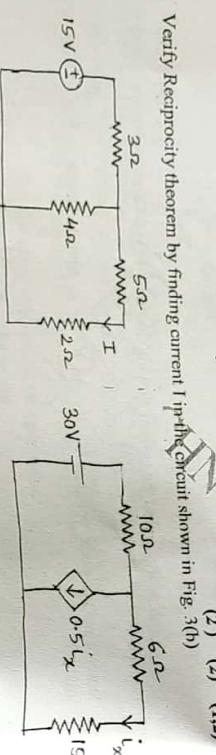


Fig. 3(b)

- b. Verify Reciprocity theorem by finding current i_1 in the circuit shown in Fig. 3(b)

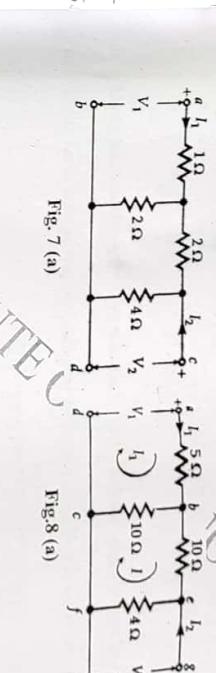


Fig. 3(c)

- c. Find the current flowing through 15Ω in the circuit of Fig. 3(c) using Thevenin's theorem

UNIT - III

4. a. What is resonance? Derive an expression for resonant frequency of a series RLC circuit.

(3) (2) (1,2)

- b. An inductive coil of resistance 6Ω and inductance 1mH is connected in parallel with an branch consisting of a resistance of 4Ω in series with a capacitance of 20μF. Find the resonant frequency and the corresponding current when applied voltage is 200V. When voltage is reduced to 100V, what is the current?

(2) (3) (1,2)

- c. Show that in RLC series resonant circuit $f_r = f_1 f_2$, where f_1 and f_2 are lower and upper cut-off frequencies.

5. a. Derive an expression for the voltage developed across the capacitor during transient period in R-C circuit with DC excitation. Also sketch the voltage waveform.

(2) (4) (1)

UNIT - IV

(2) (3) (1)

L CO PO

- b. In the circuit shown in Fig. 5(b), switch K is changed from a to b at $t=0$ steady state condition having been reached before switching. Find the values of i_1 , di/dt and d^2i/dt^2 at $t=0^+$.

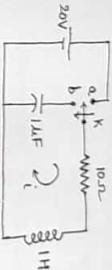


Fig. 5(b)

(3) (4) (1,2) (0,0)

OR

- a. What are transients in electrical systems? Explain the behavior of circuit elements R, L and C under transients, i.e., at $t=0^-$ and $t=0^+$.

- b. In the circuit shown in Fig. 6(b), $V=20V$, $R=10\Omega$, $L=0.5H$, $C=10\mu F$ and $v_c(0)=0$, find $i(0^-)$, $dv/dt(0^-)$, $d^2v/dt^2(0^-)$

UNIT - V

- a. Calculate Y parameters for the network given in Fig. 7 (a)

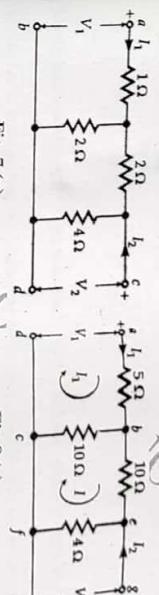


Fig. 7 (a)

(3) (5) (1) (08)

- b. Explain the conditions for symmetry and reciprocity of Z parameters.

(2) (5) (1) (08)

- c. Define the following terms i) Graph ii) Co-tree iii) Tie-set iv) Fundamental Cut set.

(1) (5) (1) (04)

OR

- a. Calculate Z parameters for the network given in Fig. 8(a). Using them calculate Y parameters.

(3) (5) (1) (10)

- b. Derive expressions for Y parameters in terms of Z parameters.

(2) (5) (1) (10)

Third Semester B.E. Makeup Examination, January 2019
NETWORK ANALYSIS

Time: 3 Hours

Max. Marks: 100

Instructions: 1. Unit I and III are COMPULSORY

2. Answer ANY ONE Question from Unit II, IV and V

UNIT - I

- 1 a. Calculate equivalent resistance between terminals A and B using star-delta transformation technique for the circuit shown in Fig. 1(a)

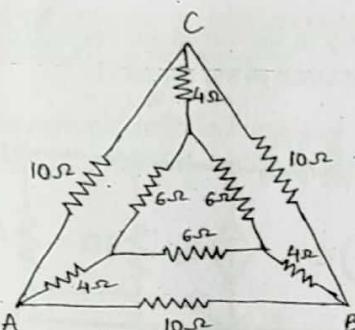


Fig. 1(a)

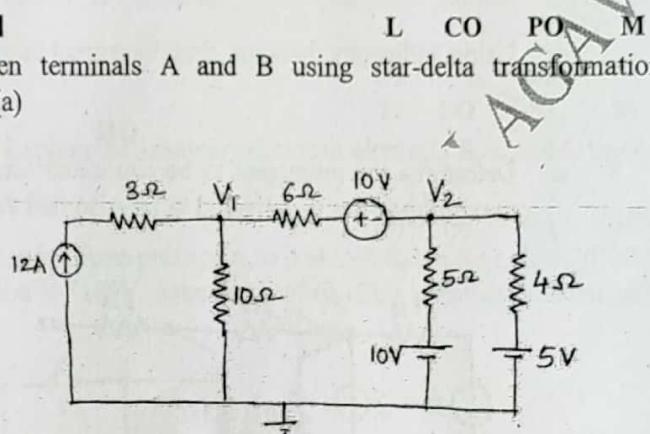


Fig. 1(b)

(2) (1) (1,2) (06)

- b. Using mesh analysis, find the voltages V_1 and V_2 of the circuit shown in Fig. 1(b)

(3) (1) (1,2) (06)

- c. Using node voltage analysis, calculate voltages V_1 and V_2 of the circuit shown in Fig. 1(c)

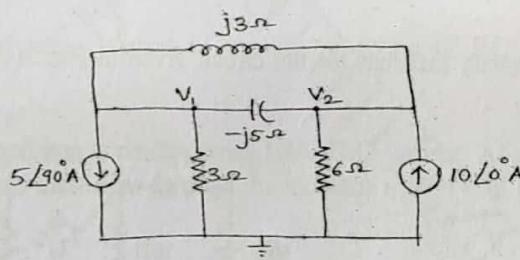


Fig. 1(c)

(3) (1) (1,2) (08)

UNIT - II

L CO PO M

- 2 a. Apply Superposition theorem to find the current i for the circuit shown in Fig. 2(a)

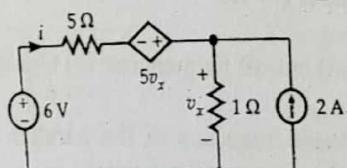


Fig. 2(a)

(3) (2) (1) (07)

Note: L (Level), CO (Course Outcome), PO (Programme Outcome), M (Marks)

- b. Apply Thevenin's theorem to find the power loss in 1Ω resistor connected across x-y terminals in the circuit shown in Fig 2(b).

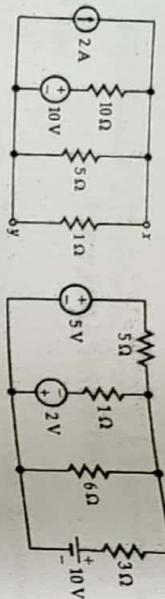


Fig 2 (b)

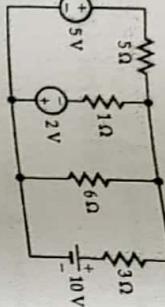


Fig 2 (c)

- c. Using Millman's theorem, find the current through 6Ω resistor for the network shown in Fig 3(a).

OR

- 3 a. Determine the resistance to be connected across x-y in the circuit shown in Fig 3 (a) such that maximum power is delivered to it? Also find the maximum power delivered.

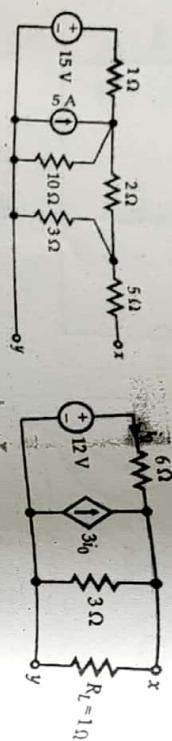


Fig 3(a)

Fig 3(b)

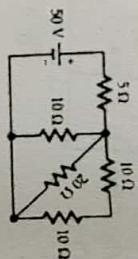


Fig 3(c)

(1) (2) (1) (2)

(3) (2) (1) (1)

a.

- c. Verify Reciprocity theorem for the circuit given in Fig. 3(c) by determining the current through R_{L1} in the circuit given in Fig. 3(b) for the applied voltage $V = 100V$.

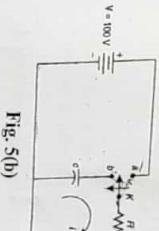


Fig 5(b)

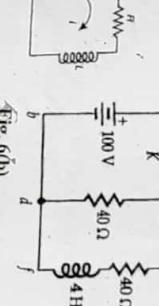


Fig 6(b)

(3) (4) (1) (10)

(2) (4) (1) (10)

(1) (5) (1) (04)

(4) (4) (1) (10)

L CO PO M

OR

a.

- a. Explain the importance of time constant in transient analysis of RC series circuit with D.C. excitation.

- b. In Fig 6 (b) steady state condition is reached with 100V D.C. source. At $t=0$, switch K is suddenly opened. Find the expression of current through the inductor after $t=1/2$ sec, $t=1$ sec. Analyse the results.

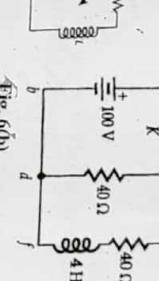


Fig 7(b)

(3) (4) (1) (10)

(2) (4) (1) (10)

(1) (5) (1) (04)

(4) (4) (1) (10)

L CO PO M

UNIT - V

i) Tree ii) Oriented graph iii) Cut-set iv) Incidence matrix

b. Calculate z-parameters for the network given in Fig. 7(b)

(1) (4) (1) (10)

L CO PO M

(3) (2) (1) (04)

L CO PO M

(1) (4) (1) (10)

L CO PO M

(3) (2) (1) (04)

L CO PO M

(1) (4) (1) (10)

L CO PO M

(3) (2) (1) (04)

L CO PO M

- a. Define the following terms with respect to network topology.
- i) Resonance ii) Bandwidth iii) cut-off frequencies iv) Quality factor
- b. Calculate z-parameters for the network given in Fig. 7(b)
- (1) (5) (1) (04)
- (2) (4) (1) (10)
- (1) (5) (1) (04)
- (4) (4) (1) (10)
- L CO PO M

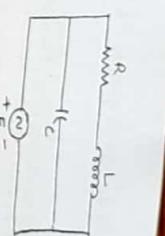


Fig 8 (a)

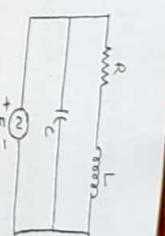


Fig 8 (b)

b.

- b. Derive the expression for resonant frequency of the parallel resonant circuit shown in Fig. 7(b). Also derive the expression for the current at resonance.

- 8 a. Calculate y-parameters for the network given in Fig. 8 (a). Also draw the y-parameter equivalent network.
 b. Derive expressions for z-parameters in terms of y-parameters.

(3) (5) (1,2)
 (2) (5) (1)

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Third Semester B.E. Semester End Examination, Dec/Jan 2017-18

16EC35

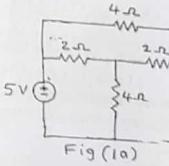
Time: 3 Hours

NETWORK ANALYSIS

Max. Marks: 100

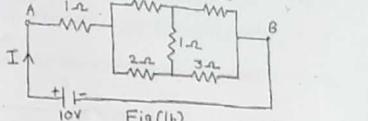
Instructions: 1. Units I and II are COMPULSORY.
 2. Assume suitable data.

- I a. Using source shifting and transformation find the voltage V_x in the circuit shown in Fig.(1a).



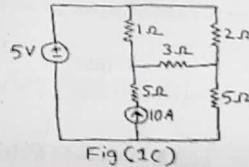
UNIT - I

Fig (1a) 08 M



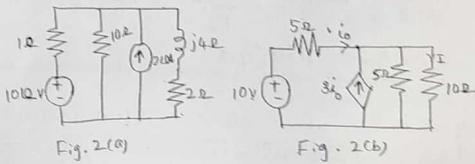
- b. Find the current I in the circuit given in Fig.(1b) by reducing the resistive network between terminals A-B into a single resistor. (Level [3], CO [1], PO [1]) 06 M

- c. Using mesh analysis evaluate all the mesh currents for the circuit shown in Fig.(1c). (Level [3], CO [1], PO [1,2]) 06 M



(Level [2], CO [1], PO [1])

- 2 a. Find the current through $(2+4j)\Omega$ in the circuit given in Fig. 2 (a) using Millman's theorem. (Level [2], CO [1], PO [1]) 06 M



- b. Solve and draw the Thevenin's equivalent circuit of the network given in Fig. 2(b) and also calculate the current I through the 10Ω resistor. (Level [3], CO [2], PO [1]) 08 M

(Level [3], CO [2], PO [1,2])



Note: L (Level), CO (Course Outcome), PO (Programme Outcome), M (Marks)

- c. Apply the maximum power transfer theorem to calculate the load Z_L and maximum power dissipated in the load.

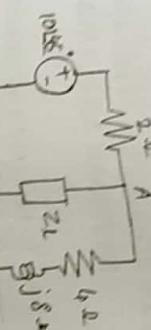


Fig. 2(c)

(Level [3], CO [2], PO [1, 2])

UNIT-III

a. Define : i) Resonance ii) Bandwidth iii) Cut-off frequencies iv) Quality factor.
(Level [1], CO [3], PO [1])

- b. Derive the expressions for I_0 and I_0 for the parallel resonant circuit shown in Fig.(3b).

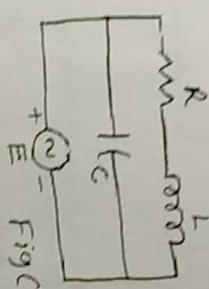


Fig 3(b)

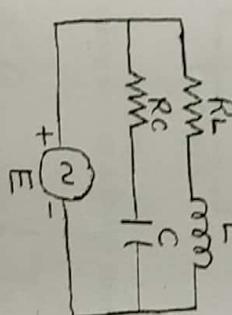
(Level [2], CO [3], PO [1])

- c. Derive the expressions for the cut-off frequencies f_L and f_H in terms of the frequency deviation (δ).

OR

(Level [2], CO [3], PO [1])

- 4 a. Derive an expression for the resonant frequency f_0 for the parallel resonant circuit shown in Fig.(4a).



Fig(4a)

(Level [2], CO [3], PO [1])

- b. Explain the conditions for symmetry and reciprocity of Z parameters.
(Level [3], CO [3], PO [1, 2])

OR

(Level [2], CO [5], PO [1, 12])

- b. An inductive coil of inductance 10mH and a series resistance of 10Ω is connected in parallel with the other branch consisting of a capacitor of 100μF and a series resistance of 100Ω with it. Find the resonant frequency and the corresponding current when the applied voltage is 50V.

(Level [3], CO [3], PO [2])

- 5 a. Find the current at $t > 0$ if alternating voltage v is applied when the switch K is moved from 1 to 2 at $t = 0$ in Fig.5 (a). Assume a steady state current of 1A in L R circuit when the switch was at position 1.

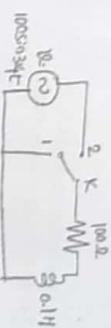


Fig 5(a)

(Level [3], CO [2], PO [1, 2])

- b. How is time constant of an RC circuit defined? Explain its importance in transient analysis with a suitable example.
(Level [3], CO [3], PO [1, 2])

10 M

- a. How is time constant of an RL circuit defined? Explain its importance in transient analysis with a suitable example.
OR
(Level [2], CO [3], PO [1])

10 M

- b. In the circuit shown in Fig. 6 (b) the switch is changed from position 1 to 2 at $t = 0$. The steady state having been reached before switching. Determine the values of i_1 , $d\dot{i}_1/dt$ and $d^2\dot{i}_1/dt^2$ at $t = 0$.
(Level [2], CO [3], PO [1])

10 M



Fig 6 (b)

(Level [3], CO [3], PO [1, 2, 12])

- a. Determine Y parameters of the network given Fig.7 (a). Also draw Y parameter equivalent network.
UNIT-V

10 M

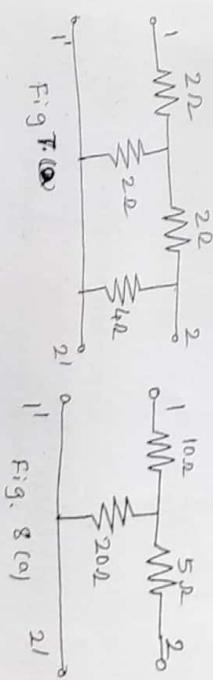


Fig 7(a)

Fig 7(a)

- b. Explain the conditions for symmetry and reciprocity of Z parameters.
(Level [3], CO [3], PO [1, 2])

OR

(Level [2], CO [5], PO [1, 12])

- a. Determine Z parameter for the network given in Fig. 8 (a). Also draw Z parameter equivalent network.
(Level [2], CO [5], PO [1, 12])

10 M

- b. Explain the conditions for symmetry and reciprocity of Y parameters.
(Level [3], CO [3], PO [1, 2])

10 M

USN

16EC35

Third Semester B.E. Makeup Examination January 2018
NETWORK ANALYSIS

Time: 3 Hours

Max. Marks: 100

- Instructions:*
1. Unit I and II are compulsory
 2. Answer any one question from Unit III, IV to V

1. a. Calculate the current flowing through 3Ω resistor of the circuit given in Fig. 1(a) using mesh analysis method.

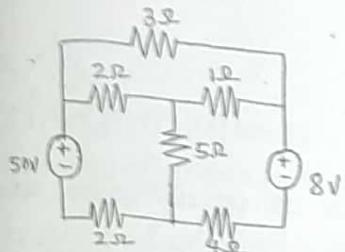


Fig. 1(a)

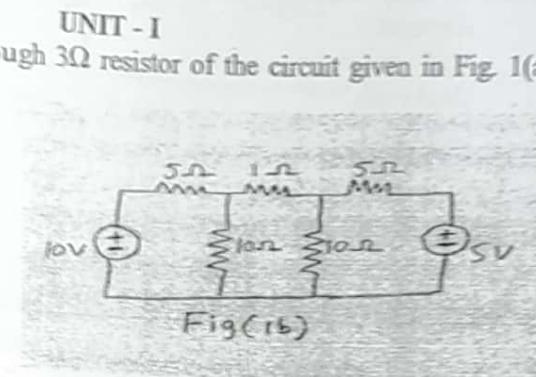


Fig. 1(b)

- b. Determine the value of current through 1Ω in the circuit given in Fig. 1(b) using node analysis method.

(Level [3], CO [1], PO [1])

2. a. Find the value of R in the circuit shown in Fig. 2(a) such that maximum power transfer takes place. Also, calculate the amount of maximum power.

UNIT - II

(Level [3], CO [1], PO [1])

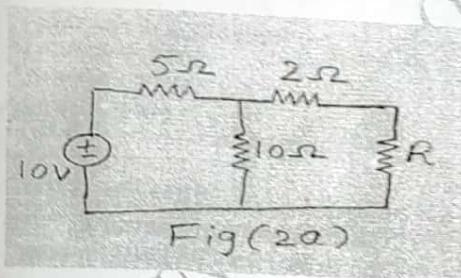


Fig. 2(a)

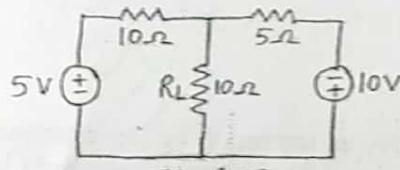


Fig. 2(b)

- b. Using Millman's theorem find the current through $R_L=10\Omega$ for the circuit shown in Fig. 2(b).

(Level [3], CO [2], PO [1, 2])

- c. State: i) Superposition theorem ii) Reciprocity theorem

(Level [3], CO [2], PO [1, 2])

04 M

UNIT - III

(Level [1], CO [2], PO [1])

3. a. A series RLC circuit has a resistance of $1K\Omega$, an inductance of $10mH$ in series with a capacitance of $1nF$. If $50V$ is applied as input across the combination, determine
- i) The resonant frequency
 - ii) The current at resonance
 - iii) The half power frequencies
 - iv) The bandwidth

(Level [L3], CO [3], PO [1])

- b. In a series resonant circuit, prove that resonant frequency $f_0 = \sqrt{(f_1 f_2)}$ where f_1 and f_2 are half power frequencies.

(Level [2], CO [3], PO [1,2,12])

- 4 a. A coil of inductance 10H and 10Ω resistance is connected in parallel with 100pF capacitor. The combination is applied with a voltage of 100V . Determine

i)

Resonant frequency

ii)

Impedance at resonance

iii)

Current at resonance

- iv) Absolute bandwidth

b. Define the terms with appropriate formula for a series resonant circuit.

i) Resonance

ii) Half - power frequencies

iii) Bandwidth

Q-factor

v) Selectivity

OR

a. Derive the expressions for Y parameters in terms of Z parameters.

b. Analyze the circuit shown in Fig.(7b) and evaluate Y parameters.

UNIT - V

(Level [2], CO [5], PO [1])

10 M



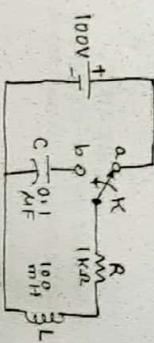
Fig (7b)

(Level [1], CO [3], PO [1,2,12])

UNIT - IV

5 a. Analyze the transient behavior of resistor, inductor and capacitor under switching condition with proper justification for your explanation.

b. In the network shown in Fig.(5b), K is changed from position a to b at $t = 0$. Solve for i , dV/dt and d^2V/dt^2 at $t = 0^+$ if $R = 1\text{K}\Omega$, $L = 100\text{mH}$, $C = 0.1\mu\text{F}$ and $V = 100\text{V}$. Assume that the capacitor is initially uncharged.



Fig(5b)

(Level [3], CO [4], PO [2])

OR

6 a. Derive an expression for the instantaneous value of the current i for a DC excited series RL circuit. Draw the rough graph of $V_s t$ showing the variation of i with respect to t and mark the time-constant on it. Also write an expression for the time-constant.

(Level [2], CO [4], PO [1])

b. In the circuit shown in Fig.(6b), the capacitor is initially unchanged. Switch K is closed at $t = 0$. The initial value of the current is 25mA . The transient disappears (reduces to 2% of its initial value) after a time of 0.1 sec. By the classical method determine the (i) value of R (ii) value of C (iii) an expression for the current $i(t)$ for $t > 0$.

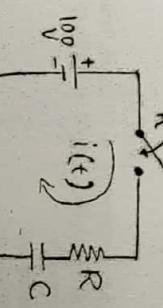


Fig (6b)

(Level [4], CO [5], PO [2])

UNIT - V

7 a. Derive the expressions for Y parameters in terms of Z parameters.

b. Analyze the circuit shown in Fig.(8b) and evaluate Z parameters.

UNIT - V

(Level [2], CO [5], PO [1])

10 M

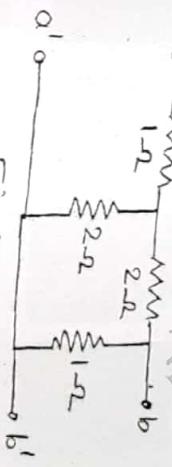


Fig (8b)

(Level [4], CO [5], PO [2])

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15EE34

Third Semester B.E. Semester End Examination, Nov. / Dec. 2016-17
NETWORK ANALYSIS

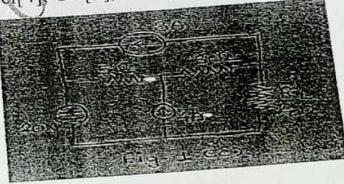
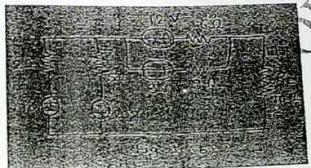
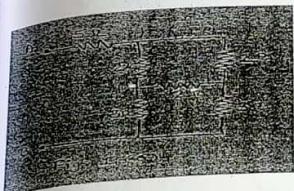
Time: 3 Hours

Max. Marks: 100

- Instructions: 1. Answer FIVE Full questions selecting at least One Question from each Unit.
2. UNIT-III and V are compulsory.

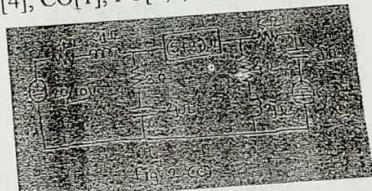
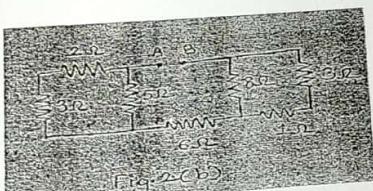
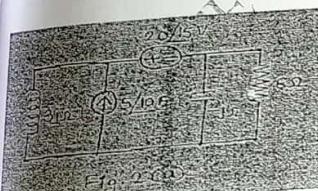
UNIT - I

1. a. Using star-delta transformation find equivalent resistance between terminals AB in the network shown in Fig. 1 (a). (Level[3], CO[1], PO[2,1]) 06 M
b. Using source transformation to find the current through load resistance R_L for the network shown in Fig. 1 (b). (Level[1], CO[1], PO[1,2]) 07 M
c. Using mesh analysis find the power delivered to the load resistance $R_L = 4\Omega$ as shown in Fig. 1 (c). (Level[4], CO[1], PO[1,10]) 07 M



OR

2. a. Using super-nodal analysis technique determine amount of current through 6Ω resistor for the network shown in Fig. 2(a). (Level[3], CO[1], PO[2,3]) 08 M
b. Find the equivalent resistance between the terminals AB for the network as shown in Fig. 2 (b). (Level[1], CO[1], PO[1,2]) 05 M
c. Using node voltage method find V_x for the circuit shown in Fig. 2(c), if the current through the $(5+3)\Omega$ is zero (Level[4], CO[1], PO[2,3,11]) 07 M



UNIT - II

3. a. State and explain Superposition theorem. (Level[3], CO[2], PO[1,2]) 04 M
b. Using Millman's theorem, find I_L through load resistance R_L for the network shown in Fig. 3 (b). (Level[4], CO[2], PO[2,12]) 08 M
c. Find V_x and hence verify reciprocity theorem for the network shown in Fig. 3(c). (Level[4], CO[2], PO[2,3]) 08 M

Fig. 2(a)

Fig. 2(b)

Fig. 2(c)

- UNIT - III**
5. a. Derive an expression for resonant frequency for RLC parallel circuit. (Level[1,4], CO[3], PO[2,3]) 06 M
 b. Derive an expression for Q-factor of RL series circuit. (Level[3], CO[3], PO[1,2]) 06 M
 c. A series RLC circuit has $R=4\Omega$, $L=1mH$ & $C=10\mu F$, calculate Q-factor, resonant frequency, maximum value of voltage across inductor and capacitor. (Level[4,6], CO[3], PO[2,10]) 08 M
- UNIT - IV**
6. a. Define Laplace Transform, find the Laplace Transform of an exponential function
 i. $f(t) = e^{at}$ for $t \geq 0$
 ii. $f(t) = e^{-at} \cos \omega t$
 iii. $f(t) = e^{-at} \sin \omega t$
 iv. $f(t) = 5(t-4)u(t-4)$
 v. $f(t) = 10(u(t)+u(t-2)+u(t-4)+....)$ State Initial value and Final Value Theorem. Find Initial value and Final Values of the function $f(t)$ if $F(s) = 12(s+4)/s^2(s+3)(s+5)$ (Level[2,4], CO[4], PO[1,3]) 10 M
- OR**
7. a. Obtain the Laplace Transform of standard functions
 i. Step Function.
 ii. Ramp Function. The network shown in Fig. 7(b) the switch K is opened at $t=0$. Find the expression for voltage across C for $t > 0$. Also find voltage at $t = 0.036$ sec. (Level[5], CO[4], PO[2,3,10]) 10 M
- OR**
4. a. State and explain Thevenin's theorem. (Level[3], CO[2], PO[1,2]) 08 M
 b. Obtain the Norton's equivalent circuit across the load terminals for the network shown in Fig 4(b) below. (Level[4], CO[2], PO[2,3]) 08 M
 c. Find the value of load resistance when maximum power is transferred across it and also find the maximum power for the network shown in Fig. 4(c) above. (Level[4], CO[2], PO[2,11]) 08 M
- UNIT - V**
- a. State and explain hybrid parameters. (Level[2], CO[4], PO[3,4]) 06 M
 b. Derive Y-parameters of a circuit in terms of Z-parameters. (Level[4], CO[4], PO[2,4]) 06 M
 c. Using the definition, find Y-parameters of the two port network shown in Fig. 8(a). ((Level[5], CO[4], PO[2,10])) 08 M
- UNIT - VI**
- a. State and explain hybrid parameters. (Level[2], CO[4], PO[3,4]) 06 M
 b. Derive Y-parameters of a circuit in terms of Z-parameters. (Level[4], CO[4], PO[2,4]) 06 M
 c. Using the definition, find Y-parameters of the two port network shown in Fig. 8(a). ((Level[5], CO[4], PO[2,10])) 08 M

Third Semester B.E. Semester End Examination, Dec/Jan 2018-19
STATISTICAL-NUMERICAL-FOURIER TECHNIQUES /
ENGINEERING MATHEMATICS - III

Time: 3 Hours

Max. Marks: 100

- Instructions: 1. Units III and V are compulsory.
 2. Answer any one full question from remaining units.

UNIT - I

1. Compute the real root of the equation $x \log_{10} x - 1.2 = 0$ by the method of false position. Carry out three iterations. L CO PO M (2) (1) (1) (07)

- b. Using Runge-Kutta method of fourth order, find $y(0.2)$ for the equation $\frac{dy}{dx} = \frac{y-x}{y+x}$ with $y(0) = 1$ taking $h = 0.2$. (2) (1) (1) (07)

- c. Find a real root of the equation $x^3 + x + 1 = 0$ by fixed point iteration method. (1) (1) (1) (07)

OR

2. Find a real root of the equation $xe^x - \cos x = 0$ applying Newton-Raphson's method correct to four decimal places. (1) (1) (1) (06)

- b. Use modified Euler's method to solve $\frac{dy}{dx} = x + |y|$ in the range $0 \leq x \leq 0.4$ by taking $h = 0.2$ given that $y(0) = 1$ initially. (1) (1) (1) (07)

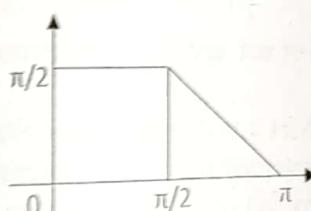
- c. Employ Taylor's series method to find y at $x = 0.1$ and 0.2 correct to three places of decimal in step size of 0.1 for the linear differential equation $\frac{dy}{dx} - 2y = 3e^x$ with $y(0) = 0$. (2) (1) (1) (07)

UNIT - II

L CO PO M (1) (1) (1) (06)

- a. Obtain Fourier expansion of $f(x) = \begin{cases} -\pi & -\pi < x < 0 \\ x & 0 < x < \pi \end{cases}$. Hence deduce that $\sum \frac{1}{(2n-1)^2} = \frac{\pi^2}{8}$ (2) (2) (1) (07)

- b. Find the Fourier cosine series of the function which is expressed graphically as follows



(2) (2) (1) (07)

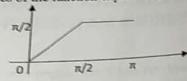
- c. Obtain the Fourier series up to second harmonics to the following data.

x:	0	1	2	3	4	5
y:	4	8	15	7	6	2

(1) (2) (1) (06)

- 4 a. Draw the graph of the function $f(x) = \begin{cases} \pi x & 0 \leq x \leq 1 \\ \pi(2-x) & 1 \leq x \leq 2 \end{cases}$ also find the Fourier expansion of $f(x)$. (2) (2) (1)

- b. Find half range sine series of the function expressed graphically



(2) (2) (1)

- c. Obtain the constant term and first three coefficients in the fourier cosine series of $f(x)$ using the following table

x: 0	1	2	3	4	5
y: 4	8	15	7	6	2

(1) (2) (1)
L CO PO

UNIT - III

- 5 a. Find the Fourier transform of $f(x) = \begin{cases} 1-|x| & \text{in } |x| < 1 \\ 0 & \text{otherwise} \end{cases}$. Hence evaluate $\int_{-\infty}^{\infty} \frac{\sin^2 x}{x^2} dx$. (2) (3) (1)

- b. Find Fourier sine transform of $e^{-\frac{|x|}{k}}$. Hence evaluate $\int_{-\infty}^{\infty} \frac{x \sin x}{x^2+k^2} dx$. (1) (3) (1)

- c. Find $f(x)$ given that $F_c\{f(x)\} = \frac{1}{1+s^2}$. (2) (3) (1)
L CO PO

UNIT - IV

- 6 a. A random variable X has following probability function for various values of x

x: 0	1	2	3	4	5	6	7
y: 0	k						

- (i) Find k (ii) Evaluate $p(x < 6)$, $p(x \geq 6)$ and $p(3 < x \leq 6)$. Also find mean.

(1) (4) (1)

- b. Derive mean and standard deviation of binomial distribution. (2) (4) (1)

- c. The marks of 1000 students in an examination follow normal distribution with mean 75 and standard deviation 5. Find the number of students whose marks will be (i) less than 65 (ii) more than 75 (iii) between 65 and 75. (2) (4) (1)

OR

- 7 a. Verify whether the function $p(x) = \begin{cases} 6x(1-x) & , 0 \leq x \leq 1 \\ 0 & , \text{otherwise} \end{cases}$

- (i) is a probability density function or not and (ii) Also determine mean and standard deviation. (1) (4) (1)

- b. While forming 36-digit binary numbers a malfunctioning digital computer is found to form incorrectly about 1 times in 1000. Assuming the errors in forming different digits to be independent, find the probability of having (i) zero, (ii) one, (iii) more than one and (iv) less than five in digits in a given 36-digit number. (2) (4) (1)

Note: L (Level), CO (Course Outcome), PO (Programme Outcome), M (Marks)

Note: L (Level), CO (Course Outcome), PO (Programme Outcome), M (Marks)

- c. Obtain the equation of the normal probability curve that may be fitted to the following data:
Variable (x) : 6 7 8 9 10 11 12
Frequency (f) : 3 6 9 13 8 5 4

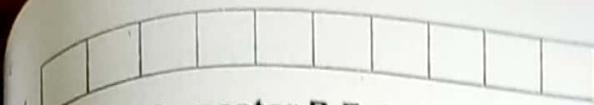
- 8 a. A joint probability distribution is given by the following table
UNIT - V

X \ Y	-8	2	4
1	0.1	0.2	0.4
3	0.3	0.1	0.1

Find the marginal distribution of X and Y and Cov(X, Y). Are they independent?

- b. Assume that a computer system is in one of three states: busy, idle or undergoing repair denoted by states 0, 1, 2 respectively. It is found that the system approximately behaves like a Markov chain with the transition probability matrix $P = \begin{bmatrix} 0.6 & 0.2 & 0.2 \\ 0.1 & 0.8 & 0.1 \\ 0.6 & 0.0 & 0.4 \end{bmatrix}$. Prove that the chain is irreducible, and determine the steady state probability vector.

- c. Define i) State classification of Markov chain. ii) covariance and correlation of X and Y.
(2) (5) (1) (07)
(1) (5) (1) (06)



16MAT31 / MAT31

Third Semester B.E. Semester End Examination, Dec/Jan 2017-18 STATISTICAL-NUMERICAL-FOURIER TECHNIQUES / ENGINEERING MATHEMATICS-III)

Time: 3 Hours

Max. Marks: 100

Instructions:

1. UNIT III and V are compulsory.
2. Answer any one question from remaining each unit.

UNIT - I

Use Regula falsi method to extract the root of the equation $2x - \log_{10}x = 7$ upto 3 decimals. 06 M

Apply Newton-Raphson's method to obtain the non-zero root of the equation $f(x) = xe^x - \cos x = 0$ upto 3 decimals. 07 M

Illustrate fixed point iteration method to find the root of the equation $x = \frac{1}{2} + \sin x$ upto 3 decimals. 07 M

Use Taylor's series method up to 5th derivative term to obtain y(0.3) for the differential equation $\frac{dy}{dx} = x + y^2$, $y(0) = 1$. 06 M

Apply Euler's modified method to solve the differential equation $\frac{dy}{dx} = x^2 - y^2$, $y(0) = 1$ at $x = 0.4$ in 2 steps. 07 M

Illustrate Runge-Kutta's fourth order method to solve $\frac{dy}{dx} = e^x + y$, $y(0) = 0$ at $x = 0.1$ in 1 step. 07 M

Construct Fourier series for the function $f(x) = 2\pi x$, $0 \leq x \leq 2$ 06 M

(Level [3], CO [2], PO [1]) 07 M

Transform the function $f(x) = \begin{cases} x^2, & 0 < x < \frac{\pi}{2} \\ x, & \frac{\pi}{2} < x < \pi \end{cases}$ into half-range sine series. 07 M

(Level [2], CO [2], PO [2]) 07 M

Represent the following function in Fourier series up to first harmonic. 07 M

t sec	0	T/6	T/3	T/2	2T/3	5T/6	T
A amp	1.98	1.3	1.05	1.3	-0.88	-0.25	1.98

(Level [2], CO [2], PO [1])

OR

a. Construct Fourier series for the function $f(t) = 1 - t^2$, $-1 \leq t \leq 1$. 06 M

(Level [3], CO [2], PO [2]) 07 M

b. Transform the function $f(x) = x \cos x$, $0 \leq x \leq 2$ into half-range Fourier cosine series. 07 M

(Level [2], CO [2], PO [1]) 07 M

c. Represent the following function in Fourier cosine series up to second term. 07 M

x	0	1	2	3	4	5
y	2	8	12	10	9	6

(Level [2], CO [2], PO [2])

UNIT - III

a. Evaluate Fourier Transform of the function $f(t) = a^2 - t^2$, $-a \leq t \leq a$. Hence deduce the 06 M

value of $\int_0^\infty \frac{t \cos t - \sin t}{t^2} dt$ (Level [2], CO [3], PO [2])

b. Solve the integral equation $\int_0^\infty f(x) \sin(tx) dx = \begin{cases} 1, & 0 \leq t \leq 1 \\ 2, & 1 \leq t \leq 2 \\ 0, & t \geq 2. \end{cases}$ (Level [2], CO [3], PO [1])

c. Evaluate Fourier Cosine Transform of the function $f(x) = \begin{cases} \cos x, & 0 < x < a \\ 0, & x \geq a. \end{cases}$ (Level [2], CO [3], PO [2])

UNIT IV

6 a. If the following represents valid p.d.f. then calculate k, Mean and Standard deviation given

X	0.5	1.5	2.5	3.5	4.5
P(X)	2k	k	4k	k	2k

(Level [2], CO [4], PO [1])

b. Demonstrate that mean and variance of Binomial distribution are np and npq respectively.

(Level [2], CO [4], PO [1])

c. Under the Normal distribution 31% are below 35 and 9% are above 60. identify the mean

and Standard deviation of the distribution. Given $A(\alpha=0.5)=0.19$ and $A(z=1.4)=0.42$.

(Level [2], CO [4], PO [1])

OR

7 a. If the following represents a valid p.d.f. then calculate k, $P(0 < x < 2)$, $P(x > 2)$ given

$$p(x) = \begin{cases} \frac{kx^2}{1+x^2}, & 1 < x < 5 \\ 0, & \text{elsewhere} \end{cases}$$

(Level [2], CO [4], PO [2])

b. If the conversation on telephone is a Poisson variate with mean 4 mins, then calculate the probability that

(i) conversation lasts for more than 1 min.

(ii) conversation lasts between 2 and 3 mins.

(Level [2], CO [4], PO [1])

c. The Mean of an Exponential distribution is 8, then identify $E(x^2)$, $P(1 < x < 4)$, $P(x > 5)$.

(Level [2], CO [5], PO [2])

UNIT - V

8 a. The joint distribution of two random variables X and Y are given by

X \ Y	1	2	3	4
1	0.06	0.05	.09	0.2
2	0.14	0.15	0.21	0.1

Determine the individual distributions of X and Y. Also verify whether X and Y are stochastically independent.

(Level [2], CO [5], PO [1])

b. A software engineer goes to his work place every day by motor bike or car. He never goes by same mode on two successive days; but if he goes by car on a day then he is equally likely to go by bike or car on the next day. Represent the transition matrix of his mode of transport. If he uses car on the first day of the week, find the probability that (i) bike is used (ii) car is used, on the fifth day.

(Level [2], CO [5], PO [2])

c. Define a stochastic matrix. Find unit fixed probability vector for the stochastic matrix P.

$$P = \begin{pmatrix} 0 & 1 & 0 \\ \frac{1}{8} & \frac{1}{2} & \frac{1}{8} \\ 0 & \frac{1}{2} & \frac{1}{2} \end{pmatrix}$$

(Level [2], CO [5], PO [1])

USN _____

24-11-16

15MAT31

Third Semester B.E. Semester End Examination, Nov. / Dec. 2016-17 ENGINEERING MATHEMATICS III

Time: 3 Hours

Max. Marks: 100

- Instructions: 1. Answer FIVE full questions.
2. Units III and V are compulsory
3. choose one full question from each the remaining units

1 a. Use Regula falsi method to find a real root of the equation $x^3 - 2x - 5 = 0$. Correct to three decimal places. **UNIT - I**

b. Find the real root of the equation $3x = \cos x + 1$ using Newton-Raphson method taking initial approximation $x_0 = 0.6$. Perform three iterations. **OR** (Level [3], CO [1], PO [1])

c. Apply fixed point iteration method to find the root of the equation $\cos x = 2x - 3$ up to 3 decimal places. (Level [1], CO [1], PO [1])

2 a. Use Taylor's series method up to 4th derivatives to obtain $y(0.1)$, given that $\frac{dy}{dx} = xy + 1$, $y(0) = 1$. **OR** (Level [3], CO [1], PO [1])

b. Use Euler's modified method to find the solution at $x = 0.1$ correct to four decimal places of the differential equation $\frac{dy}{dx} = x + y$, and $y=1$ when $x=0$. **OR** (Level [3], CO [1], PO [1])

c. Apply Runge-Kutta method of fourth order to solve $\frac{dy}{dx} = x + y^2$ given that $y=1$ when $x=0$ at $x = 0.2$ in one step. **OR** (Level [3], CO [1], PO [1])

3 a. Construct Fourier series for the function $f(x) = x + x^2$ $-\pi \leq x \leq \pi$. **UNIT - II**

b. Transform the function $f(x) = x \sin x$ $0 < x < \pi$ into half range Fourier sine series. **OR** (Level [3], CO [2], PO [2])

c. Represent the following function in Fourier series up to first harmonic. **OR** (Level [2], CO [1], PO [1])

x	0	$\pi/3$	$2\pi/3$	π	$4\pi/3$	$5\pi/3$	2π
y	1	1.4	1.9	1.7	1.5	1.2	1

(Level [2], CO [2], PO [2])

4 a. Construct Fourier series for the function $f(t) = 1 + t^2$ $-1 \leq t \leq 1$. **OR** (Level [3], CO [2], PO [1])

b. Transform the function $f(x) = x \sin x$ $0 \leq x \leq \pi$ into half range Fourier sine series. **OR** (Level [2], CO [2], PO [2])

c. Represent the following function in Fourier sine series up to second term. **OR** (Level [2], CO [2], PO [2])

x	0	1	2	3	4	5	6
y	5	6	9	7	4	11	5

(Level [2], CO [2], PO [1])

- UNIT - III**
- 5 a. Fit a straight line $y = a + bx$ by least square method to the data
 $x: 50 \quad 70 \quad 100 \quad 120$

- b. Each year a company executive changes his car. If he has a car of make B, if he has a car of make C, he is just likely to change over to a car of make A. If he has a car of make A, he changes over to car of make B. If he has a car of make C, he is just likely to change over to car of make B. If he changes car C on the first year find the probability that he has car i) A, ii) B, iii) C on third year.

- c. Define stochastic process with an example. Also state the four types of stochastic process.
 $(Level[2], CO[3], PO[1])$

- 6 a. Find the correlation coefficient and the lines of regression for the following data
 $x: 1 \quad 2 \quad 3 \quad 4 \quad 5$

- b. Each year a company executive changes his car. If he has a car of make B, if he has a car of make C, he is just likely to change over to a car of make A. If he has a car of make A, he changes over to car of make B. If he has a car of make C, he is just likely to change over to car of make B. If he changes car C on the first year find the probability that he has car i) A, ii) B, iii) C on third year.

- c. Define stochastic process with an example. Also state the four types of stochastic process.
 $(Level[2], CO[3], PO[1])$

- d. Find the joint distribution of two random variables x and y is given by the following table
 $(Level[2], CO[5], PO[1])$

- e. Each year a company executive changes his car. If he has a car of make B, if he has a car of make C, he is just likely to change over to a car of make A. If he has a car of make A, he changes over to car of make B. If he has a car of make C, he is just likely to change over to car of make B. If he changes car C on the first year find the probability that he has car i) A, ii) B, iii) C on third year.

- f. Define stochastic process with an example. Also state the four types of stochastic process.
 $(Level[2], CO[3], PO[1])$

- 7 a. If the following represents valid p.d.f. then calculate k, mean and s.d.
 $X \quad 0 \quad 1 \quad 2 \quad 3 \quad 4 \quad 5 \quad 6$
 $P(X) \quad k \quad 3k \quad 5k \quad 7k \quad 9k \quad 11k \quad 13k$

- b. Demonstrate that mean and variance of an exponential distribution are $1/\alpha$ and $1/\alpha^2$ respectively
 $(Level[1], CO[4], PO[2])$

- c. Under the normal distribution 70% are below 35 and 89% are less than 60 identify the mean and standard deviation Given $A(z=1.226)=0.39$ and $A(z=1.4757)=0.43$
 $(Level[2], CO[4], PO[1])$

- d. If the following represents a valid p.d.f. then compute k, $P(0 < x < 1)$, $P(-0.3 < x < 0.3)$
 $p(x) = k(x+1) \quad -1 \leq x \leq 1$
 $= 0 \quad \text{elsewhere.}$

- e. Demonstrate that mean and variance of a binomial distribution are ' np ' and ' $n\mu q$ ' respectively
 $(Level[1], CO[4], PO[1])$

- f. The mean of an exponential distribution is 5, identify $E(x^2)$, $P(1 < x < 4)$, $P(x > 5)$
 $(Level[2], CO[4], PO[1])$

- 8 a. The joint distribution of two random variables x and y is given by the following table
 $(Level[2], CO[5], PO[1])$

- b. Determine the marginal distribution of x and y
 $(Level[2], CO[5], PO[1])$

- c. Verify whether x and y are independent.

Third Semester B.E. Makeup Examination, Dec. 2016-17
ENGINEERING MATHEMATICS III

Instructions: 1. Questions from Unit III & V are compulsory
 2. Answer any one full question from remaining each units.

Max. Marks: 100

- UNIT - I**
1. a. Use Regula falsi method to extract the root of the equation $f(x) = 2x - \log_{10}x - 7 = 0$ upto 3 decimals. (Level[3], CO[1], PO[1]) 07 M
- b. Apply Newton Raphson method to obtain the root of the equation $f(x) = x^2 - 45 = 0$ upto 3 decimals (Level[3], CO[1], PO[1]) 07 M
- c. Find the root of the equation $f(x) = \cos x - 2x + 3 = 0$ upto 3 decimals using fixed point iteration (Level[3], CO[1], PO[1]) 06 M
- OR**
2. a. Use Taylors series method upto 5th derivative term to obtain $y(0.3)$ for the differential equation $\frac{dy}{dx} = x + 2y, y(0) = 2$ (Level[2], CO[1], PO[1]) 07 M
- b. Apply Euler's modified formula to solve the differential equation $\frac{dy}{dx} = x^2 - y, y(0) = 2$ at $x = 0.2$ in 2 steps (Level[3], CO[1], PO[1]) 07 M
- c. Illustrate Runge Kutta fourth order method to solve $\frac{dy}{dx} = \frac{y-x}{y+x}, y(0) = 1$ at $x = 0.2$ in one step (Level[3], CO[1], PO[1]) 06 M
- UNIT - II**
3. a. Find Fourier series of $f(x) = x - x^2$ from $x = -\pi$ to $x = \pi$ (Level[2], CO[1], PO[1]) 06 M
- b. Find half range sine series of $f(x) = x$ in $0 \leq x \leq 2$ (Level[2], CO[2], PO[1]) 07 M
- c. Obtain the constant term and the coefficients of first sine and cosine terms in the Fourier expansion of y as (Level[2], CO[2], PO[1]) 07 M
- | | | | | | | |
|------|---|----|----|----|----|----|
| $x:$ | 0 | 1 | 2 | 3 | 4 | 5 |
| $y:$ | 9 | 18 | 24 | 28 | 26 | 20 |
- OR**
4. a. Find Fourier series for the function $f(x) = x^2$, in $-1 \leq x \leq 1$ (Level[2], CO[2], PO[1]) 06 M
- b. Express $f(x) = \pi - x$ in $0 < x < \pi$ as cosine half range series. Hence deduce that $\frac{1}{1^2} + \frac{1}{3^2} + \frac{1}{5^2} + \dots = \frac{\pi^2}{8}$ (Level[2], CO[2], PO[1]) 07 M
- c. Expand y in terms of Fourier series up to first harmonic given that the values (y) of the displacement in inches of a certain machine part for the rotation x of the flywheel as (Level[2], CO[2], PO[1]) 07 M
- | | | | | | | |
|------|---|---------|----------|----------|----------|----------|
| $x:$ | 0 | $\pi/6$ | $2\pi/6$ | $3\pi/6$ | $4\pi/6$ | $5\pi/6$ |
| $y:$ | 0 | 9.2 | 14.4 | 17.8 | 17.3 | 11.7 |

(Level[2], CO[2], PO[1])

5

- a. The straight line $y = ax + b$ for the following data using the method of least squares.

UNIT - III					
X	-1	0	1	2	0
Y	2	5	3	0	

- b. Fit the parabola $y = ax^2 + bx + c$ by method of least squares.

X	1	1.5	2	2.5	3	3.5	4
Y	1.1	1.3	1.6	2	2.7	3.4	4.1

- c. Calculate coefficient of correlation and identify both regression lines for the data below

X	1	2	3	4	5	6	7
Y	9	8	10	12	11	13	14

- 6 a. The probability density function of a variable x is

$$x: \begin{array}{cccccc} 0 & 1 & 2 & 3 & 4 & 5 & 6 \\ Y: & k & 3k & 5k & 7k & 9k & 11k & 13k \end{array}$$

Find k, $p(x < 4)$, $p(x \geq 5)$, $p(3 < x \leq 6)$

(Level[2], CO[4], PO[1])

- b. The probability that a pen manufactured by a company will be defective is $\frac{1}{10}$. If 12 such pens are manufactured, then using Binomial distribution find

- i) Exactly two will be defective
ii) At least two will be defective

(Level[2], CO[4], PO[1])

- c. In a test on 2000 electric bulbs, it was found that the life of a particular make was normally distributed with an average life of 2040 hours and S.D. of 60 hours. Using Normal distribution, estimate the number of bulbs likely to burn for

- i) More than 2150 hours
ii) Less than 1950 hours and
iii) More than 1920 hours but less than 2160 hours.

Given that the normal distribution table values are $A(z = 1.8333) = 0.4664$, $A(z = 1.33) = 0.4082$, $A(z = 2) = 0.4772$

7 a. If $f(x)$ is a continuous p.d.f given by

$$\begin{aligned} f(x) &= kx, & 0 \leq x < 2 \\ &= 2k, & 2 \leq x < 4 \\ &= -kx + 6k, & 4 \leq x < 6 \\ &= 0 \text{ otherwise} \end{aligned}$$

Then find k and mean value of x

(Level[2], CO[4], PO[1])

- b. Show that the mean and standard deviation of an exponential distribution are same.

(Level[2], CO[4], PO[1])

- c. If the probability of bad reaction from a certain injection is 0.0001, determine the chance that out of 2000 individuals more than two will get a bad reaction. Use poisson distribution

(Level[2], CO[4], PO[1])

07 M

8

- a. The joint distribution of two random variables X and Y are given by

UNIT - V					
Y	1	2	3	0	0
X	1	0.06	0.15	.09	0.7 M

- b. Determine the individual distributions of X and Y. also verify whether X and Y are stochastically independent.

(Level[2], CO[5], PO[1])

06 M

9

- c. A software engineer goes to his work place every day by motor bike or car. he never goes by on two successive days, but if he goes by car on a day then he is equally likely to go by bike or car on the next day. Represent the transition matrix of this mode of transport. If he uses car on the first day of the week, find the probability that (i) bike is used(ii) car is used on the fifth day.

(Level[2], CO[5], PO[1])

07 M

10

- c. Describe about classification states of a Markov Chain with an example. each

(Level[2], CO[5], PO[1])

06 M