

SC60 Secure Boot User Guide

Smart LTE Module Series

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About the Document

History

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1 Introduction

This document mainly introduces what is secure boot and how to use the secure boot function of Quectel SC60 module.





2 Secure Boot Overview

Secure boot refers to the bootup sequence that establishes a trusted platform for secure applications. It starts as an immutable sequence that validates the origin of the code using cryptographic authentication so only authorized software can be executed. The bootup sequence places SC60 in a known security state and protects it against binary manipulation of software and reflashing attacks.

A secure boot system adds cryptographic checks to each stage of the bootup process. This process asserts the authenticity of all secure software images that are executed by SC60. This additional check prevents any unauthorized or maliciously modified software from running on the module. Secure boot is enabled through a set of hardware fuses. For the code to be executed, it must be signed by the trusted entity identified in the hardware fuses.

NOTE

Secure boot is not guaranteed without blowing an eFuse. Licensees must blow certain eFuses.

To sign the images, a trusted vendor uses their private key to generate a signature of the raw code that they want to use, and adds this to the device alongside the software binary. The device contains the corresponding public key of the vendor, which can be used to verify that the binary has not been modified and that it was provided by the trusted vendor in question.

Images (the format in which the code is packaged) can be signed using the licensee's own code signing system. Signed images include the code signature and the certificate chain. The certificates carry the public keys used to decrypt the certificate and image signatures. Secure boot supports 2048 bit exponent 3 or 65537 public RSA keys for the certificate and image signatures. The certificate signatures support the PKCS v1.2 standard format for SHA1 or SHA256, while the code signature follows a proprietary algorithm.

The certificate chain can have three certificates: attestation certificate \rightarrow attestation Certificate Authority (CA) certificate \rightarrow root certificate; or two certificates: attestation certificate \rightarrow self-signed root certificate. For the two-certificate chain model, the root of the certificate chain must be self-signed, which need not be the case for the three-certificate chain model. Version 1, 2, or 3 X.509 certificate format is supported.

The boot up of a device comprises a multiple-stage process. Each image in the stage performs a specific function, and each image is verified by the previous image (e.g., Primary Boot Loader (PBL) \rightarrow Secondary Boot Loader (SBL) \rightarrow ARM® TrustZone). The root of trust (the most trusted entity that kicks off this process) is the PBL, which is firmware and therefore already trusted. Before the next image in the



boot up sequence is executed, that image is first authenticated to ensure that it contains authorized software. For example, control passes to the SBL only after it has been successfully authenticated by the PBL. Since the SBL is now trusted, it can be trusted to authenticate the image. The images further establish the security of the device through their functionality.



3 Set up Compiling Environment

This chapter takes SC60 to illustrate how to set up compiling environment. Customers should download the files of corresponding version, not the wrong version.

Install open source tools Python and OpenSSL, or use Quectel's compile_tools tools.

Open source tools Python and OpenSSL can be downloaded from their respective official websites. Quectel's compile_tools can be downloaded from FTP. After downloading, extract *compile_tools.tar* to *C:* root directory.

2. Download tool prebuilt for QFIL

Download file *SC60_Android_XXX_prebuilt_for_QFIL_XXX.rar* from FTP and unzip it. Then change the Read Only files into Read-Write ones.

3. Download firmware SC60xx_prebuilt

Download file *SC60xxTAR02A01V01H16G_prebuilt.zip* from FTP and unzip it. Then use the unzipped files to overwrite prebuilt_for_QFIL.

4. Modify partition.xml

non-hlos, tz, rpm, sbl and other files of the firmware are not written in partition.xml by default. When using secure boot, there is a need to reflash signed firmware image, and thus customers have to modify common/build/partition.xml to fill in the corresponding filename.



NOTE

Please make sure partition.xml exactly matches with the firmware version.

5. Download sec_boot_tools

Download file $sec_boot_tools_SC60_SG30.zip$ from FTP and unzip it. Then use the unzipped files to overwrite prebuilt for QFIL.



4 Configure OEM Key

The flow to configure OEM key is illustrated below:

1. Run as administrator to run the following command, and then a new key will be generated in common/tools/sectools/resources/data_prov_assets/Signing/Local/oem_certs.

build_sc60_secboot.bat newkey

```
D:\quecte1\SC60_Android_7.1.2_prebuilt_for_QFIL>
D:\quectel\SC60_Android_7.1.2_prebuilt_for_QFIL>build_SC60_secboot.bat newkey
Loading 'screen' into random state – done
Generating RSA private key, 2048 bit long modulus
unable to write 'random state'
e is 3 (0x3)
Loading 'screen' into random state - done
Loading 'screen' into random state - done
Generating RSA private key, 2048 bit long modulus
unable to write 'random state'
e is 3 (0x3)
Loading 'screen' into random state - done
Loading 'screen' into random state – done
Signature ok
subject=/C=US/ST=CA/L=SANDIEGO/0=0EM/0U=General OEM attestation CA/CN=0EM attest
ation CA
Getting CA Private Key
unable to write 'random state'
        1 file(s) moved.
        1 file(s) moved.
```

Backup these keys.

Table 1: OEM Key List

Keys	Path	Description
qpsa_rootca.key	common/tools/sectools/resources/data_prov_assets/ Signing/Local/oem_certs	Root CA private key
qpsa_rootca.cer	common/tools/sectools/resources/data_prov_assets/ Signing/Local/oem_certs	Root certificate



qpsa_attestca.key	common/tools/sectools/resources/data_prov_assets/ Signing/Local/oem_certs	Attestation CA private key
qpsa_attestca.cer	common/tools/sectools/resources/data_prov_assets/ Signing/Local/oem_certs	Attestation CA certificate



5 Sign Zap

Use the following command to sign zap images:

1. Run the command below, and then the new image a*_zap.* will be generated in the path below: /common/tools/sectools/secimage_output/8953/gfx_microcode.

build_sc60_secboot.bat zap

- Copy the new image a*_zap.* to Android source code and then build the Android image. Android source code is available from the path below:
 LA.UM.5.6/LINUX/android/vendor/qcom/proprietary/prebuilt_HY11/target/product/msm8953_64/syst em/etc/firmware/a*_zap.*.
- 3. Copy Android image to prebuilt_for_QFIL_xxx/LINUX/android/out/target/product/msm8953/.

NOTE

If a*_zap.* is not signed, then system cannot work. So there is a need to use the signed image to rebuild Android image. Path of a*_zap.* is shown below:

LA.UM.5.6/LINUX/android/vendor/qcom/proprietary/prebuilt_HY11/target/product/msm8953_64/system/e tc/firmware/a*_zap.*.



6 Generate Secure Images

The steps to generate secure images are illustrated below:

1. Use the following command to sign all images:

build_sc60_secboot.bat

```
\barret\SC60_Android_7.1.2_prebuilt_for_QFIL_20170829>build_SC60_secboot.bat
Logging to I:\barret\SC60_Android_7.1.2_prebuilt_for_QFIL_20170829\MSM8953.LA.2.0\common\sectools\se
cimage_output\SecImage_log.txt
     SecImage launched as: "common/sectools/sectools.py secimage -m . -p 8953 -sa"
Config path is set to: I:\barret\SC60_Android_7.1.2_prebuilt_for_QFIL_20170829\MSM8953.LA.2.0\common
sectools/config/8953/8953_secimage.xml
Chipset is set to: 8953
 MARNING: OEM ID is set to 0 for sign_id "sbl1"
 ARNING: OEM ID is set to 0 for sign_id 'prog_emmc_firehose_ddr'
PRINTING: OEM ID is set to 8 for sign_id "prog_emmc_firehose_lite"

PARNING: OEM ID is set to 0 for sign_id "validated_emmc_firehose_ddr"

PARNING: OEM ID is set to 0 for sign_id "validated_emmc_firehose_ddr"

PARNING: OEM ID is set to 0 for sign_id "validated_emmc_firehose_lite"
 ARNING: OEM ID is set to 0 for sign_id "qsee"
PARNING: OEM ID is set to 0 for sign_id "devcfg"
PARNING: OEM ID is set to 0 for sign_id "appsbl"
PARNING: File not found in meta build: uefi
 ARNING: OEM ID is set to 0 for sign_id "adsp
 ARNING: OEM ID is set to 0 for sign_id "mba"
ARNING: OEM ID is set to 0 for sign_id "modem"
 ARNING: OEM ID is set to 0 for sign_id "rpm"
 ARNING: OEM ID is set to 0 for sign_id "wons
```

2. Then store the signed images to the default path as below: /common/sectools/common/tools/sectools/secimage_output/8953



7 Generate sec.dat

		:	sec.dat			
secdat	qfuse list header	qfuse	qfuse	qfuse	qfuse	secdat
header		entry	entry	entry	entry	footer

Figure 1: sec.dat Construction

sec.dat contains fuses information that is going to be blown by trustzone. Customers can enable secure boot by blowing OEM special fuses.

The procedure to generate *sec.dat* is shown below:

1. Use the following command to generate sec.dat.

build sc60 secboot.bat sec.dat

```
I:\barret\SC60_Android_7.1.2_prebuilt_for_QFIL_20170829>build_SC60_secboot.bat sec.dat
Logging to I:\barret\$C60_Android_7.1.2_prebuilt_for_QFIL_20170829\MSM8953.LA.2.0\common\sectools\fu
seblower_output\FuseBlower_log.txt
DEBUG: Debug logging to I:\barret\SC60_Android_7.1.2_prebuilt_for_QFIL_20170829\MSM8953.LA.2.0\commo
n\sectools\fuseblower_output\FuseBlower_log_debug.txt
DEBUG:
         FuseBlower launched as: "common/sectools/sectools.py fuseblower -p 8953 -g -v"
DEBUG: Searching configs corresponding to platform: 8953
Config paths set to:
         OEM: I:\barret\SC60_Android_7.1.2_prebuilt_for_QFIL_20170829\MSM8953.LA.2.0\common\sectools\conf
 ig\8953\8953_fuseblower_OEM.xml
         QC: I: \barret \$C60_Android_7.1.2_prebuilt_for_QFIL_20170829 \MSM8953.LA.2.0 \common \sectools \confine fixed for the confine of the confin
  \8953\8953_fuseblower_QC.xml
         UI: None
         USER: I:\barret\SC60_Android_7.1.2_prebuilt_for_QFIL_20170829\MSM8953.LA.2.0\common\sectools\com
 fig\8953\8953_fuseblower_USER.xml
Output dir is set to: I:\barret\SC60_Android_7.1.2_prebuilt_for_QFIL_20170829\MSM8953.LA.2.0\common\
sectools
Secdat generated at: I:\barret\SC60_Android_7.1.2_prebuilt_for_QFIL_20170829\MSM8953.LA.2.0\common\s
ectools\fuseblower_output\v2\sec.dat
Secdat generated at: I:\barret\SC60_Android_7.1.2_prebuilt_for_QFIL_20170829\MSM8953.LA<sub>.2</sub>.0\common\s
 ectools common_output w2\sec.dat
Secdat path is set to: I:\barret\SC60_Android_7.1.2_prebuilt_for_QFIL_20170829\MSM8953.LA.2.0\common
\sectools\fuseblower_output\v2\sec.dat
```

The newly generated sec.dat will be found in common/sectools/common_output/v2/sec.dat, and
please use the new sec.dat to replace the old one in
common\sectools\resources\build\fileversion2\sec.dat. It is recommended to backup the old one



before replacing it with the new one.

3. Load sec.dat file using fastboot or QFIL tool.

The command below can be used when using fastboot tool for sec.dat file loading:

fastboot flash sec <sec.dat file path>

For details of loading sec.dat file with QFIL tool, please refer to Chapter 2 in document [1].



8 Flatten a Meta Build

- 1. Download QFIL tool and install it.
- 2. From the Tools menu, select "Flat Meta Build".
- 3. In the "Content XML" field, click "Browse".
- 4. In the "Flat Build Path" field, click "Browse" and navigate to and select the location where the flat build image will be written. Please make sure that you have Administrator access to the folder where the flat build will be written, and the folder must be empty.
- 5. Select the desired "Product Flavors" and "Device Type".
- 6. Click "**OK**" to start flattening the build. When the flattening of the build completes successfully, messages "Flatten Succeed" and "Finish Flatten" will be displayed in the "Status" field.



Figure 2: QFIL Flat Meta Build Interface

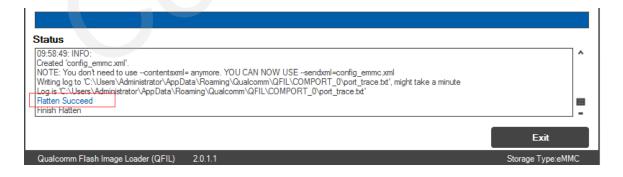


Figure 3: QFIL Flat Meta Build Interface



9 Download Image via QFIL

When using QFIL for image downloading under emergency download mode on S60, it will return an error to indicate download failure.

Reason

The technical reason is that there is no native (.exe/dll) available that would sign digest table, therefore QFIL will not be able to operate in VIP mode.

QFIL team would develop a tool which can support VIP mode when the signing tool that would work in windows environment is ready. Currently it is not ready.

Solutions

1. Use the file in Path 1 to replace the Flat Meta Build image in Path 2.

Path 1

common/tools/sectools/secimage_output/8953/prog_emmc_ddr/prog_emmc_firehose_8953_ddr_quettel.mbn

Path 2: emmc/prog_emmc_firehose_8953_ddr.mbn

2. Make the following file modifications first, then rebuilt prog_emmc_firehose_8909_ddr.mbn, and finally sign the image. When using the signed image for downloading, the downloading will be successful.

File Paths:

.BOOT.BF.3.3/boot_images

/core/storage/tools/deviceprogrammer_ddr/src/firehose/deviceprogrammer_initialize.c

```
// In function void deviceprogrammer_init_hw()
/* comment out - start
#ifndef SKIP_SECBOOT_CHECK_NOT_RECOMMENDED_BY_QUALCOMM
    // The check below is used to ensure that only VIP programmer is run on secure boot devices.
    // In other words, signing the non-VIP programmer is NOT recommended.
    // if (FALSE == isValidationMode() && TRUE == isAuthenticationEnabled())
    //{
        // strlcat(err_log, "Secure boot detected. VIP not enabled:fail ", sizeof(err_log));
        //}
#endif + comment out - end */
```



10 Appendix A Reference

Table 2: Related Documents

SN	Document Name	Remark
[1]	Quectel_SC60_Android_Burning_User_Guide	SC60 Android Burning User Guide

Table 3: Terms and Abbreviations

Abbreviation	Description
AP	Application Processor
APPSBL	Applications Boot Loader
APPS PBL	Applications Primary Boot Loader
BP	Baseband Processor
CA	Certificate Authority
HLOS	High-Level Operating System
mba	Modem Boot Authentication
PIL	Peripheral Image Loading
QFIL	Quelcomm Flash Image Loader
QSEE	Qualcomm Secure Execution Environment
RPM	Resource Power Manager
SBL	Secondary Boot Loader
TZ	Trustzone