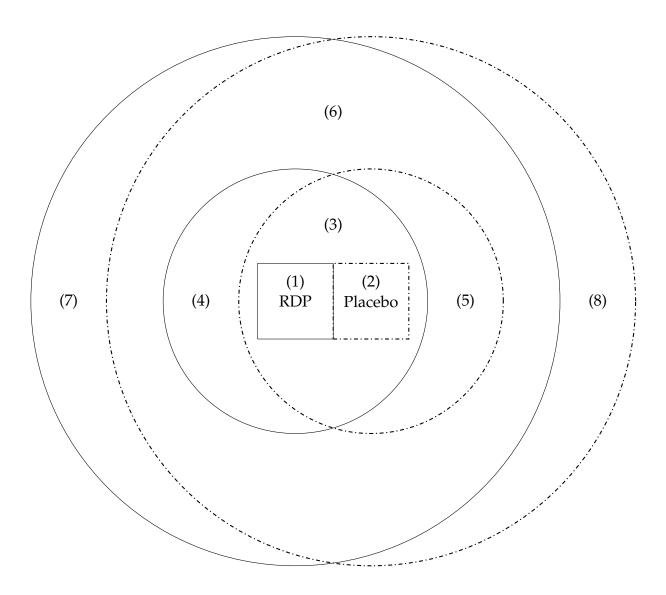
Overlap Approach



$$Y = \underbrace{post}_{(1)-(8)} + \underbrace{(RDPinside}_{(1)} + \underbrace{RDPoutside}_{(3)+(4)} + \underbrace{RDPcontrol}_{(6)+(7)} + \underbrace{PCBinside}_{(2)} + \underbrace{PCBoutside}_{(3)+(5)}) (1 + post) + \epsilon$$