

SYLLABUS :-

Pulse Circuits: Bistable, Astable and monostable MVs and Schmitt Triggers using transistors, OP Amps and 555 timers.

TTL and CMOS Gates: Study the characteristics of TTL and CMOS gates.

Combinational logic circuits: Design and implementation of combinational circuits such as, ALU, 7-segment LED display driver, etc.

Sequential Circuits: Design of sequence generators and detectors, counters, Design of ASMs such as, traffic light controllers, lift controllers, etc.

References

1. H. Taub and D. Schilling, Digital Integrated Electronics, McGraw-Hill .
2. Z. Kohavi, Switching and Finite Automata Theory, Tata McGraw-Hill.
3. Randy H. Katz and Gaetano Borriello, Contemporary Logic Design, Prentice Hall of India.
4. Giovanni De Micheli, Synthesis and Optimization of Digital Circuits, Tata McGraw-Hill.