

SYLLABUS :-

1. Design an amplitude modulation system with envelop detection and study its (a) signal handling, and (b) frequency response characteristics
2. Design a DSBSC modulation system with coherent demodulation and study its (a) signal handling, and (b) frequency response characteristics when carrier is suppressed (i) fully, (ii) partially.
3. Design an SSB modulation system and study its (a) signal handling, and (b) frequency response characteristics
4. Design an AGC stage at 455 kHz centre frequency for voice communication and study effect of AGC on amplifier performance.
5. Design and test a mixer stage for translating AM signal to IF frequency stage.
6. Design a carrier recovery circuit for coherent demodulation of DSBSC signal.