

SYLLABUS :-

Prerequisite: voidCodes, switching algebra, minimization and decomposition of switching functions, Karnaugh map, digital circuits, logical gates - AND, OR, NAND, NOR etc., combinatorial logic, threshold logic, fault diagnosis, sequential machines - synchronous and asynchronous circuits, basic flip-flops - SR flip-flop, T flip-flop, JK flip-flop, flip-flop, clocked and master-slave flip-flop, decoder, multiplexer circuits, counters - synchronous and asynchronous, sequence detector, parity-bit generator, registers, shift registers, minimization and transformation of sequential machines, turing machines.