

SYLLABUS :-

Pre-requisites: EC30004 Architecture trends for 90s, efficiency versus performance, memory organization, allocation and management; hierarchical memory structure and virtual memory; cache coherence; cache memory organization; pipeline and vector processing; vector processors; RISC and CISC architecture; topological issues; design of pipelined processor -SIMD array processors; processor interconnection structure, bus-based multiprocessors; multiple bus multiprocessors; processor interconnection structures; multistage interconnection networks for multiprocessors; multiprocessor machines: Architecture and Programming, Control and Algorithm, Examples; Data Flow Architecture, Systolic/Wave Front Architecture; Wafer Scale Integration and Array Processing.