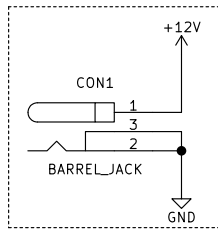
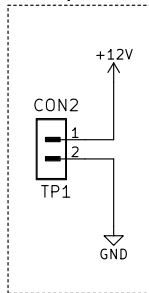


Breakout and Prog

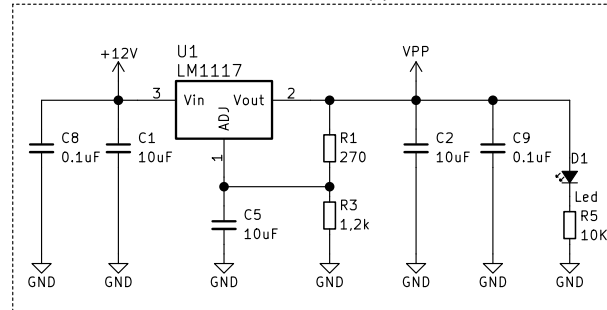
DC Input



Testpoint

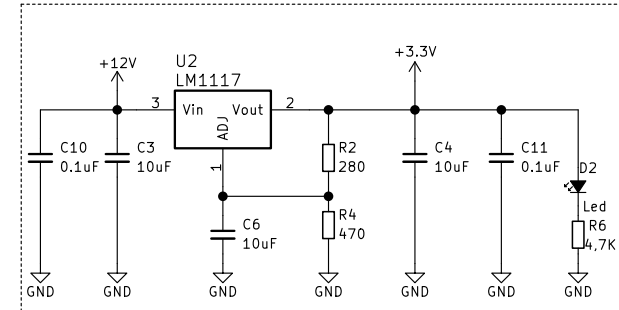


Power DC Vpp

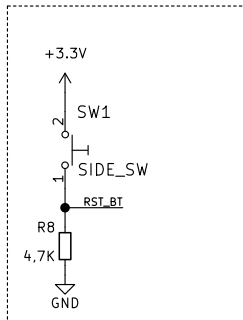


Vpp = 6.7 v, is the programming voltage for the Murata BT module.

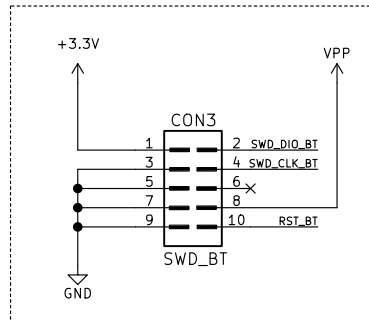
Power DC +3.3V



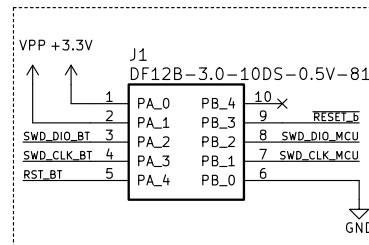
Reset BT



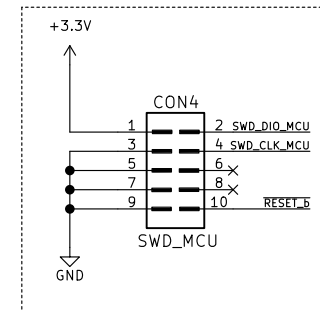
SWD BT



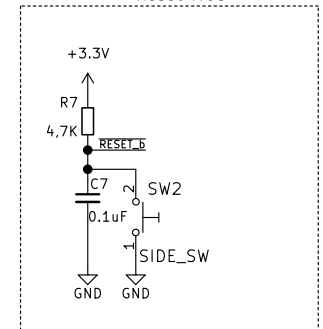
DEBUGGER



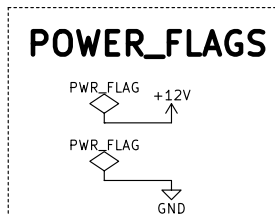
SWD_MCU



Reset MCU



POWER_FLAGS



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