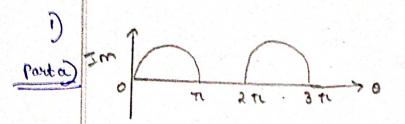
# AMRITA SCHOOL OF COMPUTING CHENNAI AMRITA VISHWA VIDYAPEETHAM

# 22AIE114 INTRODUCTION TO ELECTRICAL AND ELECTRONICS ENGINEERING ASSIGNMNENT - 2

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a) Average value = Asiea

Time period

$$= \sqrt{\frac{V^2 \int_{0}^{\pi} \frac{1}{2} - (0.022)}{2\pi}} d\theta$$

$$= \sqrt{\frac{V_{m}^{2} \times \pi}{\frac{2}{2\pi}}}$$

$$= \sqrt{\frac{v_{m^2}}{4}}$$

2) Form factors:

form factor = 
$$\frac{V_{simy}}{V_{aug}} = \frac{\frac{V_{m}}{2}}{\frac{V_{sh}}{\pi}} = 1.57$$

d) Peak factor:

peak factor = 
$$\frac{V_m}{V_{\text{suns}}} = \frac{V_m}{V_{\text{m/2}}} = 2$$

Devato

a) Average Value:

6) farm factor:

form factor = 
$$\frac{Vm}{V_{mrs}} = \frac{Vm}{V_{m}/S_2} = \frac{V_2}{V_{mrs}}$$

d) peak factor:

Peak factor = 
$$\frac{V_{0}}{\sqrt{2}} = \frac{T}{2\sqrt{2}}$$

$$\frac{2V_{0}}{TL}$$

= 1.11

rebut dual !!

V(+) = 171.4 sin314t

a) Mar voltage:

\* Amplitude is the max voltage

6) Average voltage:

C) Rms voltage:

d) fredmering:

e) Time:-

$$T = 0.028.$$

f) Instantaneous Voltage, when t=4ms; -

V (t) = 283 Lin 314 t

a) Resistance: -

$$7 = Vm = 283 = 70.75 \Omega$$
Im 4

$$cosp = \frac{R}{Z}$$

b) Indutarie:

$$X_{L}^{2} + R^{2} = Z^{2}$$

$$X_{L}^{2} \mp Z^{2} - R^{2}$$

$$X_{L} = \sqrt{Z^{2} - R^{2}}$$

$$= \sqrt{(70.75)^{2} - (50.02)^{2}}$$

$$= \sqrt{2503.562}$$

$$X_{L} = 50.03.$$

## Pour factor: -

$$\cos \phi = \frac{R}{Z}$$

$$(\cos \phi = 50.02$$

$$70.75$$

$$(\cos \phi = 0.7069)$$

a) Resistance,

$$Z = \frac{V_{\text{m}}}{I_{\text{m}}} = \frac{200}{I_{\text{m}}} = \frac{200}{I_{\text{m}}} = \frac{200}{I_{\text{m}}}$$

b) Reactance:-

$$con\phi = \frac{R}{Z}$$

3) Indutance:

$$XL = UL$$
 $L = XL \Rightarrow 10.008$ 
 $314.15$ 
 $L = 0.3 + 0.$ 

$$\phi = \tan^{3}(\frac{y}{3}) \Rightarrow \phi = 53.06$$

#### Vm= 130V, R= 1462

6

#### a) capacitor value! -

$$Z = \sqrt{R^2 + x_c^2}$$

$$x_c^2 = z^2 - R^2$$

# c) power foctor :-

$$(\cos \phi = \frac{\pi}{z} \Rightarrow \phi \cos^{-1}\left(\frac{R}{z}\right)$$

$$con \phi = (on^{-1} \left( \frac{145}{208} \right)$$

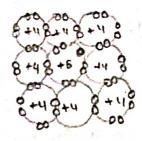


#### (3) PN-Juntion:

the pN-Juntion is formed between the p-type and horse n-type semiconductors. In this p-N juntion is an interfere or a bondary between two semiconductor material types, namely the p-type and the n-type inside a semi-conductor.

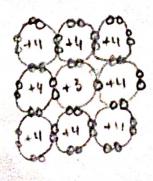
#### N- type:

material such as silicon and n-type material is produced. The extra valence electrons are introduced by putting impurities or depart into the silicon. The depart used to weate an n-type material are group relement the most commonly used. depart from Group vare ansenic, antimony and phosphorous



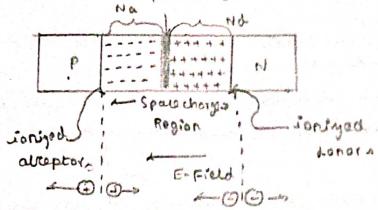
#### p- type:

p-type material is produced when the dopont that is introduced is from Guroupiii. Guroup in elements have only 3 valence electrons and therefore there is an electron missing. This weater a hole (h+), or a +ve charge that can more arround in the mesterial. commonly used Groupiii dopont are aluminium, boron and gallium.



PN- Junetion:

metallurgical Juniton



Characteristics of P-N Junction Diodo:

forward bias: In forward bias the depletion stegion shrinks slightly in width. with this shrinking the energy stequered for change convien to Cross the depletion stegion developes exponentially. herefore, as the applied voltage insures, where starts to flaw autons the function

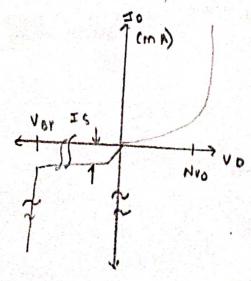
#### Vapplied 70

#### Revenue bias:

Under Rowerse bias the depletion region under This causes the electric field produced by the tono to cancel out the applied reverse bias voltage. A small leakage assessed. to (saturation unverse) flows under neverse bias condition

#### Vapplied Lo

2 - v characteristic growth



VP > Brasvoltage

ai at a day of the series to so the series and so the series and so the series and series are series and serie

15 3 Saturation unvent

VBR => Bounkdown voltage

V& & Barrier potential Collage

Work principle of half-mans & Full mane mone Rectifier and also obtain the efficiency of the both subject

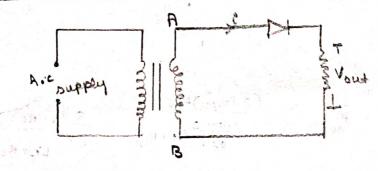
half - woul - Hertification

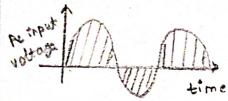
only during the 100 holf-cycles of right a.c supply

the -ve half-cycle of acc supply are suppressed.

(i.es during -ve half cycles, no consument is conducted and no voltage appears amount the load. Therefore consument always from in on a direction (i.e) dec thorough the load thorough after half-cycle

## Working of half-mane Rectifiers





ocoutation of the sector

\* During +ve half yell >

the hose more rectifier allows only
the tre part of an AC bigned. the -ve
part is blocked this pulsating DC
voltage is then sent to load
susister

# # During and traffcycle >

direction from -ve tot ve

Ffficiency of half-wave Reitigier

efficients of Restifix = d.c power output  $\Rightarrow (Jm/\pi)^2 \times RC = 0.406RC$ Frence -  $(Jm/2)^2 (Y+RC) = 0.406RC$  $R_C$ 

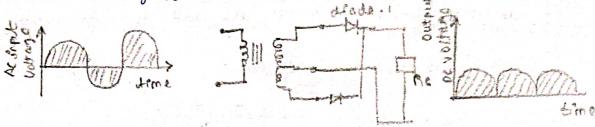
Max. retty ion officiency = 40.6%.

#### Fuel mane -Rectifier

In full runne vertification unvent flower through the load in the same direction for both half-cycles of input a-c voltage.

This can be arbieved with two drodes working efficiently

"For the IVE half-cycle of input Voltage. on e diods supplies awwent to the load and for the -ve half-cycle - the other diods does so the wevent always been in the same direction



efficiency=)

$$\frac{n = Pdc}{P} = \frac{(2 I m/\pi)^2 RL}{\left(\frac{I m}{\sqrt{2}}\right)^2 (V + Rc)} = \frac{8}{\pi^2} \times \frac{Rc}{\Gamma^2}$$

$$= \frac{0.812}{14 Y L}$$

$$\frac{14 Y L}{V_c}$$

Mox - efficiency = 81.27.

# the characteristics of common emider transistor with sketch.

"Common Enliter transistor > two terminals are needed for input and two terminals for output. Transistors have three terminals. So one terminal have to be taken as common terminal for both input and output

> In common emitter, terminal emitter is taken as common for both input and output. so input is given blue base and the emitter terminals.

Configuration is BIT.



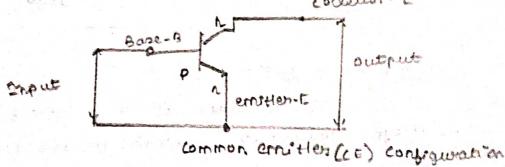
# Treat characteristics:

the input characteristics of BII shows the relationship blu the base werent and the base-emitter voltage.

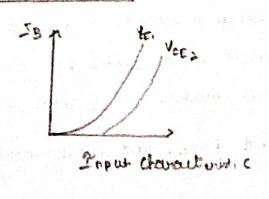
\* It exhibits an exponential Helationship, where a small change in UBE Healto in a significant change in the base

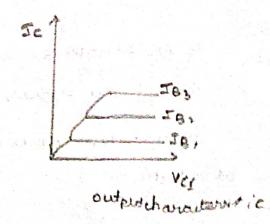
#### Output characteristics:-

\* the output characteristic of the curus villustrates the substrates the substrates the substrates the substrates the collector-emitter voltage.



#### I-v characteristic curve of BJT





Write Difference BJT and FET EfExplain

BJT	and the state of t	FET
3	No.9 terminals	3
Ubu (oz) bub	Semi-conducts' material	N-type or p-type
Controlled by base current	(worent flow	curveret controlled by gate voltage
higher	Gwin	Louier
Slouds	Switch speer	factor.
High	Noice James	Louis.

# Working principle for N- channel JEET:

An N-channel JFET is a type of FET that uses an N-type semiconductor material for the channel. the gabe is insulated from the channel by a thin layer of oxides

of it (socrates an electric field that repels electrons in the channel. this reduces the number of electrons than can flow between the source and drain, and hence the current that blows

