EMV Level 1 Terminal tests

VISA Implementation

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This tool must be used according to the user guide. Any operation relating to maintenance, reparation or calibration must be carried out by qualified personnel. Consequently, in case of failure, contact Microprossss to find out about the procedure to follow. This apparatus is intended for industrial or laboratory use. Its use for any fraudulent operation (reverse-engineering, hacking, eavesdropping) is forbidden.

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1. Introduction

From the EMV specifications, VISA has developed a set of tests to check the compliance of the terminals to the specifications.
MICROPROSS has implemented these tests on a software and hardware platform, in order to quickly achieve a pre-certification of the terminal.
The software includes the main test engine, called MUST, and libraries for terminal testing. The STAR 3150, a smart card emulator coupled to a spy, is used as test tool.
Note that to get a test report, it is required to have installed Microsoft Word®.

2. Reference documents

Test Equipment Requirements for EMV Level 1 Terminal Testing	Version. 1.1, March 2003
EMV '96, ICC Specification for Payment Systems	Version 3.1.1, May 31, 1998
EMV 2000, ICC Specification for Payment Systems, Book 1	Version 4.0, December 2000

These documents are available upon request at industryservices@visa.com.

3. Installation

3.1. MUST software

MUST, the test engine, is a product which can be used to test either smart cards or terminals, contact or contactless. However, it relies on a test library to manage these tests. Please refer to MUST user manual for installation.

3.2. Terminal test suite

The libraries used to test contact terminal are referred under the name CtermVisa. Install these libraries by running the setup program `InstallCTermVisa.exe` provided on the CD-ROM.

3.3. STAR 3150

The test tool used is the STAR 3150. It must be connected using TCP/IP. Refers to the STAR 3150 user manual for setting a proper IP address.

An application needs to be embedded into the STAR 3150 in order to run the tests. To store the application into the STAR 3150, proceed as follow :

- If not already done, install the StarUpdate software provided with the STAR 3150and used to manage the firmware and embedded applications;
- Run `StarUpdate.exe`, and select the file `StarUpdate.dat` provided with this package;
- Update the STAR 3150.
- When you are prompted to program the application CardSim, select **Yes**.

The STAR 3150 is now ready to connect to the MUST to run the tests.

3.4. Terminal under test

3.4.1. The loop-back application

This chapter describes the test application used to test the protocol characteristics. This application must be implemented by the terminal provider to be able to test the Level 1 of the IFM.

According to EMV200 Part one specifications, the Terminal Application Layer (*TAL*) sends an Application Data Unit Command (C-APDU) to the ICC via the Terminal Transport Level (*TTL*). The ICC processes it and sends Application Data Unit Response (R-APDU) via the TTL to the TAL.

There are four command cases (C-APDU) and one response case (R-APDU)

Command cases :

Case	C-APDU Structure
1	CLA/INS/P1/P2
2	CLA/INS/P1/P2 Le
3	CLA/INS/P1/P2 Lc Data
4	CLA/INS/P1/P2 Lc Data Le

Response case : R-APDU = [DATA] SW1 / SW2
The data field is conditional. The trailer made of the two status words is mandatory.

This test application shall meet following main objectives :

- ability to involve Application Data Unit (APDU) of case 1, 2, 3 or 4
- ability to involve large data blocks
- ability to involve multiple APDU exchanges
- ability to ensure that APDU content (data and/or status words) is correctly transmitted from TTL to TAL.
- standard test exchange close to real behavior

3.4.2. Implementation

In order to achieve all the tests, the terminal must be under control of the simulator. A test application is embedded into the terminal under test. This application is simple and easy to write. Here is the description of this test application.

1. After the ATR, the terminal send a C-APDU (Select VISA Credit) :

CLA	INS	P1	P2	Lc	Data	Le
00	A4	00	00	07	A0 00 00 00 03 10 10	00

2. The simulator send its response :

R1	R2	R3	R4	R5	…	Rn	SW1	SW2

3. The terminal interpret the response sent by the simulator

- If SW1/SW2=6A 82 → Stop condition – Deactivation sequence.

- If R-APDU length < 8 bytes :
  Compute LRC of the received R-APDU.
  Send the C-APDU "Select Visa Credit" with P2=LRC.

CLA	INS	P1	P2	Lc	Data	Le
00	A4	00	LRC	07	A0 00 00 00 03 10 10	00

- If R-APDU >= 8 bytes :
  Compute LRC of the received R-APDU.

  R1 defines CLA
  R2 defines INS
  R3 defines P1
  R4 defines APDU case (1, 2, 3 or 4)
  R5 defines Lc for case 3 or 4
  R6 defines Le for case 2 or 4

The next APDU can now be built depending upon R4

Case 1:

CLA	INS	P1	P2	Lc	Data	Le
R1	R2	R3	LRC	-	-	-

Case 2:

CLA	INS	P1	P2	Lc	Data	Le
R1	R2	R3	LRC	-	-	R6

Case 3:

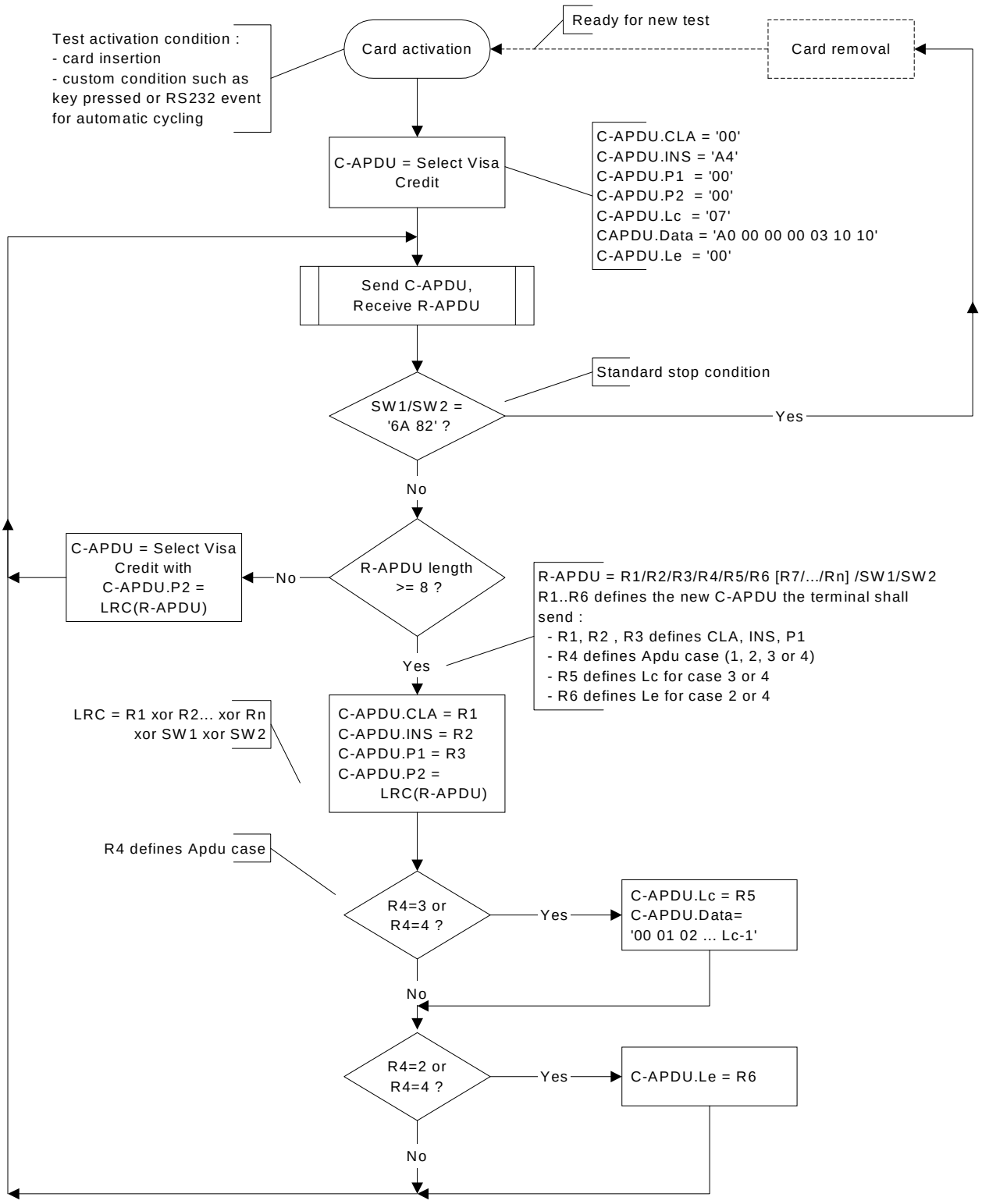
CLA	INS	P1	P2	Lc	Data	Le
R1	R2	R3	LRC	R5	00 01 02 … (Lc-1)	-

Case 4:

CLA	INS	P1	P2	Lc	Data	Le
R1	R2	R3	LRC	R5	00 01 02 … (Lc-1)	R6

Note: P2 is used to check the validity of the exchanges. When sent by the terminal, it conveys the LRC of the previously received R-APDU.
LRC computation: R1 xor R2 … xor Rn xor SW1 xor SW2

3.4.3. General principle of test application



3.4.4. Implementation sample

```
typedef struct
{
unsigned char data[261] ;  // 'CLA INS P1 P2 [Lc + Lc data] [Le]' for command or
unsigned int length ;      // '[Le data] SW1 SW2' for response ( max = 256+2 )
} T_APDU ;

#define TypeAPDU R_Apdu.data[3]
#define LC R_Apdu.data[4]
#define LE R_Apdu.data[5]
#define OK 0

// Proprietary card activation and APDU Command/Response functions
extern unsigned char EMV_PowerOn (char *Historical ) ;
extern unsigned char EMV_apdu (T_APDU *C_Apdu , T_APDU *R_Apdu ) ;

const unsigned char selectVisaCredit[]={0x00,0xA4,0x04,0x00,7,0xA0,0,0,0,3,0x10,0x10,0} ;

T_APDU C_Apdu, R_Apdu ;    // C_Apdu sent to the Card, R_Apdu received from the Card
char ComputeLrc(int ln, unsigned char *p )
{
char lrc ; int i ;
     lrc=0 ; i = 0 ;
     while ( i<ln ) lrc ^= p[i++] ;
     return (lrc) ;
}

static void InitSelectVisa ( T_APDU *apdu )
{
     memcpy ( apdu->data , selectVisaCredit , 13 ) ;
     apdu->length = 13 ;
}

/* Main function */
unsigned char TestVisa()
{
unsigned char RetCode, profil ;
unsigned short DataInSize ;
unsigned char *ptr , Historical[15] ;

     ReturnCode = 0 ;
     RetCode = EMW_PowerOn (Historical ) ;
     If (RetCode != OK)
            Return (RetCode) ;
     InitSelectVisa ( &C_Apdu ) ;         // First APDU Command = Select Visa Credit
     nb_cycles = 0 ;
     while (1)
     {
            RetCode = EMV_apdu(&C_Apdu, &R_Apdu ); // Proprietary Command/Response function
            if ( (RetCode!=OK) || !memcmp( &R_Apdu.data[R_Apdu.length-2],"\x6A""\x82",2 ) )
                   break ;
            // Building new APDU command
            if ( R_Apdu.length < 8 )             // Not enough data to create new command
                   InitSelectVisa ( &C_Apdu ) ;  // -> default APDU Command = Select PSE
            else
            {      // R-APDU : R_Apdu.data -> CLA/INS/P1/Type APDU/Lc/Le/[data]/Me1/Me2
                   memcpy ( C_Apdu.data , R_Apdu.data , 3 ) ;          // CLA/INS/P1
                   C_Apdu.length = 4 ;
                   ptr = C_Apdu.data + 4 ;
                   if ((TypeAPDU > 2) && LC)     // Case 3 or 4 : Lc field is present
                   {
                          DataInSize = LC ;
                          C_Apdu.length += (1+DataInSize) ;
                          *ptr++ = DataInSize ;
                          profil = 0 ;
                          while(DataInSize--)           // Building data string
                                 *ptr++ = profil++ ;    // -> Lc data = 0,1,2...,Lc-1
                   }
                   if ( (TypeAPDU == 2) || (TypeAPDU == 4) )    // Le is present
                   {
                          C_Apdu.length++ ;
                          *ptr++ = LE ;
                   }
            }
            C_Apdu.data[3] = ComputeLrc (R_Apdu.length, R_Apdu.data) ;  // P2
     }
     return (RetCode) ;
}
```

4. Operation

Once all the software is installed and the hardware correctly set, you can run the MUST software:

- Insert the dongle delivered with the test package in any free USB port;
- Start the MUST software;
- Open the script file `TestVisaCTerm.scrt;`
- Edit and select a correct ICS (*Implementation Conformance Statement*) for your terminal under test. An example is provided for a STAR 260.
- Select the correct IP address for the STAR 3150, and connect the tool.

You can now select the tests to be run from the list box. A list of the implemented tests is given in the next chapter. To run the whole set of tests, it may takes several hours, depending of the host and the terminal.

Insert the probe of the STAR 3150 into the terminal running the loop back application, then run the selected test.

Each time a test is run, two files are generated in the **Trace** directory (as selected in the MUST interface):

- A file with extension .txt, giving in plain text the sequence of actions;
- A file with extension .log, which can be read using the StarScope software. This gives a graphical representation of the exchanges.

If a test is failed, examine first the report generated. To get more details, read the .txt file. At least, if you need to refine your diagnostic, start STARSCOPE and open the log file generated.

5. List of tests

The numbering scheme used by Visa sometimes gathers some tests under the same reference.
In order to distinguish between different test cases, the files used and generated have a suffix defined as follow :

EMV96 only (EMV 3.1)	(311)
EMV2000 (EMV 4.0)	(40)
Terminal with PPS	(P)
Terminal without PPS	(nP)
Deactivation	(D)
No deactivation (R-block)	(nD)

For example, test numbered 1CE.007.00 will be called 1CE.007.00(P) if your terminal support PPS, as defined in the ICS.

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CA.001.0x	1CA.001.0x	Physical Compatibility and Contact location	Unimplemented
		1CA.002.00	1CA.002.00	IFM contact force	Unimplemented
		1CB.001.0x	1CB.001.0x	Short circuit resilience	Unimplemented
		1CB.002.00	1CB.002.00	V_{PP} Isolation	Unimplemented
		1CB.003.0x	1CB.003.0x	V_{PP} Voltage	Unimplemented
		1CB.004.0x	1CB.004.0x	I/O Current	Unimplemented
		1CB.005.0x	1CB.005.0x	I/O Transmission voltage	Unimplemented
		1CB.006.0x	1CB.006.0x	I/O Transmission rise and fall time	Unimplemented
		1CB.007.0x	1CB.007.0x	I/O Transmission signal perturbations	Unimplemented
		1CB.008.0x	1CB.008.0x	I/O Reception voltage	Unimplemented
		1CB.009.0x	1CB.009.0x	I/O Reception rise and fall time	Unimplemented
		1CB.010.0x	1CB.010.0x	CLK voltage	Unimplemented
		1CB.011.0x	1CB.011.0x	CLK rise and fall times	Unimplemented
		1CB.012.0x	1CB.012.0x	CLK signal perturbations	Unimplemented
		1CB.013.0x	1CB.013.0x	CLK Frequency and duty cycles	Unimplemented
		1CB.014.0x	1CB.014.0x	RST Voltage	Unimplemented
		1CB.015.0x	1CB.015.0x	RST Rise and fall times	Unimplemented
		1CB.016.0x	1CB.016.0x	RST Signal perturbations	Unimplemented
		1CB.017.0x	1CB.017.0x	V_{CC} Contact Voltage (Short)	Unimplemented
		1CB.017.0x	1CB.017.0x	V_{CC} Contact Voltage (Extended)	Unimplemented
		1CB.018.0x	1CB.018.0x	Transient neutralization on V_{CC}	Unimplemented
		1CC.001.0x	1CC.001.0x	Contact activation sequence	Unimplemented
		1CC.002.0x	1CC.002.0x	Contact deactivation sequence	Unimplemented
		1CC.003.0x	1CC.003.00	Cold Reset – RST to TS=380	
			1CC.003.01	Cold Reset – RST to TS=42000	
		1CC.004.0x	1CC.004.00	Warm Reset	
			1CC.004.01	Warm Reset	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CE.001.00	1CE.001.00	Valid Minimum Character to Character Interval during ATR	
	X	1CE.002.00	1CE.002.00(40)	Valid Maximum Interval between characters during ATR (EMV2000)	
X			1CE.002.00(311)	Valid Maximum Interval between characters during ATR (EMV96)	
	X	1CE.003.00	1CE.003.00(40)	ATR Global Respected – Cold Reset (EMV2000)	
X			1CE.003.00(311)	ATR Global Respected – Cold Reset (EMV96)	
	X	1CE.004.00	1CE.004.00(40)	ATR Global Respected – Warm Reset (EMV2000)	
X			1CE.004.00(311)	ATR Global Respected – Warm Reset (EMV96)	
		1CE.005.xy	1CE.005.00	ETU Measurement – TA1='11' Specific Mode – T=0, Direct – cold reset	
			1CE.005.01	ETU Measurement – TA1='11' Specific Mode – T=0, Inverse – cold reset	
			1CE.005.02	ETU Measurement – TA1='11' Specific Mode – T=1, Direct – cold reset	
			1CE.005.03	ETU Measurement – TA1='11' Specific Mode – T=1, Inverse – cold reset	
	X		1CE.005.10	ETU Measurement – TA1='12' Specific Mode – T=0, Direct – cold reset	
	X		1CE.005.11	ETU Measurement – TA1='12' Specific Mode – T=0, Inverse – cold reset	
	X		1CE.005.12	ETU Measurement – TA1='12' Specific Mode – T=1, Direct – cold reset	
	X		1CE.005.13	ETU Measurement – TA1='12' Specific Mode – T=1, Inverse – cold reset	
	X		1CE.005.20	ETU Measurement – TA1='13' Specific Mode – T=0, Direct – cold reset	
	X		1CE.005.21	ETU Measurement – TA1='13' Specific Mode – T=0, Inverse – cold reset	
	X		1CE.005.22	ETU Measurement – TA1='13' Specific Mode – T=1, Direct – cold reset	
	X		1CE.005.23	ETU Measurement – TA1='13' Specific Mode – T=1, Inverse – cold reset	
		1CE.006.xy	1CE.006.00	ETU Measurement – TA1='11' Specific Mode – T=0, Direct – cold reset	
			1CE.006.01	ETU Measurement – TA1='11' Specific Mode – T=0, Inverse – warm reset	
			1CE.006.02	ETU Measurement – TA1='11' Specific Mode – T=1, Direct – warm reset	
			1CE.006.03	ETU Measurement – TA1='11' Specific Mode – T=1, Inverse – warm reset	
	X		1CE.006.10	ETU Measurement – TA1='12' Specific Mode – T=0, Direct – warm reset	
	X		1CE.006.11	ETU Measurement – TA1='12' Specific Mode – T=0, Inverse – warm reset	
	X		1CE.006.12	ETU Measurement – TA1='12' Specific Mode – T=1, Direct – warm reset	
	X		1CE.006.13	ETU Measurement – TA1='12' Specific Mode – T=1, Inverse – warm reset	
	X		1CE.006.20	ETU Measurement – TA1='13' Specific Mode – T=0, Direct – warm reset	
	X		1CE.006.21	ETU Measurement – TA1='13' Specific Mode – T=0, Inverse – warm reset	
	X		1CE.006.22	ETU Measurement – TA1='13' Specific Mode – T=1, Direct – warm reset	
	X		1CE.006.23	ETU Measurement – TA1='13' Specific Mode – T=1, Inverse – warm reset	
		1CE.007.xy	1CE.007.00(P)	ETU Measurement – TA1='11' in Negotiable Mode – T=0, Direct – cold Reset	
			1CE.007.00(nP)	ETU Measurement – TA1='11' in Negotiable Mode – T=0, Direct – cold Reset	
			1CE.007.01(P)	ETU Measurement – TA1='11' in Negotiable Mode – T=1, Direct – cold Reset	
			1CE.007.01(nP)	ETU Measurement – TA1='11' in Negotiable Mode – T=1, Direct – cold Reset	
	X		1CE.007.10(nP)	ETU Measurement – TA1='12' in Negotiable Mode – T=0, Direct – cold Reset	
	X		1CE.007.11(nP)	ETU Measurement – TA1='12' in Negotiable Mode – T=1, Direct – cold Reset	
	X		1CE.007.20(nP)	ETU Measurement – TA1='13' in Negotiable Mode – T=0, Direct – cold Reset	
	X		1CE.007.21(nP)	ETU Measurement – TA1='13' in Negotiable Mode – T=1, Direct – cold Reset	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CE.008.xy	1CE.008.00(P)	ETU Measurement - TA1='11' in Negotiable Mode - T=0, Direct - warm Reset	
			1CE.008.00(nP)	ETU Measurement - TA1='11' in Negotiable Mode - T=0, Direct - warm Reset	
			1CE.008.01(P)	ETU Measurement - TA1='11' in Negotiable Mode - T=1, Direct - warm Reset	
			1CE.008.01(nP)	ETU Measurement - TA1='11' in Negotiable Mode - T=1, Direct - warm Reset	
	X		1CE.008.10(nP)	ETU Measurement - TA1='12' in Negotiable Mode - T=0, Direct - warm Reset	
	X		1CE.008.11(nP)	ETU Measurement - TA1='12' in Negotiable Mode - T=1, Direct - warm Reset	
	X		1CE.008.20(nP)	ETU Measurement - TA1='13' in Negotiable Mode - T=0, Direct - warm Reset	
	X		1CE.008.21(nP)	ETU Measurement - TA1='13' in Negotiable Mode - T=1, Direct - warm Reset	
		1CE.009.0y	1CE.009.00	Inter-Character Timings Measurement - TC1=00 - Cold Reset - T=0	
			1CE.009.01	Inter-Character Timings Measurement - TC1=80 - Cold Reset - T=0	
			1CE.009.02	Inter-Character Timings Measurement - TC1=F0 - Cold Reset - T=0	
			1CE.009.03	Inter-Character Timings Measurement - TC1=FF - Cold Reset - T=0	
			1CE.009.04	Inter-Character Timings Measurement - TC1=FE - Cold Reset - T=0	
		1CE.010.0y	1CE.010.00	Inter-Character Timings Measurement - TC1=00 - Cold Reset - T=1	
			1CE.010.01	Inter-Character Timings Measurement - TC1=1E - Cold Reset - T=1	
			1CE.010.02	Inter-Character Timings Measurement - TC1=FF - Cold Reset - T=1	
		1CE.011.0y	1CE.011.00	Inter-Character Timings Measurement - TC1=00 - warm Reset - T=0	
			1CE.011.01	Inter-Character Timings Measurement - TC1=80 - warm Reset - T=0	
			1CE.011.02	Inter-Character Timings Measurement - TC1=F0 - warm Reset - T=0	
			1CE.011.03	Inter-Character Timings Measurement - TC1=FF - warm Reset - T=0	
			1CE.011.04	Inter-Character Timings Measurement - TC1=FE - warm Reset - T=0	
		1CE.012.0y	1CE.012.00	Inter-Character Timings Measurement - TC1=00 - warm Reset - T=1	
			1CE.012.01	Inter-Character Timings Measurement - TC1=1E - warm Reset - T=1	
			1CE.012.02	Inter-Character Timings Measurement - TC1=FF - warm Reset - T=1	
		1CE.013.00	1CE.013.00	Inter-Character Timings Measurement - TC1 absent - cold Reset - T=0	
		1CE.014.00	1CE.014.00	Inter-Character Timings Measurement - TC1 absent - Cold Reset - T=1	
		1CE.015.00	1CE.015.00	Inter-Character Timings Measurement - TC1 absent - Warm Reset - T=0	
		1CE.016.00	1CE.016.00	Inter-Character Timings Measurement - TC1 absent - Warm Reset - T=1	
		1CE.017.0y	1CE.017.00	Basic ATRs - Cold reset - TC1=00 - T=0, direct	
			1CE.017.01	Basic ATRs - Cold reset - TC1=00 - T=0, inverse	
			1CE.017.02	Basic ATRs - Cold reset - TC1=FE - T=0, inverse	
			1CE.017.03	Basic ATRs - Cold reset - TC1=1E - T=1, inverse	
			1CE.017.04	Basic ATRs - Cold reset - TC1=00 - T=1, inverse	
		1CE.018.0y	1CE.018.00	Basic ATRs - Warm reset - TB1=00, TC1=00 - T=0, direct	
			1CE.018.01	Basic ATRs - Warm reset - TB1=00, TC1=00 - T=0, inverse	
			1CE.018.02	Basic ATRs - Warm reset - TB1=01, TC1=00 - T=0, direct	
			1CE.018.03	Basic ATRs - Warm reset - TB1=01, TC1=00 - T=0, inverse	
			1CE.018.04	Basic ATRs - Warm reset - TB1=00, TC1=FE - T=0, inverse	
			1CE.018.05	Basic ATRs - Warm reset - TB1=00, TC1=1E - T=1, inverse	
			1CE.018.06	Basic ATRs - Warm reset - TB1=0, TC1=0 - T=1, inverse	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CE.019.0y	1CE.019.00	ATR longer than basic ATR - Cold Reset - T=0	
			1CE.019.01	ATR longer than basic ATR - Cold Reset - T=1	
		1CE.020.0y	1CE.020.00	ATR longer than basic ATR - Warm Reset - T=0	
			1CE.020.01	ATR longer than basic ATR - Warm Reset - T=1	
		1CE.021.0y	1CE.021.00	Supported values for T0 - Cold Reset - T0=E0 - T=0	
			1CE.021.01(nP)	Supported values for T0 - Cold Reset - T0=F0 - T=1 (Terminal without PPS)	
			1CE.021.01(P)	Supported values for T0 - Cold Reset - T0=F0 - T=1 (Terminal with PPS)	
			1CE.021.02	Supported values for T0 - Cold Reset - T0=A0 - T=1	
		1CE.022.0y	1CE.022.00	Supported values for T0 - Warm Reset - T0=E0 - T=0	
			1CE.022.01	Supported values for T0 - Warm Reset - T0=F0 - T=1	
			1CE.022.01(nP)	Supported values for T0 - Warm Reset - T0=F0 - T=1 (Terminal without PPS)	
			1CE.022.02(P)	Supported values for T0 - Warm Reset - T0=A0 - T=1 (Terminal with PPS)	
		1CE.023.00	1CE.023.00	Supported values for TB1 - Cold Reset	
		1CE.024.0y	1CE.024.00	Supported values for TB1 - Warm Reset - TB1=A5 - T=0	
			1CE.024.01	Supported values for TB1 - Warm Reset - TB1 absent - T=0	
			1CE.024.02	Supported values for TB1 - Warm Reset - TB1=A5 - T=1	
			1CE.024.03	Supported values for TB1 - Warm Reset - TB1 absent - T=1	
		1CE.025.0y	1CE.025.00	Supported values of TD1 - Cold Reset - TD1=81	
			1CE.025.01	Supported values of TD1 - Cold Reset - TD1=91	
		1CE.026.0y	1CE.026.00	Supported values of TD1 - Warm Reset - TD1=10	
			1CE.026.01	Supported values of TD1 - Warm Reset - TD1 =81	
			1CE.026.02	Supported values of TD1 - Warm Reset - TD1=91	
		1CE.027.00	1CE.027.00	Supported values of TC2 - Cold Reset - TC2=0A	
		1CE.028.00	1CE.028.00	Supported values of TC2 - Warm Reset - TC2=0A	
		1CE.029.00	1CE.029.00	Supported values of TD2 - Cold Reset - TD2=0E	
		1CE.030.00	1CE.030.00	Supported values of TD2 - Warm Reset - TD2=0E	
		1CE.031.00	1CE.031.00	Default values of TA3 - Cold Reset	
		1CE.032.00	1CE.032.00	Default values of TA3 - Warm Reset	
		1CE.033.0y	1CE.033.00	Supported values of TB3 - Cold Reset - TB3=45, TC1=00	
			1CE.033.01	Supported values of TB3 - Cold Reset - TB3=41, TC1=00	
			1CE.033.02	Supported values of TB3 - Cold Reset - TB3=45, TC1=1E	
		1CE.034.0y	1CE.034.00	Supported values of TB3 - Warm Reset - TB3=45, TC1=00	
			1CE.034.01	Supported values of TB3 - Warm Reset - TB3=41, TC1=00	
			1CE.034.02	Supported values of TB3 - Warm Reset - TB3=45, TC1=1E	
		1CE.035.00	1CE.035.00	Supported values of TC3 - Cold Reset	
		1CE.036.00	1CE.036.00	Supported values of TC3 - Warm Reset	
	X	1CE.037.0y	1CE.037.00	Different conventions between cold and warm ATRs - Direct then inverse, T=0	
	X		1CE.037.01	Different conventions between cold and warm ATRs - Inverse then direct, T=0	
	X		1CE.037.02	Different conventions between cold and warm ATRs - Direct then inverse, T=1	
	X		1CE.037.03	Different conventions between cold and warm ATRs - Inverse then direct, T=1	
	X	1CE.050.00-1	1CE.050.00-1	ATR Global Time Exceeded - Cold Reset (1)	
	X	1CE.051.00-1	1CE.051.00-1	ATR Global Time Exceeded - Warm Reset (1)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
	X	1CE.052.00-2	1CE.052.00-2(40)	Maximum interval between characters during ATR Exceeded- Cold Reset (2) (EMV2000)	
X			1CE.052.00-2(311)	Maximum interval between characters during ATR Exceeded- Cold Reset (2) (EMV96)	
	X	1CE.053.00-2	1CE.053.00-2(40)	Maximum interval between characters during ATR Exceeded-warm reset (2) (EMV2000)	
X			1CE.053.00-2(311)	Maximum interval between characters during ATR Exceeded-warm reset (2) (EMV96)	
	X	1CE.054.00-2	1CE.054.00-2	Incorrect Cold ATR Initiation Delay (2)	
	X	1CE.055.00-2	1CE.055.00-2	Incorrect Warm ATR Initiation Delay (2)	
	X	1CE.056.0y	1CE.056.00(40)	Invalid ATR- Parity Error - Cold Reset - T=0 (EMV2000)	
	X		1CE.056.01(40)	Invalid ATR- Parity Error - Cold Reset - T=1 (EMV2000)	
X			1CE.056.00(311)	Invalid ATR- Parity Error - Cold Reset - T=0 (EMV96)	
X			1CE.056.01(311)	Invalid ATR- Parity Error - Cold Reset - T=1 (EMV96)	
	X	1CE.057.0y	1CE.057.00(40)	Invalid ATR- Parity Error - Warm Reset - T=0 (EMV2000)	
	X		1CE.057.01(40)	Invalid ATR- Parity Error - Warm Reset - T=1 (EMV2000)	
X			1CE.057.00(311)	Invalid ATR- Parity Error - Warm Reset - T=0 (EMV96)	
X			1CE.057.01(311)	Invalid ATR- Parity Error - Warm Reset - T=1 (EMV96)	
	X	1CE.058.0y	1CE.058.00(40)	Incorrect TS – Cold Reset – direct (EMV2000)	
	X		1CE.058.01(40)	Incorrect TS – Cold Reset – direct, parity error (EMV2000)	
	X		1CE.058.02(40)	Incorrect TS – Cold Reset – inverse (EMV2000)	
	X		1CE.058.03(40)	Incorrect TS – Cold Reset – inverse, parity error (EMV2000)	
X			1CE.058.00(311)	Incorrect TS – Cold Reset – direct (EMV96)	
X			1CE.058.01(311)	Incorrect TS – Cold Reset – direct, parity error (EMV96)	
X			1CE.058.02(311)	Incorrect TS – Cold Reset – inverse (EMV96)	
X			1CE.058.03(311)	Incorrect TS – Cold Reset – inverse, parity error (EMV96)	
	X	1CE.059.0y	1CE.059.00(40)	Incorrect TS – Warm Reset – direct (EMV2000)	
	X		1CE.059.01(40)	Incorrect TS – Warm Reset – direct, parity error (EMV2000)	
	X		1CE.059.02(40)	Incorrect TS – Warm Reset – inverse (EMV2000)	
	X		1CE.059.03(40)	Incorrect TS – Warm Reset – inverse, parity error (EMV2000)	
X			1CE.059.00(311)	Incorrect TS – Warm Reset – direct (EMV96)	
X			1CE.059.01(311)	Incorrect TS – Warm Reset – direct, parity error (EMV96)	
X			1CE.059.02(311)	Incorrect TS – Warm Reset – inverse (EMV96)	
X			1CE.059.03(311)	Incorrect TS – Warm Reset – inverse, parity error (EMV96)	
	X	1CE.060.0y	1CE.060.00(40)	Character Overrun in the ATR – Cold Reset – T0=40 (EMV2000)	
	X		1CE.060.01(40)	Character Overrun in the ATR – Cold Reset – T0=20 (EMV2000)	
X			1CE.060.00(311)	Character Overrun in the ATR – Cold Reset – T0=40 (EMV96)	
X			1CE.060.01(311)	Character Overrun in the ATR – Cold Reset – T0=20 (EMV96)	
	X	1CE.061.0y	1CE.061.00(40)	Character Overrun in the ATR – Warm Reset – T0=40 (EMV2000)	
	X		1CE.061.01(40)	Character Overrun in the ATR – Warm Reset – T0=20 (EMV2000)	
X			1CE.061.00(311)	Character Overrun in the ATR – Warm Reset – T0=40 (EMV96)	
X			1CE.061.01(311)	Character Overrun in the ATR – Warm Reset – T0=20 (EMV96)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
	X	1CE.062.0y	1CE.062.00(40)	Unsupported TA1 in Specific Mode – Cold Reset – T=0 (EMV2000)	
	X		1CE.062.01(40)	Unsupported TA1 in Specific Mode – Cold Reset – T=1 (EMV2000)	
X			1CE.062.00(311)	Unsupported TA1 in Specific Mode – Cold Reset – T=0 (EMV96)	
X			1CE.062.01(311)	Unsupported TA1 in Specific Mode – Cold Reset – T=1 (EMV96)	
	X	1CE.063.0y	1CE.063.00(40)	Unsupported TA1 in Specific Mode – Warm Reset – T=0 (EMV2000)	
	X		1CE.063.01(40)	Unsupported TA1 in Specific Mode – Warm Reset – T=1 (EMV2000)	
X			1CE.063.00(311)	Unsupported TA1 in Specific Mode – Warm Reset – T=0 (EMV96)	
X			1CE.063.01(311)	Unsupported TA1 in Specific Mode – Warm Reset – T=1 (EMV96)	
	X	1CE.064.0y	1CE.064.00(40)	Unsupported Values of TB1 – Cold Reset – TB1=05 – T=0 (EMV2000)	
	X		1CE.064.01(40)	Unsupported Values of TB1 – Cold Reset – TB1 absent – T=0 (EMV2000)	
	X		1CE.064.02(40)	Unsupported Values of TB1 – Cold Reset – TB1=10 – T=1 (EMV2000)	
	X		1CE.064.03(40)	Unsupported Values of TB1 – Cold Reset – TB1 absent – T=1 (EMV2000)	
X			1CE.064.00(311)	Unsupported Values of TB1 – Cold Reset – TB1=05 – T=0 (EMV96)	
X			1CE.064.01(311)	Unsupported Values of TB1 – Cold Reset – TB1 absent – T=0 (EMV96)	
X			1CE.064.02(311)	Unsupported Values of TB1 – Cold Reset – TB1=10 – T=1 (EMV96)	
X			1CE.064.03(311)	Unsupported Values of TB1 – Cold Reset – TB1 absent – T=1 (EMV96)	
	X	1CE.065.0y	1CE.065.00(40)	Unsupported Values of TD1 – Cold Reset – T=14 (EMV2000)	
	X		1CE.065.01(40)	Unsupported Values of TD1 – Cold Reset – T=4 (EMV2000)	
X			1CE.065.00(311)	Unsupported Values of TD1 – Cold Reset – T=14 (EMV96)	
X			1CE.065.01(311)	Unsupported Values of TD1 – Cold Reset – T=4 (EMV96)	
	X	1CE.066.0y	1CE.066.00(40)	Unsupported Values of TD1 - Warm Reset - T=14 (EMV2000)	
	X		1CE.066.01(40)	Unsupported Values of TD1 - Warm Reset - T=4 (EMV2000)	
X			1CE.066.00(311)	Unsupported Values of TD1 - Warm Reset - T=14 (EMV96)	
X			1CE.066.01(311)	Unsupported Values of TD1 - Warm Reset - T=4 (EMV96)	
	X	1CE.067.00	1CE.067.00(40)	Unsupported Values of TA2 - Cold Reset (EMV2000)	
X			1CE.067.00(311)	Unsupported Values of TA2 - Cold Reset (EMV96)	
	X	1CE.068.00	1CE.068.00(40)	Unsupported Values of TA2 - Warm Reset (EMV2000)	
X			1CE.068.00(311)	Unsupported Values of TA2 - Warm Reset (EMV96)	
	X	1CE.069.00	1CE.069.00(40)	Irrelevant presence of TB2 - Cold Reset (EMV2000)	
X			1CE.069.00(311)	Irrelevant presence of TB2 - Cold Reset (EMV96)	
	X	1CE.070.00	1CE.070.00(40)	Irrelevant presence of TB2 - Warm Reset (EMV2000)	
X			1CE.070.00(311)	Irrelevant presence of TB2 - Warm Reset (EMV96)	
	X	1CE.071.0y	1CE.071.00(40)	Unsupported Values of TC2 - Cold Reset - TC2=00 (EMV2000)	
	X		1CE.071.01(40)	Unsupported Values of TC2 - Cold Reset - TC2= 0B (EMV2000)	
	X		1CE.071.02(40)	Unsupported Values of TC2 - Cold Reset - TC2=01 (EMV2000)	
	X		1CE.071.03(40)	Unsupported Values of TC2 - Cold Reset - TC2=09 (EMV2000)	
X			1CE.071.00(311)	Unsupported Values of TC2 - Cold Reset - TC2=00 (EMV96)	
X			1CE.071.01(311)	Unsupported Values of TC2 - Cold Reset - TC2= 0B (EMV96)	
X			1CE.071.02(311)	Unsupported Values of TC2 - Cold Reset - TC2=01 (EMV96)	
X			1CE.071.03(311)	Unsupported Values of TC2 - Cold Reset - TC2=09 (EMV96)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
	X	1CE.072.0y	1CE.072.00(40)	Unsupported Values of TC2 - Warm Reset - TC2=00 (EMV2000)	
	X		1CE.072.01(40)	Unsupported Values of TC2 - Warm Reset - TC2=0B (EMV2000)	
	X		1CE.072.02(40)	Unsupported Values of TC2 - Warm Reset - TC2=01 (EMV2000)	
	X		1CE.072.03(40)	Unsupported Values of TC2 - Warm Reset - TC2=09 (EMV2000)	
X			1CE.072.00(311)	Unsupported Values of TC2 - Warm Reset - TC2=00 (EMV96)	
X			1CE.072.01(311)	Unsupported Values of TC2 - Warm Reset - TC2=0B (EMV96)	
X			1CE.072.02(311)	Unsupported Values of TC2 - Warm Reset - TC2=01 (EMV96)	
X			1CE.072.03(311)	Unsupported Values of TC2 - Warm Reset - TC2=09 (EMV96)	
	X	1CE.073.0y	1CE.073.00(40)	Unsupported Values of TD2 - Cold Reset - TD2=20 (EMV2000)	
	X		1CE.073.01(40)	Unsupported Values of TD2 - Cold Reset - TD2=2E (EMV2000)	
	X		1CE.073.02(40)	Unsupported Values of TD2 - Cold Reset - TD2=2F (EMV2000)	
X			1CE.073.00(311)	Unsupported Values of TD2 - Cold Reset - TD2=20 (EMV96)	
X			1CE.073.01(311)	Unsupported Values of TD2 - Cold Reset - TD2=2E (EMV96)	
X			1CE.073.02(311)	Unsupported Values of TD2 - Cold Reset - TD2=2F (EMV96)	
	X	1CE.074.0y	1CE.074.00(40)	Unsupported Values of TD2 - Warm Reset - TD2=20 (EMV2000)	
	X		1CE.074.01(40)	Unsupported Values of TD2 - Warm Reset - TD2=2E (EMV2000)	
	X		1CE.074.02(40)	Unsupported Values of TD2 - Warm Reset - TD2=2F (EMV2000)	
X			1CE.074.00(311)	Unsupported Values of TD2 - Warm Reset - TD2=20 (EMV96)	
X			1CE.074.01(311)	Unsupported Values of TD2 - Warm Reset - TD2=2E (EMV96)	
X			1CE.074.02(311)	Unsupported Values of TD2 - Warm Reset - TD2=2F (EMV96)	
	X	1CE.075.0y	1CE.075.00(40)	Unsupported Values of TA3 - Cold Reset - TA3=FF (EMV2000)	
	X		1CE.075.01(40)	Unsupported Values of TA3 - Cold Reset - TA3=00 (EMV2000)	
	X		1CE.075.02(40)	Unsupported Values of TA3 - Cold Reset - TA3=0F (EMV2000)	
X			1CE.075.00(311)	Unsupported Values of TA3 - Cold Reset - TA3=FF (EMV96)	
X			1CE.075.01(311)	Unsupported Values of TA3 - Cold Reset - TA3=00 (EMV96)	
X			1CE.075.02(311)	Unsupported Values of TA3 - Cold Reset - TA3=0F (EMV96)	
	X	1CE.076.0y	1CE.076.00(40)	Unsupported Values of TA3 - Warm Reset - TA3=FF (EMV2000)	
	X		1CE.076.01(40)	Unsupported Values of TA3 - Warm Reset - TA3=00 (EMV2000)	
	X		1CE.076.02(40)	Unsupported Values of TA3 - Warm Reset - TA3=0F (EMV2000)	
X			1CE.076.00(311)	Unsupported Values of TA3 - Warm Reset - TA3=FF (EMV96)	
X			1CE.076.01(311)	Unsupported Values of TA3 - Warm Reset - TA3=00 (EMV96)	
X			1CE.076.02(311)	Unsupported Values of TA3 - Warm Reset - TA3=0F (EMV96)	
	X	1CE.077.0y	1CE.077.00(40)	Unsupported Values of TB3 - Cold Reset - TB3 absent (EMV2000)	
	X		1CE.077.01(40)	Unsupported Values of TB3 - Cold Reset - TB3=51 (EMV2000)	
	X		1CE.077.02(40)	Unsupported Values of TB3 - Cold Reset - TB3=06 (EMV2000)	
	X		1CE.077.03(40)	Unsupported Values of TB3 - Cold Reset - TB3=00 (EMV2000)	
X			1CE.077.00(311)	Unsupported Values of TB3 - Cold Reset - TB3 absent (EMV96)	
X			1CE.077.01(311)	Unsupported Values of TB3 - Cold Reset - TB3=51 (EMV96)	
X			1CE.077.02(311)	Unsupported Values of TB3 - Cold Reset - TB3=06 (EMV96)	
X			1CE.077.03(311)	Unsupported Values of TB3 - Cold Reset - TB3=00 (EMV96)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
	X	1CE.078.0y	1CE.078.00(40)	Unsupported Values of TB3 - Warm Reset - TB3 absent (EMV2000)	
	X		1CE.078.01(40)	Unsupported Values of TB3 - Warm Reset - TB3=51 (EMV2000)	
	X		1CE.078.02(40)	Unsupported Values of TB3 - Warm Reset - TB3=06 (EMV2000)	
	X		1CE.078.03(40)	Unsupported Values of TB3 - Warm Reset - TB3=00 (EMV2000)	
X			1CE.078.00(311)	Unsupported Values of TB3 - Warm Reset - TB3 absent (EMV96)	
X			1CE.078.01(311)	Unsupported Values of TB3 - Warm Reset - TB3=51 (EMV96)	
X			1CE.078.02(311)	Unsupported Values of TB3 - Warm Reset - TB3=06 (EMV96)	
X			1CE.078.03(311)	Unsupported Values of TB3 - Warm Reset - TB3=00 (EMV96)	
	X	1CE.079.0y	1CE.079.00(40)	Unsupported Values of TC3 - Cold Reset - TC3=FF (EMV2000)	
	X		1CE.079.01(40)	Unsupported Values of TC3 - Cold Reset - TC3=01 (EMV2000)	
X			1CE.079.00(311)	Unsupported Values of TC3 - Cold Reset - TC3=FF (EMV96)	
X			1CE.079.01(311)	Unsupported Values of TC3 - Cold Reset - TC3=01 (EMV96)	
	X	1CE.080.0y	1CE.080.00(40)	Unsupported Values of TC3 - Warm Reset - TC3=FF (EMV2000)	
	X		1CE.080.01(40)	Unsupported Values of TC3 - Warm Reset - TC3=01 (EMV2000)	
X			1CE.080.00(311)	Unsupported Values of TC3 – Warm Reset - TC3=FF (EMV96)	
X			1CE.080.01(311)	Unsupported Values of TC3 – Warm Reset - TC3=01 (EMV96)	
		1CE.081.0y	1CE.081.00	Invalid TCK when protocol <> T=0 - Cold Reset - TCK missing	
	X		1CE.081.01(40)	Invalid TCK when protocol <> T=0 - Cold Reset - Wrong TCK (EMV2000)	
X			1CE.081.01(311)	Invalid TCK when protocol <> T=0 - Cold Reset - Wrong TCK (EMV96)	
		1CE.082.0y	1CE.082.00	Invalid TCK when protocol <> T=0 - Warm Reset - TCK missing	
	X		1CE.082.01(40)	Invalid TCK when protocol <> T=0 - Warm Reset - Wrong TCK (EMV2000)	
X			1CE.082.01(311)	Invalid TCK when protocol <> T=0 - Warm Reset - Wrong TCK (EMV96)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.001.00	1CF.001.00	ETU Measurement - Cold Reset	
		1CF.002.00	1CF.002.00	ETU Measurement - Warm Reset	
		1CF.003.00	1CF.003.00	Min. Interval between char. sent in the same direction respected	
	X	1CF.004.00	1CF.004.00(40)	Min. Interval between char. sent in same opposite direction respected EMV2000)	
X			1CF.004.00(311)	Min. Interval between char. sent in same opposite direction respected (EMV96)	
		1CF.005.0y	1CF.005.00	Max. Interval between char. sent in the same direction (WWT) - D=1	
	X		1CF.005.01	Max. Interval between char. sent in the same direction (WWT) - D=2	
	X		1CF.005.02	Max. Interval between char. sent in the same direction (WWT) - D=4	
X		1CF.006.0y	1CF.006.00(311)	Max. Interval between char. sent in opposite directions (WWT) - D=1	
	X		1CF.006.00(40)	Max. Interval between char. sent in opposite directions (WWT) - D=1	
	X		1CF.006.01(40)	Max. Interval between char. sent in opposite directions (WWT) - D=2	
	X		1CF.006.02(40)	Max. Interval between char. sent in opposite directions (WWT) - D=4	
		1CF.007.00	1CF.007.00	Min. Interval between char. sent in opposite directions - cold reset	
		1CF.008.00	1CF.008.00	Min. Interval between char. sent in opposite directions - warm reset	
		1CF.009.00	1CF.009.00	INS Procedure Byte - Cases 3 and 4 Commands	
		1CF.010.00	1CF.010.00	INS Procedure Byte - Cases 2 Commands	
		1CF.011.0y	1CF.011.00	INS Complement Byte - Cases 3 and 4 Commands - /INS	
			1CF.011.01	INS Complement Byte - Cases 3 and 4 Commands - /INS then INS	
		1CF.012.0y	1CF.012.00	INS Complement Byte - Cases 2 Command - /INS	
			1CF.012.01	INS Complement Byte - Cases 2 Command - /INS then INS	
		1CF.013.0y	1CF.013.00	'60' Procedure Byte x 1	
			1CF.013.01	'60' Procedure Byte x 5	
			1CF.013.02	'60' Procedure Byte x 10	
		1CF.014.0y	1CF.014.00	'60' amid INS Complement Procedure Bytes - transmitting	
			1CF.014.01	'60' amid INS Complement Procedure Bytes - receiving	
		1CF.015.00	1CF.015.00	'61' Procedure Byte - Cases 2 Command	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.016.xy	1CF.016.00	Status Bytes '6x' or '9x' - 62XX	
			1CF.016.01	Status Bytes '6x' or '9x' - 63XX	
			1CF.016.02	Status Bytes '6x' or '9x' - 6700	
			1CF.016.03	Status Bytes '6x' or '9x' - 6AXX	
			1CF.016.04	Status Bytes '6x' or '9x' - 6FXX	
			1CF.016.05	Status Bytes '6x' or '9x' - 9000	
			1CF.016.06	Status Bytes '6x' or '9x' - 64XX	
			1CF.016.07	Status Bytes '6x' or '9x' - 65XX	
			1CF.016.08	Status Bytes '6x' or '9x' - 66XX	
			1CF.016.09	Status Bytes '6x' or '9x' - 68XX	
			1CF.016.10	Status Bytes '6x' or '9x' - 69XX	
			1CF.016.11	Status Bytes '6x' or '9x' - 6BXX	
			1CF.016.12	Status Bytes '6x' or '9x' - 6DXX	
			1CF.016.13	Status Bytes '6x' or '9x' - 6EXX	
			1CF.016.14	Status Bytes '6x' or '9x' - 91XX	
			1CF.016.15	Status Bytes '6x' or '9x' - 92XX	
			1CF.016.16	Status Bytes '6x' or '9x' - 93XX	
			1CF.016.17	Status Bytes '6x' or '9x' - 94XX	
			1CF.016.18	Status Bytes '6x' or '9x' - 95XX	
			1CF.016.19	Status Bytes '6x' or '9x' - 96XX	
			1CF.016.20	Status Bytes '6x' or '9x' - 97XX	
			1CF.016.21	Status Bytes '6x' or '9x' - 98XX	
			1CF.016.22	Status Bytes '6x' or '9x' - 99XX	
			1CF.016.23	Status Bytes '6x' or '9x' - 9AXX	
			1CF.016.24	Status Bytes '6x' or '9x' - 9BXX	
			1CF.016.25	Status Bytes '6x' or '9x' - 9CXX	
			1CF.016.26	Status Bytes '6x' or '9x' - 9DXX	
			1CF.016.27	Status Bytes '6x' or '9x' - 9EXX	
			1CF.016.28	Status Bytes '6x' or '9x' - 9FXX	
	X	1CF.030.0y-2	1CF.030.00-2(40)	Max. Interval between char. sent in same direction exceeded (WWT) - D=1 (EMV 2000)	
	X		1CF.030.01-2	Max. Interval between char. sent in same direction exceeded (WWT) - D=2	
	X		1CF.030.02-2	Max. Interval between char. sent in same direction exceeded (WWT) - D=4	
X			1CF.030.00-2(311)	Max. Interval between char. sent in same direction exceeded (WWT) - D=1 (EMV 96)	
	X	1CF.031.0y-2	1CF.031.00-2(40)	Max. Interval between char. sent in opposite directions exceeded (WWT) - D=1 (EMV 2000)	
	X		1CF.031.01-2	Max. Interval between char. sent in opposite directions exceeded (WWT) - D=2	
	X		1CF.031.02-2	Max. Interval between char. sent in opposite directions exceeded (WWT) - D=4	
X			1CF.031.00-2(311)	Max. Interval between char. sent in opposite directions exceeded (WWT) - D=1 (EMV 96)	
		1CF.032.0y	1CF.032.00	Unsupported Procedure Byte or Status Byte - invalid procedure byte	
			1CF.032.01	Unsupported Procedure Byte or Status Byte - invalid status byte	
		1CF.033.0y	1CF.033.00	Error Detection (Parity) - direct	
			1CF.033.01	Error Detection (Parity) - inverse	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.034.00	1CF.034.00	Multiple Error Repetitions (Receiving)	
		1CF.035.0y	1CF.035.00	Excess Error Repetitions (Receiving) - D=1	
	X		1CF.035.01	Excess Error Repetitions (Receiving) - D=2	
	X		1CF.035.02	Excess Error Repetitions (Receiving) - D=4	
		1CF.036.00	1CF.036.00	Interpretation of Repeated Character	
		1CF.037.0y	1CF.037.00	Error Correction - start=10.5, duration=1	
			1CF.037.01	Error Correction - start=10.3, duration=1	
			1CF.037.02	Error Correction - start=10.3, duration=2	
			1CF.037.03	Error Correction - start=10.7, duration=1	
			1CF.037.04	Error Correction - start=10.7, duration=2	
		1CF.038.00	1CF.038.00	Multiple Error Repetitions (Transmitting)	
		1CF.039.0y	1CF.039.00	Excess Error Repetitions (Transmitting) - D=1	
	X		1CF.039.01	Excess Error Repetitions (Transmitting) - D=2	
	X		1CF.039.02	Excess Error Repetitions (Transmitting) - D=4	
	X	1CF.051.00	1CF.051.00(40)	Minimum Interval between Characters Respected (EMV2000)	
X			1CF.051.00(311)	Minimum Interval between Characters Respected (EMV96)	
	X	1CF.052.00	1CF.052.00(40)	Character Waiting Time (CWT) Respected - CWI=1 (EMV2000)	
	X		1CF.052.01(40)	Character Waiting Time (CWT) Respected - CWI=2 (EMV2000)	
	X		1CF.052.02(40)	Character Waiting Time (CWT) Respected - CWI=3 (EMV2000)	
	X		1CF.052.03(40)	Character Waiting Time (CWT) Respected - CWI=4 (EMV2000)	
	X		1CF.052.04(40)	Character Waiting Time (CWT) Respected - CWI=5 (EMV2000)	
X			1CF.052.00(311)	Character Waiting Time (CWT) Respected - CWI=1 (EMV96)	
X			1CF.052.01(311)	Character Waiting Time (CWT) Respected - CWI=2 (EMV96)	
X			1CF.052.02(311)	Character Waiting Time (CWT) Respected - CWI=3 (EMV96)	
X			1CF.052.03(311)	Character Waiting Time (CWT) Respected - CWI=4 (EMV96)	
X			1CF.052.04(311)	Character Waiting Time (CWT) Respected - CWI=5 (EMV96)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
	X	1CF.053.xy	1CF.053.00(40)	Block Waiting Time Respected (BWT) - BWI=0, D=1 (EMV2000)	
	X		1CF.053.01(40)	Block Waiting Time Respected (BWT) - BWI=1, D=1 (EMV2000)	
	X		1CF.053.02(40)	Block Waiting Time Respected (BWT) - BWI=2, D=1 (EMV2000)	
	X		1CF.053.03(40)	Block Waiting Time Respected (BWT) - BWI=3, D=1 (EMV2000)	
	X		1CF.053.04(40)	Block Waiting Time Respected (BWT) - BWI=4, D=1 (EMV2000)	
	X		1CF.053.10(40)	Block Waiting Time Respected (BWT) - BWI=0, D=2	
	X		1CF.053.11(40)	Block Waiting Time Respected (BWT) - BWI=1, D=2	
	X		1CF.053.12(40)	Block Waiting Time Respected (BWT) - BWI=2, D=2	
	X		1CF.053.13(40)	Block Waiting Time Respected (BWT) - BWI=3, D=2	
	X		1CF.053.14(40)	Block Waiting Time Respected (BWT) - BWI=4, D=2	
	X		1CF.053.20(40)	Block Waiting Time Respected (BWT) - BWI=0, D=4	
	X		1CF.053.21(40)	Block Waiting Time Respected (BWT) - BWI=1, D=4	
	X		1CF.053.22(40)	Block Waiting Time Respected (BWT) - BWI=2, D=4	
	X		1CF.053.23(40)	Block Waiting Time Respected (BWT) - BWI=3, D=4	
	X		1CF.053.24(40)	Block Waiting Time Respected (BWT) - BWI=4, D=4	
X			1CF.053.00(311)	Block Waiting Time Respected (BWT) - BWI=0, D=1 (EMV96)	
X			1CF.053.01(311)	Block Waiting Time Respected (BWT) - BWI=1, D=1 (EMV96)	
X			1CF.053.02(311)	Block Waiting Time Respected (BWT) - BWI=2, D=1 (EMV96)	
X			1CF.053.03(311)	Block Waiting Time Respected (BWT) - BWI=3, D=1 (EMV96)	
X			1CF.053.04(311)	Block Waiting Time Respected (BWT) - BWI=4, D=1 (EMV96)	
	X	1CF.054.00	1CF.054.00(40)	Block Guard Time (BGT) Respected (EMV2000)	
X			1CF.054.00(311)	Block Guard Time (BGT) Respected (EMV96)	
		1CF.055.00	1CF.055.00	Block Guard Time (BGT) Respected by the IUT after Cold Reset	
		1CF.056.00	1CF.056.00	Block Guard Time (BGT) Respected by the IUT after Warm Reset	
		1CF.057.0y	1CF.057.00	Chained Blocks - Waiting Time Extension (WTX) Respected - D=1	
			1CF.057.01(40)	Chained Blocks - Waiting Time Extension (WTX) Respected - D=2	
			1CF.057.02(40)	Chained Blocks - Waiting Time Extension (WTX) Respected - D=3	
		1CF.058.00	1CF.058.00	Sequence Number of the First I-block	
		1CF.059.00	1CF.059.00	Valid Exchanges of I-Blocks	
		1CF.060.00	1CF.060.00	Chaining - IUT Receiving	
		1CF.061.00	1CF.061.00	Chaining - Sequence Number of the R-blocks	
		1CF.062.00	1CF.062.00	Chaining in both directions	
		1CF.063.0y	1CF.063.00	IUT Sends Chained and non Chained Blocks - Respect of IFSI by the IUT (10)	
			1CF.063.01	IUT Sends Chained and non Chained Blocks - Respect of IFSI by the IUT (20)	
			1CF.063.02	IUT Sends Chained and non Chained Blocks - Respect of IFSI by the IUT (40)	
			1CF.063.03	IUT Sends Chained and non Chained Blocks - Respect of IFSI by the IUT (60)	
			1CF.063.04	IUT Sends Chained and non Chained Blocks - Respect of IFSI by the IUT (80)	
			1CF.063.05	IUT Sends Chained and non Chained Blocks - Respect of IFSI by the IUT (A0)	
			1CF.063.06	IUT Sends Chained and non Chained Blocks - Respect of IFSI by the IUT (C0)	
			1CF.063.07	IUT Sends Chained and non Chained Blocks - Respect of IFSI by the IUT (E0)	
			1CF.063.08	IUT Sends Chained and non Chained Blocks - Respect of IFSI by the IUT (FE)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.064.0y	1CF.064.00	Chaining or Not - Repeated Requests to Change IFSC Between Two chains (16)	
			1CF.064.01	Chaining or Not - Repeated Requests to Change IFSC Between Two chains (150)	
			1CF.064.02	Chaining or Not - Repeated Requests to Change IFSC Between Two chains (260)	
		1CF.080.xy-1	1CF.080.00-1(D)	Character Waiting Time (CWT) Exceeded - I-Block - CWI=1 (deactivation)	
			1CF.080.01-1(D)	Character Waiting Time (CWT) Exceeded - I-Block - CWI=2 (deactivation)	
			1CF.080.02-1(D)	Character Waiting Time (CWT) Exceeded - I-Block - CWI=3 (deactivation)	
			1CF.080.03-1(D)	Character Waiting Time (CWT) Exceeded - I-Block - CWI=4 (deactivation)	
			1CF.080.04-1(D)	Character Waiting Time (CWT) Exceeded - I-Block - CWI=5 (deactivation)	
			1CF.080.10-1(D)	Character Waiting Time (CWT) Exceeded - S-Request Block - CWI=1 (deactivation)	
			1CF.080.11-1(D)	Character Waiting Time (CWT) Exceeded - S-Request Block - CWI=2 (deactivation)	
			1CF.080.12-1(D)	Character Waiting Time (CWT) Exceeded - S-Request Block - CWI=3 (deactivation)	
			1CF.080.13-1(D)	Character Waiting Time (CWT) Exceeded - S-Request Block - CWI=4 (deactivation)	
			1CF.080.14-1(D)	Character Waiting Time (CWT) Exceeded - S-Request Block - CWI=5 (deactivation)	
			1CF.080.00-1(nD)	Character Waiting Time (CWT) Exceeded - I-Block - CWI=1 (R-block)	
			1CF.080.01-1(nD)	Character Waiting Time (CWT) Exceeded - I-Block - CWI=2 (R-block)	
			1CF.080.02-1(nD)	Character Waiting Time (CWT) Exceeded - I-Block - CWI=3 (R-block)	
			1CF.080.03-1(nD)	Character Waiting Time (CWT) Exceeded - I-Block - CWI=4 (R-block)	
			1CF.080.04-1(nD)	Character Waiting Time (CWT) Exceeded - I-Block - CWI=5 (R-block)	
			1CF.080.10-1(nD)	Character Waiting Time (CWT) Exceeded - S-Request Block - CWI=1 (R-block)	
			1CF.080.11-1(nD)	Character Waiting Time (CWT) Exceeded - S-Request Block - CWI=2 (R-block)	
			1CF.080.12-1(nD)	Character Waiting Time (CWT) Exceeded - S-Request Block - CWI=3 (R-block)	
			1CF.080.13-1(nD)	Character Waiting Time (CWT) Exceeded - S-Request Block - CWI=4 (R-block)	
			1CF.080.14-1(nD)	Character Waiting Time (CWT) Exceeded - S-Request Block - CWI=5 (R-block)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.081.xy	1CF.081.00-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=1, BWI=0 (Deactivation expected)	
			1CF.081.01-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=1, BWI=1 (Deactivation expected)	
			1CF.081.02-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=1, BWI=2 (Deactivation expected)	
			1CF.081.03-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=1, BWI=3 (Deactivation expected)	
			1CF.081.04-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=1, BWI=4 (Deactivation expected)	
	X		1CF.081.10-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=2, BWI=0 (Deactivation expected)	
	X		1CF.081.11-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=2, BWI=1 (Deactivation expected)	
	X		1CF.081.12-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=2, BWI=2 (Deactivation expected)	
	X		1CF.081.13-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=2, BWI=3 (Deactivation expected)	
	X		1CF.081.14-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=2, BWI=4 (Deactivation expected)	
	X		1CF.081.20-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=4, BWI=0 (Deactivation expected)	
	X		1CF.081.21-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=4, BWI=1 (Deactivation expected)	
	X		1CF.081.22-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=4, BWI=2 (Deactivation expected)	
	X		1CF.081.23-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=4, BWI=3 (Deactivation expected)	
	X		1CF.081.24-2(D)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=4, BWI=4 (Deactivation expected)	
			1CF.081.00-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=1, BWI=0 (S-block expected)	
			1CF.081.01-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=1, BWI=1 (S-block expected)	
			1CF.081.02-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=1, BWI=2 (S-block expected)	
			1CF.081.03-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=1, BWI=3 (S-block expected)	
			1CF.081.04-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=1, BWI=4 (S-block expected)	
	X		1CF.081.10-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=2, BWI=0 (S-block expected)	
	X		1CF.081.11-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=2, BWI=1 (S-block expected)	
	X		1CF.081.12-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=2, BWI=2 (S-block expected)	
	X		1CF.081.13-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=2, BWI=3 (S-block expected)	
	X		1CF.081.14-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=2, BWI=4 (S-block expected)	
	X		1CF.081.20-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=4, BWI=0 (S-block expected)	
	X		1CF.081.21-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=4, BWI=1 (S-block expected)	
	X		1CF.081.22-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=4, BWI=2 (S-block expected)	
	X		1CF.081.23-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=4, BWI=3 (S-block expected)	
	X		1CF.081.24-1(nD)	Block Waiting Time (BWT) Exceeded - S-Request Block - D=4, BWI=4 (S-block expected)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.081.xy bis	1CF.081.00-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=1, BWI=0 (Deactivation expected)	
			1CF.081.01-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=1, BWI=1 (Deactivation expected)	
			1CF.081.02-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=1, BWI=2 (Deactivation expected)	
			1CF.081.03-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=1, BWI=3 (Deactivation expected)	
			1CF.081.04-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=1, BWI=4 (Deactivation expected)	
	X		1CF.081.10-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=2, BWI=0 (Deactivation expected)	
	X		1CF.081.11-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=2, BWI=1 (Deactivation expected)	
	X		1CF.081.12-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=2, BWI=2 (Deactivation expected)	
	X		1CF.081.13-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=2, BWI=3 (Deactivation expected)	
	X		1CF.081.14-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=2, BWI=4 (Deactivation expected)	
	X		1CF.081.20-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=4, BWI=0 (Deactivation expected)	
	X		1CF.081.21-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=4, BWI=1 (Deactivation expected)	
	X		1CF.081.22-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=4, BWI=2 (Deactivation expected)	
	X		1CF.081.23-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=4, BWI=3 (Deactivation expected)	
	X		1CF.081.24-2 bis(D)	Block Waiting Time (BWT) Exceeded - I-Block - D=4, BWI=4 (Deactivation expected)	
			1CF.081.00-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=1, BWI=0 (S-block expected)	
			1CF.081.01-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=1, BWI=1 (S-block expected)	
			1CF.081.02-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=1, BWI=2 (S-block expected)	
			1CF.081.03-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=1, BWI=3 (S-block expected)	
			1CF.081.04-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=1, BWI=4 (S-block expected)	
	X		1CF.081.10-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=2, BWI=0 (S-block expected)	
	X		1CF.081.11-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=2, BWI=1 (S-block expected)	
	X		1CF.081.12-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=2, BWI=2 (S-block expected)	
	X		1CF.081.13-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=2, BWI=3 (S-block expected)	
	X		1CF.081.14-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=2, BWI=4 (S-block expected)	
	X		1CF.081.20-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=4, BWI=0 (S-block expected)	
	X		1CF.081.21-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=4, BWI=1 (S-block expected)	
	X		1CF.081.22-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=4, BWI=2 (S-block expected)	
	X		1CF.081.23-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=4, BWI=3 (S-block expected)	
	X		1CF.081.24-1 bis(nD)	Block Waiting Time (BWT) Exceeded - I-Block - D=4, BWI=4 (S-block expected)	
		1CF.082.0y	1CF.082.00(D)	Non Chained Blocks - Proper then Improper Use of WTX - D=1 (Deactivation)	
	X		1CF.082.01(D)	Non Chained Blocks - Proper then Improper Use of WTX - D=2 (Deactivation)	
	X		1CF.082.02(D)	Non Chained Blocks - Proper then Improper Use of WTX - D=4 (Deactivation)	
			1CF.082.00(nD)	Non Chained Blocks - Proper then Improper Use of WTX - D=1 (R-Block)	
	X		1CF.082.01(nD)	Non Chained Blocks - Proper then Improper Use of WTX - D=2 (R-Block)	
	X		1CF.082.02(nD)	Non Chained Blocks - Proper then Improper Use of WTX - D=4 (R-Block)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.083.xy-1	1CF.083.00(D)	Waiting Time Extension (WTX) Exceeded - D=1, BWI=0 (Deactivation)	
			1CF.083.01(D)	Waiting Time Extension (WTX) Exceeded - D=1, BWI=1 (Deactivation)	
			1CF.083.02(D)	Waiting Time Extension (WTX) Exceeded - D=1, BWI=2 (Deactivation)	
			1CF.083.03(D)	Waiting Time Extension (WTX) Exceeded - D=1, BWI=3 (Deactivation)	
			1CF.083.04(D)	Waiting Time Extension (WTX) Exceeded - D=1, BWI=4 (Deactivation)	
	X		1CF.083.10(D)	Waiting Time Extension (WTX) Exceeded - D=2, BWI=0 (Deactivation)	
	X		1CF.083.11(D)	Waiting Time Extension (WTX) Exceeded - D=2, BWI=1 (Deactivation)	
	X		1CF.083.12(D)	Waiting Time Extension (WTX) Exceeded - D=2, BWI=2 (Deactivation)	
	X		1CF.083.13(D)	Waiting Time Extension (WTX) Exceeded - D=2, BWI=3 (Deactivation)	
	X		1CF.083.14(D)	Waiting Time Extension (WTX) Exceeded - D=2, BWI=4 (Deactivation)	
	X		1CF.083.20(D)	Waiting Time Extension (WTX) Exceeded - D=4, BWI=0 (Deactivation)	
	X		1CF.083.21(D)	Waiting Time Extension (WTX) Exceeded - D=4, BWI=1 (Deactivation)	
	X		1CF.083.22(D)	Waiting Time Extension (WTX) Exceeded - D=4, BWI=2 (Deactivation)	
	X		1CF.083.23(D)	Waiting Time Extension (WTX) Exceeded - D=4, BWI=3 (Deactivation)	
	X		1CF.083.24(D)	Waiting Time Extension (WTX) Exceeded - D=4, BWI=4 (Deactivation)	
			1CF.083.00(nD)	Waiting Time Extension (WTX) Exceeded - D=1, BWI=0 (R-Block)	
			1CF.083.01(nD)	Waiting Time Extension (WTX) Exceeded - D=1, BWI=1 (R-Block)	
			1CF.083.02(nD)	Waiting Time Extension (WTX) Exceeded - D=1, BWI=2 (R-Block)	
			1CF.083.03(nD)	Waiting Time Extension (WTX) Exceeded - D=1, BWI=3 (R-Block)	
			1CF.083.04(nD)	Waiting Time Extension (WTX) Exceeded - D=1, BWI=4 (R-Block)	
	X		1CF.083.10(nD)	Waiting Time Extension (WTX) Exceeded - D=2, BWI=0 (R-Block)	
	X		1CF.083.11(nD)	Waiting Time Extension (WTX) Exceeded - D=2, BWI=1 (R-Block)	
	X		1CF.083.12(nD)	Waiting Time Extension (WTX) Exceeded - D=2, BWI=2 (R-Block)	
	X		1CF.083.13(nD)	Waiting Time Extension (WTX) Exceeded - D=2, BWI=3 (R-Block)	
	X		1CF.083.14(nD)	Waiting Time Extension (WTX) Exceeded - D=2, BWI=4 (R-Block)	
	X		1CF.083.20(nD)	Waiting Time Extension (WTX) Exceeded - D=4, BWI=0 (R-Block)	
	X		1CF.083.21(nD)	Waiting Time Extension (WTX) Exceeded - D=4, BWI=1 (R-Block)	
	X		1CF.083.22(nD)	Waiting Time Extension (WTX) Exceeded - D=4, BWI=2 (R-Block)	
	X		1CF.083.23(nD)	Waiting Time Extension (WTX) Exceeded - D=4, BWI=3 (R-Block)	
	X		1CF.083.24(nD)	Waiting Time Extension (WTX) Exceeded - D=4, BWI=4 (R-Block)	
		1CF.084.0y	1CF.084.00	Non Chained Blocks - TX Error followed by an Error Notification on an I-Block – parity	
			1CF.084.01	Non Chained Blocks - TX Error followed by an Error Notification on an I-Block - EDC	
			1CF.084.02	Non Chained Blocks - TX Error followed by an Error Notification on an I-Block - parity + EDC	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.085.xy	1CF.085.00	NCB - Syntax error in response to an I-Block, error notification, I-Block - NAD	
			1CF.085.01	NCB - Syntax error in response to an I-Block, error notification, I-Block - LEN≠0	
			1CF.085.02(D)	NCB - Syntax error in response to an I-Block, error notification, I-Block - LEN=FF (Deactivation expected)	
			1CF.085.02(nD)	NCB - Syntax error in response to an I-Block, error notification, I-Block - LEN=FF	
			1CF.085.03	NCB - Syntax error in response to an I-Block, error notification, I-Block - INF=LEN	
			1CF.085.04	NCB - Syntax error in response to an I-Block, error notification, I-Block - INF<10	
			1CF.085.05	NCB - Syntax error in response to an I-Block, error notification, I-Block - INF=FF	
			1CF.085.06	NCB - Syntax error in response to an I-Block, error notification, I-Block - b6=1	
			1CF.085.07	NCB - Syntax error in response to an I-Block, error notification, I-Block - R seq. number	
			1CF.085.08	NCB - Syntax error in response to an I-Block, error notification, I-Block - I seq. number	
			1CF.085.09	NCB - Syntax error in response to an I-Block, error notification, I-Block - S(ABORT)	
			1CF.085.10	NCB - Syntax error in response to an I-Block, error notification, I-Block - S(WTX)	
			1CF.085.11	NCB - Syntax error in response to an I-Block, error notification, I-Block - S(IFS)	
			1CF.085.12	NCB - Syntax error in response to an I-Block, error notification, I-Block - S(RESYNC)	
			1CF.085.13	NCB - Syntax error in response to an I-Block, error notification, I-Block – Unknown	
	X	1CF.086.0y	1CF.086.00(40)	NCB - Excess of Transmission Errors in Response to an I-Block – parity (EMV2000)	
	X		1CF.086.01(40)	NCB - Excess of Transmission Errors in Response to an I-Block – EDC (EMV2000)	
	X		1CF.086.02(40)	NCB - Excess of Transmission Errors in Response to an I-Block - parity+EDC (EMV2000)	
X			1CF.086.00(311)	NCB - Excess of Transmission Errors in Response to an I-Block – parity (EMV96)	
X			1CF.086.01(311)	NCB - Excess of Transmission Errors in Response to an I-Block - EDC (EMV96)	
X			1CF.086.02(311)	NCB - Excess of Transmission Errors in Response to an I-Block - parity+EDC (EMV96)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
	X	1CF.087.xy	1CF.087.00	NCB - Excess of Syntax Errors in Response to an I-Block – NAD	
	X		1CF.087.01(40)	NCB - Excess of Syntax Errors in Response to an I-Block - LEN≠0 (EMV2000)	
	X		1CF.087.02(40)	NCB - Excess of Syntax Errors in Response to an I-Block - LEN=FF (EMV2000)	
	X		1CF.087.03(40)	NCB - Excess of Syntax Errors in Response to an I-Block - INF=LEN (EMV2000)	
	X		1CF.087.04(40)	NCB - Excess of Syntax Errors in Response to an I-Block - INF<10 (EMV2000)	
	X		1CF.087.05(40)	NCB - Excess of Syntax Errors in Response to an I-Block - INF=FF (EMV2000)	
	X		1CF.087.06(40)	NCB - Excess of Syntax Errors in Response to an I-Block - b6=1 (EMV2000)	
	X		1CF.087.08(40)	NCB - Excess of Syntax Errors in Response to an I-Block - I seq. Number (EMV2000)	
	X		1CF.087.09(40)	NCB - Excess of Syntax Errors in Response to an I-Block - S(ABORT) (EMV2000)	
	X		1CF.087.10(40)	NCB - Excess of Syntax Errors in Response to an I-Block - S(WTX) (EMV2000)	
	X		1CF.087.11(40)	NCB - Excess of Syntax Errors in Response to an I-Block - S(IFS) (EMV2000)	
	X		1CF.087.12(40)	NCB - Excess of Syntax Errors in Response to an I-Block - S(RESYNC) (EMV2000)	
	X		1CF.087.13(40)	NCB - Excess of Syntax Errors in Response to an I-Block – Unknown (EMV2000)	
X			1CF.087.00(311)	NCB - Excess of Syntax Errors in Response to an I-Block – NAD (EMV96)	
X			1CF.087.01(311)	NCB - Excess of Syntax Errors in Response to an I-Block – LEN ≠0 (EMV96)	
X			1CF.087.02(311)	NCB - Excess of Syntax Errors in Response to an I-Block - LEN=FF (EMV96)	
X			1CF.087.03(311)	NCB - Excess of Syntax Errors in Response to an I-Block - INF=LEN (EMV96)	
X			1CF.087.04(311)	NCB - Excess of Syntax Errors in Response to an I-Block - INF<10 (EMV96)	
X			1CF.087.05(311)	NCB - Excess of Syntax Errors in Response to an I-Block - INF=FF (EMV96)	
X			1CF.087.06(311)	NCB - Excess of Syntax Errors in Response to an I-Block - b6=1 (EMV96)	
X			1CF.087.08(311)	NCB - Excess of Syntax Errors in Response to an I-Block - I seq. Number (EMV96)	
X			1CF.087.09(311)	NCB - Excess of Syntax Errors in Response to an I-Block - S(ABORT) (EMV96)	
X			1CF.087.00(311)	NCB - Excess of Syntax Errors in Response to an I-Block - S(WTX) (EMV96)	
X			1CF.087.01(311)	NCB - Excess of Syntax Errors in Response to an I-Block - S(IFS) (EMV96)	
X			1CF.087.02(311)	NCB - Excess of Syntax Errors in Response to an I-Block - S(RESYNC) (EMV96)	
X			1CF.087.03(311)	NCB - Excess of Syntax Errors in Response to an I-Block – Unknown (EMV96)	
		1CF.088.xy	1CF.088.00	NCB - One or two consecutive TX errors in response to an I-Block, I-Block - 1 parity	
			1CF.088.01	NCB - One or two consecutive TX errors in response to an I-Block, I-Block - 1 EDC	
			1CF.088.02	NCB - One or two consecutive TX errors in response to an I-Block, I-Block - 1 parity+EDC	
			1CF.088.10	NCB - One or two consecutive TX errors in response to an I-Block, I-Block - 2 parity	
			1CF.088.11	NCB - One or two consecutive TX errors in response to an I-Block, I-Block - 2 EDC	
			1CF.088.12	NCB - One or two consecutive TX errors in response to an I-Block, I-Block - 2 parity+EDC	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.089.xy	1CF.089.00	NCB - Syntax Error in Response to an I-Block, Then I-Block – NAD	
			1CF.089.01	NCB - Syntax Error in Response to an I-Block, Then I-Block – LEN≠0	
			1CF.089.02	NCB - Syntax Error in Response to an I-Block, Then I-Block – LEN=FF	
			1CF.089.03	NCB - Syntax Error in Response to an I-Block, Then I-Block – INF=LEN	
			1CF.089.04	NCB - Syntax Error in Response to an I-Block, Then I-Block - INF<10	
			1CF.089.05	NCB - Syntax Error in Response to an I-Block, Then I-Block - INF=FF	
			1CF.089.06	NCB - Syntax Error in Response to an I-Block, Then I-Block - b6=1	
			1CF.089.07	NCB - Syntax Error in Response to an I-Block, Then I-Block - R seq. number	
			1CF.089.08	NCB - Syntax Error in Response to an I-Block, Then I-Block - I seq. Number	
			1CF.089.09	NCB - Syntax Error in Response to an I-Block, Then I-Block - S(ABORT)	
			1CF.089.10	NCB - Syntax Error in Response to an I-Block, Then I-Block - S(WTX)	
			1CF.089.11	NCB - Syntax Error in Response to an I-Block, Then I-Block - S(IFS)	
			1CF.089.12	NCB - Syntax Error in Response to an I-Block, Then I-Block - S(RESYNC)	
			1CF.089.13	NCB - Syntax Error in Response to an I-Block, Then I-Block – Unknown	
		1CF.090.xy	1CF.090.00	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - NAD	
			1CF.090.01	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - LEN≠0	
			1CF.090.02	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - LEN =FF	
			1CF.090.03	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - INF=LEN	
			1CF.090.04	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - INF<10	
			1CF.090.05	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - INF=FF	
			1CF.090.06	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - b6=1	
			1CF.090.08	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - I seq. Number	
			1CF.090.09	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - S(ABORT)	
			1CF.090.10	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - S(WTX)	
			1CF.090.11	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - S(IFS)	
			1CF.090.12	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block - S(RESYNC)	
			1CF.090.13	NCB - Two Consecutive Syntax Errors in Response to an I-Block, Then I-Block – Unknown	
		1CF.091.0y	1CF.091.00	NCB - TX error in response to an I-Block, error notification on R-Block, I-Block – parity	
			1CF.091.01	NCB - TX error in response to an I-Block, error notification on R-Block, I-Block – EDC	
			1CF.091.02	NCB - TX error in response to an I-Block, error notification on R-Block, I-Block - parity+EDC	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.092.xy	1CF.092.00	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block – NAD	
			1CF.092.01	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block – LEN≠0	
			1CF.092.02	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block – LEN=FF	
			1CF.092.03	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block – INF=LEN	
			1CF.092.04	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block – INF<10	
			1CF.092.05	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block - INF=FF	
			1CF.092.06	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block - b6=1	
			1CF.092.07	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block - R seq. Number	
			1CF.092.08	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block - I seq. Number	
			1CF.092.09	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block - S(ABORT)	
			1CF.092.10	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block - S(WTX)	
			1CF.092.11	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block - S(IFS)	
			1CF.092.12	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block - S(RESYNC)	
			1CF.092.13	NCB - Syntax error in response to an I-Block, error notification on R-Block, I-Block – Unknown	
		1CF.093.00	1CF.093.00	NCB - Two consecutive TX errors in response to an I-Block then error notification - parity+EDC	
			1CF.093.01	NCB - Two consecutive TX errors in response to an I-Block then error notification - EDC+parity	
			1CF.093.02	NCB - Two consecutive TX errors in response to an I-Block then error notification - EDC+combination	
		1CF.094.00	1CF.094.00	NCB - Two consecutive syntax error in response to an I-Block, then error notification – NAD + LEN≠00	
			1CF.094.01	NCB – Two consecutive syntax error in response to an I-Block, then error notification – LEN≠01 + INF<10	
			1CF.094.02	NCB – Two consecutive syntax error in response to an I-Block, then error notification – INF=FF + b6=1	
			1CF.094.03	NCB – Two consecutive syntax error in response to an I-Block, then error notification - R seq num + I seq num	
			1CF.094.04	NCB – Two consecutive syntax error in response to an I-Block, then error notification - S(WTX)+ S(IFS)	
			1CF.094.05	NCB – Two consecutive syntax error in response to an I-Block, then error notification - S(RESYNC) + S(ABORT)	
			1CF.094.06	NCB – Two consecutive syntax error in response to an I-Block, then error notification – Unknown+ LEN=FF	
		1CF.095.0y	1CF.095.00	NCB - TX error in response to an S(IFS Response) then I-Block – parity	
			1CF.095.01	NCB - TX error in response to an S(IFS Response) then I-Block – EDC	
			1CF.095.02	NCB - TX error in response to an S(IFS Response) then I-Block - parity+EDC	
		1CF.096.xy	1CF.096.00	NCB - Syntax error in response to an S(IFS Response) then I-Block – NAD	
			1CF.096.01	NCB - Syntax error in response to an S(IFS Response) then I-Block - LEN=FF	
			1CF.096.02	NCB - Syntax error in response to an S(IFS Response) then I-Block - I seq. Number	
			1CF.096.03	NCB - Syntax error in response to an S(IFS Response) then I-Block - INF=LEN	
			1CF.096.04	NCB - Syntax error in response to an S(IFS Response) then I-Block - INF<10	
			1CF.096.05	NCB - Syntax error in response to an S(IFS Response) then I-Block - INF=FF	
			1CF.096.06	NCB - Syntax error in response to an S(IFS Response) then I-Block - R-block	
			1CF.096.07	NCB - Syntax error in response to an S(IFS Response) then I-Block - S(WTX)	
			1CF.096.08	NCB - Syntax error in response to an S(IFS Response) then I-Block - S(IFS)	
			1CF.096.09	NCB - Syntax error in response to an S(IFS Response) then I-Block - S(RESYNC)	
			1CF.096.10	NCB - Syntax error in response to an S(IFS Response) then I-Block - S(ABORT)	
			1CF.096.11	NCB - Syntax error in response to an S(IFS Response) then I-Block – Unknown	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.097.0y	1CF.097.00	NCB - TX error in response to an S(IFS Response) then error notification then I-Block – parity	
			1CF.097.01	NCB - TX error in response to an S(IFS Response) then error notification then I-Block – EDC	
			1CF.097.02	NCB - TX error in response to an S(IFS Response) then error notification then I-Block – parity+EDC	
		1CF.098.xy	1CF.098.00	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block – NAD	
			1CF.098.01	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block – LEN=FF	
			1CF.098.02	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block - I seq. Number	
			1CF.098.03	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block – INF=LEN	
			1CF.098.04	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block – INF<10	
			1CF.098.05	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block – INF=FF	
			1CF.098.06	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block - R-block	
			1CF.098.07	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block - S(WTX)	
			1CF.098.08	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block - S(IFS)	
			1CF.098.09	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block - S(RESYNC)	
			1CF.098.10	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block - S(ABORT)	
			1CF.098.11	NCB - Syntax error in response to an S(IFS Response) Block, error notification, I-Block – Unknown	
		1CF.099.00	1CF.099.00	NCB - Error notification on an I-Block then I-Block	
		1CF.100.00	1CF.100.00	NCB - Two consecutive error notifications on an I-Block then I-Block	
	X	1CF.101.00	1CF.101.00(40)	NCB - Excess of Error Notifications on I-Blocks (EMV2000)	
X			1CF.101.00(311)	NCB - Excess of Error Notifications on I-Blocks (EMV96)	
		1CF.102.0y	1CF.102.00	NCB – TX Error in Response to an I-Block, then S(IFS Request) - parity	
			1CF.102.01	NCB – TX Error in Response to an I-Block, then S(IFS Request) - EDC	
			1CF.102.02	NCB – TX Error in Response to an I-Block, then S(IFS Request) - parity+EDC	
		1CF.103.xy	1CF.103.00	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – NAD	
			1CF.103.01	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – LEN=FF	
			1CF.103.02	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – LEN≠0	
			1CF.103.03	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – INF=LEN	
			1CF.103.04	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – INF<10	
			1CF.103.05	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – INF=FF	
			1CF.103.06	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – I seq. Number	
			1CF.103.07	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – R seq. Number	
			1CF.103.08	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – b6=1	
			1CF.103.09	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – Unknown	
			1CF.103.10	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – S(WTX)	
			1CF.103.11	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – S(IFS)	
			1CF.103.12	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – S(ABORT)	
			1CF.103.13	NCB – Syntax Error in Response to an I-Block then S(IFS Request) – S(RESYNC)	
		1CF.104.00	1CF.104.00	NCB – Error Notification on S(IFS Request) Then S(IFS Response)	
		1CF.105.0y	1CF.105.00	NCB – TX Error in Response to an S(IFS Request) Then S(IFS Response) – parity	
			1CF.105.01	NCB – TX Error in Response to an S(IFS Request) Then S(IFS Response) – EDC	
			1CF.105.02	NCB – TX Error in Response to an S(IFS Request) Then S(IFS Response) – parity+EDC	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.106.xy	1CF.106.00	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – NAD	
			1CF.106.01	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – LEN≠00	
			1CF.106.02	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – LEN≠01	
			1CF.106.03	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – INF≠FE	
			1CF.106.04	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – R seq num	
			1CF.106.05	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – b6=1	
			1CF.106.06	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – any I-block	
			1CF.106.07	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – unknown	
			1CF.106.08	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – S(WTX)	
			1CF.106.09	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – S(IFS)	
			1CF.106.10	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – S(ABORT)	
			1CF.106.11	NCB – Syntax Error in Response to an S(IFS Request) then S(IFS Response) – S(RESYNC)	
		1CF.107.00	1CF.107.00	NCB – Error Notification on S(IFS Response) Then I-Block	
		1CF.108.xy	1CF.108.00	NCB – One TX Errors in Response to an I-Block, then S(WTX Request) – parity	
			1CF.108.01	NCB – One TX Errors in Response to an I-Block, then S(WTX Request) – EDC	
			1CF.108.02	NCB – One TX Errors in Response to an I-Block, then S(WTX Request) – parity+EDC	
			1CF.108.10	NCB – Two TX Errors in Response to an I-Block, then S(WTX Request) – parity	
			1CF.108.11	NCB – Two TX Errors in Response to an I-Block, then S(WTX Request) – EDC	
			1CF.108.12	NCB – Two TX Errors in Response to an I-Block, then S(WTX Request) – parity+EDC	
		1CF.109.00	1CF.109.00	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) – NAD	
			1CF.109.01	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - LEN≠0	
			1CF.109.02	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - LEN=FF	
			1CF.109.03	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - LEN≠01	
			1CF.109.04	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - INF<10	
			1CF.109.05	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - INF=FF	
			1CF.109.06	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - b6=1	
			1CF.109.07	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - R-block seq num	
			1CF.109.08	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - I-block seq num	
			1CF.109.09	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - S(WTX)	
			1CF.109.10	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - S(IFS)	
			1CF.109.11	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - S(ABORT)	
			1CF.109.12	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) - S(RESYNC)	
			1CF.109.13	NCB - Syntax Error in Response to an I-Block, then S(WTX Request) – Unknown	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.110.xy	1CF.110.00	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - NAD	
			1CF.110.01	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - LEN≠0	
			1CF.110.02	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - LEN=FF	
			1CF.110.03	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - LEN≠01	
			1CF.110.04	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - INF<10	
			1CF.110.05	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - INF=FF	
			1CF.110.06	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - b6=1	
			1CF.110.08	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - R-block seq num	
			1CF.110.09	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - I-block seq num	
			1CF.110.10	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - S(WTX)	
			1CF.110.11	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - S(IFS)	
			1CF.110.12	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - S(ABORT)	
			1CF.110.13	NCB - Two Successive Syntax Errors in Response to an I-Block Then S(WTX request) - S(RESYNC)	
		1CF.111.00	1CF.111.00	NCB - Error Notification on S(WTX response)	
		1CF.112.0y	1CF.112.00	IUT Chaining - TX Error in Response to an I-Block – parity	
			1CF.112.01	IUT Chaining - TX Error in Response to an I-Block – EDC	
			1CF.112.02	IUT Chaining - TX Error in Response to an I-Block - parity+EDC	
		1CF.113.xy	1CF.113.00	IUT Chaining - Syntax Errors in Response to an I-Block – NAD	
			1CF.113.01	IUT Chaining - Syntax Errors in Response to an I-Block - LEN≠0	
			1CF.113.02	IUT Chaining - Syntax Errors in Response to an I-Block - b6=1	
			1CF.113.03	IUT Chaining - Syntax Errors in Response to an I-Block - LEN≠01	
			1CF.113.04	IUT Chaining - Syntax Errors in Response to an I-Block - INF<10	
			1CF.113.05	IUT Chaining - Syntax Errors in Response to an I-Block - INF=FF	
			1CF.113.06	IUT Chaining - Syntax Errors in Response to an I-Block - S(ABORT)	
			1CF.113.07	IUT Chaining - Syntax Errors in Response to an I-Block - S(WTX)	
			1CF.113.08	IUT Chaining - Syntax Errors in Response to an I-Block - S(IFS)	
			1CF.113.09	IUT Chaining - Syntax Errors in Response to an I-Block - S(RESYNC)	
			1CF.113.10	IUT Chaining - Syntax Errors in Response to an I-Block – Unknown	
			1CF.113.11	IUT Chaining - Syntax Errors in Response to an I-Block - I-block	
	X	1CF.114.0y	1CF.114.00(40)	IUT Chaining – Excess of TX Errors in Response to an I-Block – parity (EMV2000)	
	X		1CF.114.01(40)	IUT Chaining – Excess of TX Errors in Response to an I-Block – EDC (EMV2000)	
	X		1CF.114.02(40)	IUT Chaining – Excess of TX Errors in Response to an I-Block – parity+EDC (EMV2000)	
X			1CF.114.00(311)	IUT Chaining – Excess of TX Errors in Response to an I-Block – parity (EMV96)	
X			1CF.114.01(311)	IUT Chaining – Excess of TX Errors in Response to an I-Block – EDC (EMV96)	
X			1CF.114.02(311)	IUT Chaining – Excess of TX Errors in Response to an I-Block – parity+EDC (EMV96)	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
	X	1CF.115.xy	1CF.115.00(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – NAD (EMV2000)	
	X		1CF.115.01(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – LEN≠0 (EMV2000)	
	X		1CF.115.02(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – b6=1 (EMV2000)	
	X		1CF.115.03(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – LEN≠01 (EMV2000)	
	X		1CF.115.04(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – INF<10 (EMV2000)	
	X		1CF.115.05(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – INF=FF (EMV2000)	
	X		1CF.115.06(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – S(ABORT) (EMV2000)	
	X		1CF.115.07(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – S(WTX) (EMV2000)	
	X		1CF.115.08(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – S(IFS) (EMV2000)	
	X		1CF.115.09(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block –S(RESYNC) (EMV2000)	
	X		1CF.115.10(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – Unknown (EMV2000)	
	X		1CF.115.11(40)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – I-block (EMV2000)	
X			1CF.115.00(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – NAD (EMV96)	
X			1CF.115.01(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – LEN≠0 (EMV96)	
X			1CF.115.02(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – b6=1 (EMV96)	
X			1CF.115.03(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – LEN≠01 (EMV96)	
X			1CF.115.04(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – INF<10 (EMV96)	
X			1CF.115.05(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – INF=FF (EMV96)	
X			1CF.115.06(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – S(ABORT) (EMV96)	
X			1CF.115.07(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – S(WTX) (EMV96)	
X			1CF.115.08(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – S(IFS) (EMV96)	
X			1CF.115.09(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block –S(RESYNC) (EMV96)	
X			1CF.115.10(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – Unknown (EMV96)	
X			1CF.115.11(311)	IUT Chaining – Excess of Syntax Errors in Response to an I-Block – I-block (EMV96)	
		1CF.116.00	1CF.116.00	IUT Chaining – Excess of Error Notifications on I-Block	
		1CF.117.0y	1CF.117.00	IUT Chaining – Reception of an S(ABORT request) – D=1	
			1CF.117.01	IUT Chaining – Reception of an S(ABORT request) – D=2	
			1CF.117.02	IUT Chaining – Reception of an S(ABORT request) – D=4	
		1CF.118.00	1CF.118.00	Chaining – Error Notification on R-Block	
		1CF.119.0y	1CF.119.00	Chaining – TX Error in response to an R-Block Then I-Block – parity	
			1CF.119.01	Chaining – TX Error in response to an R-Block Then I-Block – EDC	
			1CF.119.02	Chaining – TX Error in response to an R-Block Then I-Block – parity+EDC	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.120.xy	1CF.120.00	Chaining- Syntax Errors in Response to an R-Block Then I-Block – NAD	
			1CF.120.01	Chaining- Syntax Errors in Response to an R-Block Then I-Block - LEN≠0	
			1CF.120.02	Chaining- Syntax Errors in Response to an R-Block Then I-Block - LEN=FF	
			1CF.120.03	Chaining- Syntax Errors in Response to an R-Block Then I-Block - LEN≠01	
			1CF.120.04	Chaining- Syntax Errors in Response to an R-Block Then I-Block - INF<10	
			1CF.120.05	Chaining- Syntax Errors in Response to an R-Block Then I-Block - INF=FF	
			1CF.120.06	Chaining- Syntax Errors in Response to an R-Block Then I-Block - b6=1	
			1CF.120.07	Chaining- Syntax Errors in Response to an R-Block Then I-Block - R-block seq num	
			1CF.120.08	Chaining- Syntax Errors in Response to an R-Block Then I-Block - S-block seq num	
			1CF.120.09	Chaining- Syntax Errors in Response to an R-Block Then I-Block - S(ABORT)	
			1CF.120.10	Chaining- Syntax Errors in Response to an R-Block Then I-Block - S(WTX)	
			1CF.120.11	Chaining- Syntax Errors in Response to an R-Block Then I-Block - S(IFS)	
			1CF.120.12	Chaining- Syntax Errors in Response to an R-Block Then I-Block - S(RESYNC)	
			1CF.120.13	Chaining- Syntax Errors in Response to an R-Block Then I-Block – Unknown	
		1CF.121.0y	1CF.121.00	Chaining in both directions - Error notif. on last block of a chain, 2 TX errors during ICC chaining - parity	
			1CF.121.01	Chaining in both directions - Error notif. on last block of a chain, 2 TX errors during ICC chaining - EDC	
			1CF.121.02	Chaining in both directions - Error notif. on last block of a chain, 2 TX errors during ICC chaining - parity+EDC	
		1CF.122.xy	1CF.122.00	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - NAD	
			1CF.122.01	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - LEN≠0	
			1CF.122.02	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - LEN=FF	
			1CF.122.03	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - LEN≠01	
			1CF.122.04	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - INF=FF	
			1CF.122.05	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - INF<10	
			1CF.122.06	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - b6=1	
			1CF.122.07	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - R-block seq	
			1CF.122.08	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - I-block seq	
			1CF.122.09	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - S(WTX)	
			1CF.122.10	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - S(IFS)	
			1CF.122.11	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - S(RESYNC	
			1CF.122.12	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain - S(ABORT)	
			1CF.122.13	Chaining in both directions - Error notification on last I-Block of a chain, syntax errors in a chain – Unknown	
		1CF.123.00	1CF.123.00	Resynchronization Attempt after excess of Invalid Blocks in Response to an I-Block	

EMV 96	EMV 2000	VISA Test case number	Test case number	Test Title	Status
		1CF.124.xy	1CF.124.00	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=1, BWT=0	
			1CF.124.01	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=1, BWT=1	
			1CF.124.02	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=1, BWT=2	
			1CF.124.03	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=1, BWT=3	
			1CF.124.04	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=1, BWT=4	
	X		1CF.124.10	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=2, BWT=0	
	X		1CF.124.11	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=2, BWT=1	
	X		1CF.124.12	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=2, BWT=2	
	X		1CF.124.13	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=2, BWT=3	
	X		1CF.124.14	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=2, BWT=4	
	X		1CF.124.20	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=3, BWT=0	
	X		1CF.124.21	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=3, BWT=1	
	X		1CF.124.22	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=3, BWT=2	
	X		1CF.124.23	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=3, BWT=3	
	X		1CF.124.24	IUT Chaining - Excess of errors in response to an I-Block with variation of deactivation delay - D=3, BWT=4	
		1CF.125.00	1CF.125.00	IUT Chaining - R-block requesting next chained block and PCB indicating an error	
		1CF.151.00	1CF.151.00	Case 1 Command	
		1CF.152.00	1CF.152.00	Case 2 Command with '6C'	
		1CF.153.00	1CF.153.00	Case 2 Command ----- Error Status Received in Response to Command Header	
		1CF.154.00	1CF.154.00	Case 2 Command with '6C' and with '61'	
		1CF.155.00	1CF.155.00	Case 3 Command---- Normal Processing	
		1CF.156.00	1CF.156.00	Case 4 Command---- Normal Processing	
		1CF.157.00	1CF.157.00	Case 4 Command with '61' Procedure Bytes	
		1CF.158.0y	1CF.158.00	Case 4 Command with Warning following Response Data after the '61' Procedure Byte - SW1SW2=62XX	
			1CF.158.01	Case 4 Command with Warning following Response Data after the '61' Procedure Byte - SW1SW2=63XX	
		1CF.159.0y	1CF.159.00	Case 4 Command Status Bytes Received after Sending the Command Data - warning correct	
			1CF.159.01	Case 4 Command Status Bytes Received after Sending the Command Data - warning not correct	
		1CF.160.0y	1CF.160.00	Case 4 ----Error Indicated following Response Data after the '61' Procedure Byte - SW1SW2=6281	
			1CF.160.01	Case 4 ----Error Indicated following Response Data after the '61' Procedure Byte - SW1SW2=6700	
			1CF.160.02	Case 4 ----Error Indicated following Response Data after the '61' Procedure Byte - SW1SW2=6F00	
			1CF.160.03	Case 4 ----Error Indicated following Response Data after the '61' Procedure Byte - SW1SW2=6A86	
		1CF.161.0y	1CF.161.00	Case 4 -----Warning after Receiving Data and Error after GET RESPONSE - SW1SW2=6281	
			1CF.161.01	Case 4 -----Warning after Receiving Data and Error after GET RESPONSE - SW1SW2=6700	
			1CF.161.02	Case 4 -----Warning after Receiving Data and Error after GET RESPONSE - SW1SW2=6F00	
			1CF.161.03	Case 4 -----Warning after Receiving Data and Error after GET RESPONSE - SW1SW2=6A86	
		1CF.162.0y	1CF.162.00	Case 2 Command ---- Status Returned after Command Header	
			1CF.162.01	Case 3 Command ---- Status Returned after Command Header	
			1CF.162.02	Case 4 Command ---- Status Returned after Command Header	